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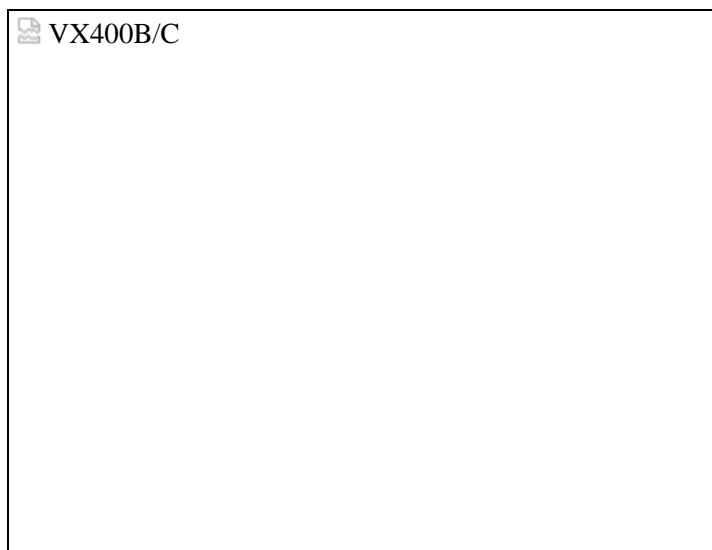
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VX400B/C Prototyping Modules



The VXI B & C sized Prototyping Modules are VXIbus compatible modules with over 22 and 78 square inches of user spaces respectively. All user space holes are plated through for ease of soldering. The 0.1" x 0.1" grid is suitable for use with wire wrap sockets. VCC and ground strips are provided along the top and bottom edges. The onboard circuitry provides the VXI Interface and supplies the user with buffered data and control signals. The standard configuration is an A16:D16 register based slave device. However, the user may upgrade to A32/A24 address offset and D32 data register by adding components to the user space.

Applications

- Prototyping VXIbus designs
- Proof of concept testing
- Design verification

Specifications

- **User Signals Provided:**
 - D00 to D15 - Buffered VXI BiDirectional Data
 - LDTACK - Local DTACK
 - WR-XX - VXI Register Write Gates
 - RD-XX/ - VXI Register Read Gates
 - RST/ - Reset
 - A01 to A03 - Buffered VXI address 01 through 03
 - BDSEL/ - VXI Module Address
 - WR/ - Used for bus control
 - DSO/, DS2 - Buffered VXI Data Strokes
 - MOD00 thru MOD07 - Input module ID number jumper
 - MODID - VXI Buffered MODID
 - All P2 Connector Signals (unbuffered) are available at headers
- **Marking:**
 - All signal names are silkscreened on both the component and solder sides to simplify wiring of development units.
- **Power**
 - +5 @ 0.5a typical
- **Prototyping Area:**

- Over 22 square inches usable layout space for B Size and 78 square inches for C Size.

Ordering Information:

- B Size; Part Number -11026195
- C Size; Part Number - 11026200
- Addition of connectors for "piggy-back" boards (C-Size only); Part Number OPT01

VXIbus Compliance

- Complies with ANSI / IEEE Std. 1014-1987 and VXIbus Rev 1.3
- A16:D16 DTB Slave
- IACKIN tied to IACKOUT
- BRX tied to BGX
- Form Factor: Size B & C



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