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Highlights

- >> **Powerful and Highly Flexible Access Controller**

- >> **Add DSP/CPU Processing via PTMC Sites**

- >> **PICMG® 2.16 or Standard PCI Bus Operation**

- >> **Eight Fully Channelized T1/E1/J1 Spans**

- >> **Optional Support Software:**
 - NexusWare™ Core Linux-based development environment
 - NexusWare C7 SS7 MTP-2 installable software package
 - NexusWare WAN installable software packages

- >> **800 MHz PowerPC® with L1/L2 Cache**

- >> **Built-In TCP Acceleration Hardware**

- >> **Onboard Octal Gi Ethernet Switch**

The IPnexus® CPC308 is a network-optimized, powerful and flexible access controller ideally suited for both PSTN and IP telephony applications. The ability to add scalable DSP/CPU processing via the PTMC (PCI Telecom Mezzanine Card) sites combined with onboard TDM/IP switches make the CPC308 an ideal platform for developers of telephony systems.

Featuring eight fully programmable T1/E1/J1 interfaces, the CPC308 offers a comprehensive design that is ideal for supporting voice, data, signaling and monitoring applications all via one architecture.

The CPC308 is optimized to handle voice circuits for protocol processing or for transfers between the H.110 bus, PCI bus, Ethernet or the onboard PTMC sites. The two PTMC sites provide the ability to connect two single or one dual PTMC module. The PTMC interfaces support 32-bit PCI, H.110 style TDM, RMI and 10/100/1000 Ethernet. Application examples include: wireless infrastructure equipment, media and signaling gateways, IP media equipment, telecom switching/routing equipment and SS7 network elements. The CPC308 offers true TDM/IP bridge capability in addition to supporting full H.110 and PICMG® 2.16 backplanes, framers for external connections and full Gi IP interconnect between major components.

Hardware Features

The CPC308 utilizes a 800 MHz PowerPC® with L1/L2 cache and built-in TAH (TCP acceleration hardware). Using the onboard TDM edge processor, the CPC308 can support up to 256 HDLC channels. The CPC308 fully complies with the latest PICMG hot-swap specification, and its fully passive RTMs also improve reliability. By incorporating the H.110 interface device, the CPC308 can send/receive any of its possible time slots to the J4 connector.

Extensive Software Support

NexusWare™ Core

The CPC308 is available with NexusWare Core, Performance Technologies' Linux-based, development, integration and management environment. NexusWare Core features an integrated suite of carrier-grade device drivers for full access to onboard resources (H.110, Ethernet, PCI) as well as a powerful and easy to use application programming interface (API). NexusWare Core provides integrated drivers for communicating over TCP/IP and is capable of operating within any host system supporting TCP/IP - without the need for bus-based device drivers. Cross development support for Linux, Windows and Solaris platforms is included.



IPnexus®

CPC308

Eight-Port T1/E1/J1 TDM/IP Edge Processor

Ordering Information

*RoHS-Compliant

- >> **PT-CPC308-11975***
Eight-port T1/E1/J1 advanced subsystem
- >> **PT-RTM308-11966***
Eight-port RTM (eight RJ48C connectors)
- >> **PT-ACC324-11977***
CPC324/CPC308/CPC300 console cable

Contact Information

Performance Technologies

205 Indigo Creek Dr.
Rochester, NY 14626
Tel: 585-256-0200
Fax: 585-256-0791
E-mail: sales@pt.com

www.pt.com

Specifications

Interface

- Eight T1 ports @ 1.544 Mbps
- Eight E1 ports @ 2.048 Mbps
- Eight J1 ports @ 1.544 Mbps
- Three 10/100/1000 Ethernet ports

Processor

- 800 MHz PowerPC processor
- 64-bit data and 32-bit address bus
- Built-in TAH capability

Framing Standards

- AMI/B8ZS
- D-4, ESF
- DS-1, PRI

Memory

- 512MB dedicated DDR SDRAM
- 32KB/32KB instruction/data L1 cache
- 256KB L2 cache and/or high speed packet/code store
- 128MB flash PROM

Specification Compliance

- ECTF H.110 hardware compatibility specification: CT Bus revision 1.0
- I2C Bus specification, version 2.1
- IEEE P1386 2.4a March 21, 2001 common mezzanine card family
- IEEE P1386.1 Draft 2.4 Jan 12, 2001 PCI mezzanine cards, PMC
- IEEE Std. 802.3 - 2000 edition CSMA/CD access method and physical layer specification
- IPMI intelligent platform management specification, version 1.5
- JEDEC JESD79C Double Data Rate (DDR) SDRAM specification
- PCI Local Bus specification revision 2.2
- PICMG 2.0 R 3.0 CompactPCI® core specification
- PICMG 2.1 R2.0 hot-swap specification
- PICMG 2.9 R1.0 CompactPCI system management specification
- PICMG 2.15 R1.0: ECN 2.15 PCI telecom mezzanine/carrier card specification
- PICMG 2.16 R1.0 CompactPCI packet-switched backplane
- VITA 32-199x processor PMC standard draft 0.41-compatible

Physical Interface

- T1/E1/J1: Eight T1/E1/J1 port connections via eight RJ48C connectors on the RTM
- Ethernet: Two RJ-45 connectors on RTM, one RJ-45 on front panel, two PICMG 2.16 fabric connections
- Monitor: One nine-pin subminiature on front panel

Protocol Support

- MTP2 Link Layer
- Transparent (Voice Stream)
- HDLC
 - LAPD (Q.921), LAPB, LAPF
- X.25
- Frame Relay

Power

- 22.9 W maximum
 - 3.3 V supply 11.9 W max.
 - 5V supply 10W max.
 - +12V supply 60mW max.
 - 12V supply 40mW max.

Dimensions

- 6U Eurocard form factor

Temperature

- Operating: 0° to 50° C (32° to 122° F)
- Non-operating: -20° to 80° C (-4° to 176° F)

Agency Certifications (pending)

- FCC Class A
- CE
- UL 60950 / EN 60950
- ETSI EN 300 386
- Designed to meet the requirements of NEBS Level 3



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