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XDS560™ JTAG Emulator

JTAG Emulator Supporting RTDX™ Data Link

Get more information about the XDS560 Emulator at: **www.ti.com/emulator**

Features and Benefits

- **Increase productivity through faster start up for larger applications** – Code download speeds of up to more than half a Mbps
- **Quickly find and fix intermittent real-time problems** – Real-time, non-intrusive breakpoint and action point capabilities via Advanced Event Triggering
- **Preserve existing emulation investment** – Upward compatible with XDS510™ Emulators
- **Speed time-to-market with real-time visibility** – High-speed RTDX with real-time data transfer rates of more than 2 Mbps

TI offers a variety of emulator adapters to support the various emulator header standards and to accommodate processor voltage range operation.

Target Board Header	Emulator Connection	Voltage Translation	Part #
14 TI	14 TI	Yes	TMDSADP1414
	20 ARM	No	TMDSADPEMU-20A
20 ARM	20 ARM	No	TMDSADPEMU-20A
60 TI	14 TI	No	TMDSADP1460
20 TI	14 TI	No	TMDSADPEMU-20T
(CTI)		Yes	TMDSADP1420
	20 ARM	No	TMDSADPEMU-20T

JTAG Emulator with High-Speed Real-Time Data Exchange (RTDX): XDS560 Emulator

The XDS560 JTAG Emulator provides an unparalleled level of real-time visibility into executing applications to assist developers in debugging and monitoring real-time systems. The XDS560 Emulator can achieve code download speeds of up to more than half a megabyte per second (500 Kbps), as much as eight times faster than XDS510-class technology, for better loading times of larger applications, speeding development. High-speed RTDX bandwidth enables visibility into a new range of high-bandwidth applications, as well as multi-channel or multiprocessing versions of lower-bandwidth applications. The XDS560 Emulator also enables Advanced Event Triggering to define and evaluate complex sequences of events before halting the CPU, or taking another action, including:

- Setting hardware breakpoints and watchpoints
  - For example, halt the CPU upon access to specific program or data address or address ranges
- Counting many kinds of events such as CPU processor stalls and cache events
- Detecting very precise debugging sequences such as a 16-bit write to a data address with a specific value

The XDS560 Emulator is fully upward compatible with TI's existing XDS510 Emulator. XDS510-class Emulator customers can move from the XDS510 Emulator to using the XDS560 Emulator with very little apparent change, using the same target boards with the existing 14-pin JTAG connectors. The XDS560 Emulator features are a superset of the XDS510 Emulator, but include all of the same XDS510-class functions that TI developers expect, with significantly improved performance.

The XDS560 Emulator is available in Ethernet, PCI and USB interfaces offering high performance and ample bandwidth to support high-speed RTDX data rates. It includes a longer and highly flexible

5-1/2 foot (1.5+ m) host-target cable suitable for use from 5 V down to 0.5 V to

XDS560 Emulator with highly flexible cable and credit card-sized pod is available in the eStore.

support TI's low-voltage device roadmap. The XDS560 Emulator is based on TI's TMS320C6202 DSP, for additional processing power to help drive the improved performance. It is well suited for space-constrained environments, as the cable can reach into tight places between rack-mounted target boards, and the cable/pod assembly is so light it can even hang from the 14-pin JTAG connector without straining it.

For more information on the XDS560 Emulator, visit **www.ti.com/emulator**

XDS560-class JTAG Emulator products are also available from many TI third parties featuring different communication interfaces such as Ethernet and USB, as well as other features. See the TI Third Party on-line catalog and search for "XDS560."

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