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ES1310.1

D/A Board

User's Guide

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Document QH131001 R1.0.5 EN

TTN F 00K 102 868

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1 Introduction

This section contains information about the basic features and applications of the ES1310.1 D/A Board.

Note

*Some components of the ES1310.1 D/A Board may be damaged or destroyed by electrostatic discharges. Please keep the D/A Board in its storage package until it is installed.
The D/A Board should only be taken from its package, configured and installed at a work place that is protected against static discharge.*

1.1 Features

The ES1310.1 D/A Board has eight D/A converters and a VMEbus interface.

The board has the following features:

- 8 analog outputs
- dc decoupling of the analog outputs from the VMEbus
- common ground of the analog outputs
- Lemo connector for EMC-proof connection to external analog measuring systems
- extended temperature range for use in vehicles
- cable with conversion from Lemo to BNC connectors is part of the delivery package

The following figure shows the front panel of the ES1310.1 D/A Board.

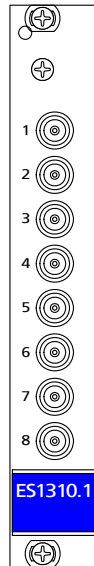


Fig. 1-1 Front Panel of the ES1310.1 D/A Board

1.2 Applications

The ES1310.1 D/A Board is used for the analog output of data, e.g. for control and simulation tasks. The data to be issued is transferred from the VMEbus, converted on the ES1310.1 D/A Board and then output as analog data.

1.3 Block Diagram

The following figure shows you a block diagram of the ES1310.1 D/A Board.

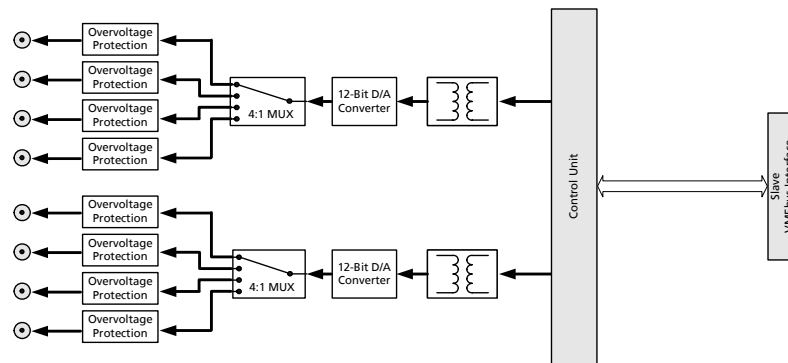


Fig. 1-2 Block Diagram

On the right you can see the VMEbus and how the signals are routed via the VME interface to the control logic or to the D/A converters after dc decoupling. On the left, the signals are converted in the D/A converters DAC 1 and DAC 2 and are made available via eight analog outputs.

1.4 Software Support

In one VME bus system, multiple cards of this type can be used. ASCET-SD, equipped with Target Integration Package Experimental (TIPEXP), and INCA support up to three ES1310.1 cards.

Note

The ES1310.1 card is not multi-master capable. This means that a given card can only be accessed by either the application or the simulation node. Hence, the simultaneous use of one card by INCA (measurement) and ASCET-SD (RTIO/simulation) is impossible.

1.5 Placement

The ES1310.1 cards have to be inserted to the right of the bus master card.

8 Introduction

2 Hardware

This section contains a detailed description of the functions, information on the hardware configuration and the connectors and the technical data of the ES1310.1 D/A Board.

2.1 Functional Description

This section provides you with a detailed overview of the features of the ES1310.1 D/A Board. It contains information on the following aspects:

- connectors
- outputs
- board ID and status

2.1.1 Connectors

The eight Lemo connectors on the front panel are designed as sockets and are used to issue the output signals.

The middle pin routes the output signal, the ground is on the connector housing.



Fig. 2-1 Lemo Connector

2.1.2 Outputs

All eight analog outputs have a common ground and are dc decoupled from the VMEbus ground. The insulation voltage is 500 V.

2.1.3 Board ID

The board has an ID which is used to identify the type of board in the VMEbus system. The ID can be queried under the offset 0x20. The ID is 0xA5.

2.2 Hardware Configuration

This section describes the base address configuration of the board.

The base address of the board is determined with jumpers J400 to J407. A jumper that is plugged in is a logical 1; an open jumper is a logical 0. The board has an address range of 1 KB. The following table shows you the assignment of the jumpers to the address lines.

Jumper	Address Line
J400	A16
J401	A17
J402	A18
J403	A19
J404	A20
J405	A21
J406	A22
J407	A23

Tab. 2-1 Base address configuration

The address lines A15 and A14 have to be 0 for addressing the board. The address lines A13 to A7 and A0 are not evaluated.

The following figure shows you the position of the jumpers.

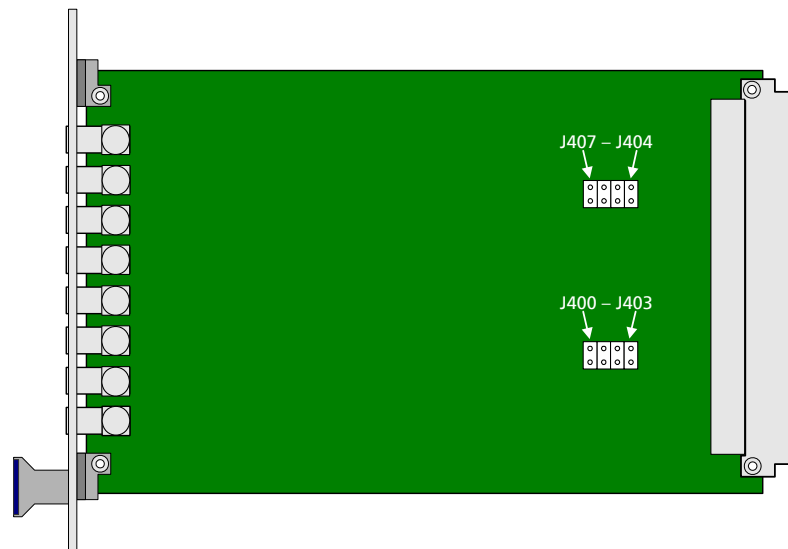


Fig. 2-2 Position of the Jumpers

2.3 Addressing the D/A Converters

The individual channels of the D/A converters are addressed by an offset to the base address.

The following tables show the offsets for the individual channels. Only 16-bit write accesses are allowed to these addresses.

D/A Converter	Channel	Offset
0	1	0x00
0	2	0x02
0	3	0x04
0	4	0x06
1	5	0x08
1	6	0x0A
1	7	0x0C
1	8	0x0E

Tab. 2-2 D/A converter channel offsets

2.4 Offset for Data Transfer, Board ID and Reset

The output data for the D/A converters is only transferred from the internal buffers to the D/A converters after the address with the offset 0x10 is write-accessed. This makes it possible to change all eight analog values synchronously.

The output signals of all D/A converters are reset to zero with a write access to the address with the offset 0x30.

The board ID can be determined by read-accessing the address with the offset 0x20.

The following table shows the offsets of the control addresses.

Function	Offset
Transfer D/A register – output	0x10
Status address: status query	0x20
Reset address: D/A reset (0 V)	0x30

Tab. 2-3 Control address offsets

2.5 Operating Multiple ES1310.1 D/A Boards

It is possible to operate up to three ES1310.1 D/A Boards in a development and experimenting environment. They have to be configured using the jumpers J400-J407. The following table shows the position of the jumpers.

Jumper	Card #1 base address E10000 (default)	Card #2 base address E20000	Card #3 base address E30000
J400	Closed	Open	Closed
J401	Open	Closed	Closed
J402	Open	Open	Open
J403	Open	Open	Open
J404	Open	Open	Open
J405	Closed	Closed	Closed
J406	Closed	Closed	Closed
J407	Closed	Closed	Closed

Tab. 2-4 Jumper assignment when operating multiple boards

The following figure shows which jumpers are open and closed on the relevant board.

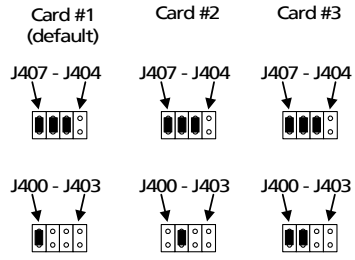


Fig. 2-3 Jumper assignment when operating multiple boards

2.6 Technical Data

This section contains the technical data of the ES1310.1 D/A Board in tabular form.

Electrical Data of the Outputs

Output voltage	0 to 10 V
Output current	4 mA max.
Output resistance	50 Ω

Electrical Data

Resolution	12-bit
Number of channels	8
Overvoltage protection	+/- 25 V
Dc decoupling between VMEbus and D/A output	500 V DC
Accuracy	+/- 2 LSB
Setup time	25 μ s, 0.01% max. area
Output rate of the analog values between two consecutive values	10 μ s
Output	5 W max.

Environmental Conditions

Temperature range	-40 $^{\circ}$ C to +85 $^{\circ}$ C
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Physical Dimensions

Board:	
Length	160 mm
Width	100 mm
Front panel:	
Height	3 U
Width	4 HP

3 **ETAS Contact Addresses**

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