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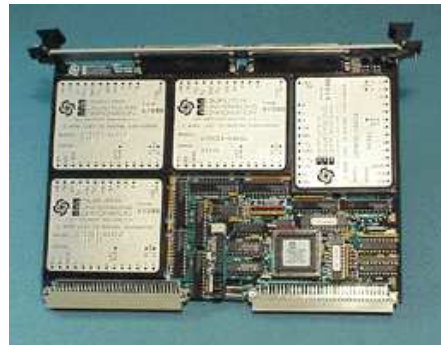
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VME Bus

- *Synchro, Resolver and LVDT Converters*
- Mix-Match any 4/8 Converters/Card
- Transformer Isolated I/O Options
- Loop-Back Testing Cards Available
- Proven "Industry Standard Converters"
- Reliable Register Based "LSI" Logic
- Quadrature Incremental Encoder Outputs
- On-Board Reference Supplies



Overview:

The "VB Series" products line contains VME compatible synchro / resolver converters and absolute encoder systems. These particular cards are ideal for any rotary motion and related process, control, or simulator type application with any VME compatible system.

The *VB Series* provide up to **eight channels** of resolver or synchro conversion on a single-width, standard 6U height VME card.

The *VBE Series* Resolver/Encoder systems feature up to 6 axis of shaft angle position encoding with a choice of single or multiturn resolvers used as the sensor inputs.

CCC's full line of "Industry Standard" Synchro/Resolver converters, and Absolute Encoder products, are used to populate standard multifunction VME decoder cards. Differing converters may be mixed to minimize real estate for a particular application.

Both *industrial and military grade* (extended) temperature range versions are available. Accuracy applies over the operating temperature range, and 883 level B processing is available on all units.

Transformer isolation is offered for all inputs and outputs, eliminating concerns for ground loops, differing potentials and high voltage field transients affecting the card itself and the VME Bus backplane.

Maximum versatility has been employed on all "VB" products to assure universal compatibility in addressing, timing, system, and microcomputer independence.

All VB Series converter cards are configured as *A24:D16 DTB Slaves*. They will respond to address modifier codes "3D" or 39 for standard addressing, and "2D" or "29" when selected for short I/O type addressing.

The VME interface is a very straight-forward register based design; simply address the channel and read or write the data.

Status registers are used to provide card configuration data, and on a per channel basis, to provide channel configuration and fault status.

The high speed register based interface allows these cards to operate reliably in any software environment, with uninhibited *Real-Time* performance.

Buffered latches are provided on all data lines to assure stable read and write cycles as commanded by the host. Address and control lines feature single point terminations to minimize any loading of the backplane. All signal output converters are provided with *inherent read-back* ability.



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