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Insert the Basecard in the Chassis

Ensure that the chassis power is turned off before inserting the card.

SVME-179

An SVME basecard is equipped with a faceplate compliant with IEEE 1101.10. The large ejectors on this faceplate facilitate insertion of the basecard into the 160-contact, 5-row connectors for P1 and P2.

If the faceplate is not compatible with your chassis, you can purchase an alternate faceplate from DY 4 (FPL-179-000). Use the basecard generic drawing (included on the SVME/DMV-179 Technical Documentation CD-ROM) to determine how to mount the faceplate on the card.

With the alternate faceplate, you must use a significant amount of insertion force to mate the backplane connectors with the VME backplane. Use extra care when aligning and inserting your basecard into your chassis, to ensure that a secure mechanical and electrical connection is made between the card and the backplane mating connectors.

Once the basecard is inserted in the chassis, secure it by tightening the screws at the top and bottom of the faceplate.

DMV-179

With DMV basecards, you must use a significant amount of insertion force to mate the backplane connectors with the VME backplane. Use extra care when aligning and inserting your basecard into your chassis, to ensure that a secure mechanical and electrical connection is made between the card and the backplane mating connectors.

Once the basecard is inserted in the chassis, use a torque driver/wrench to tighten the wedgelocks at the top and bottom of the card. The required torque is 6 in-lbs.

Connecting a Terminal

In order to access the features available within the embedded firmware on the SVME/DMV-179, you'll need to attach a terminal or PC-emulated equivalent to the Serial Channel 1 interface on the card. You can connect a terminal to the SVME/DMV-179 in one of the following ways:

- via the front panel serial port J9 connector using a serial cable such as CBL-179-001 (for the SVME-179 version product only);
- via the P2 connector using a P2 interface cable such as CBL-179-003, available from DY 4 Systems (for both SVME and DMV-179 version products);
- via the P0 connector using a P0 interface cable such as the CBL-179-002, (for both SVME and DMV-179 version products).

Default serial communication parameters are 9600 N, 8, 1 (9600 baud, no parity, 8 bits, 1 stop bit).



Ordering Cables

The cable assemblies described in Table 2.6 on page 2-10 are available from DY 4 Systems by ordering individual cables by their part numbers. Should you need to develop your own cabling solution, please refer to Chapter 2 of the Hardware User’s Manual for complete interface pinout listings for the J9, P0, and P2 interfaces.

Serial Communications via the Front Panel J9 Connector



The SVME-179 has a single 31-pin connector on the front panel that provides access to two EIA-232 serial channels (1 and 2), an Ethernet 10BaseT or 100BaseT interface, plus a COP/JTAG interface for debugging purposes. The front panel cable (CBL-179-001 or CBL-SBC-FP-000) routes the different interfaces to separate connectors.

CBL-SBC-FP-000 is compatible with both the SVME-179 and the SVME-181. It includes a USB connector (P6) for use with the SVME-181. The USB connector (P6) should **not** be used with the SVME-179 as the USB interface is not present on the SVME-179.

The signal mapping for the cables is shown in Tables 3.1 to 3.4. Figure 3.1 illustrates the CBL-179-001 front panel and cable.

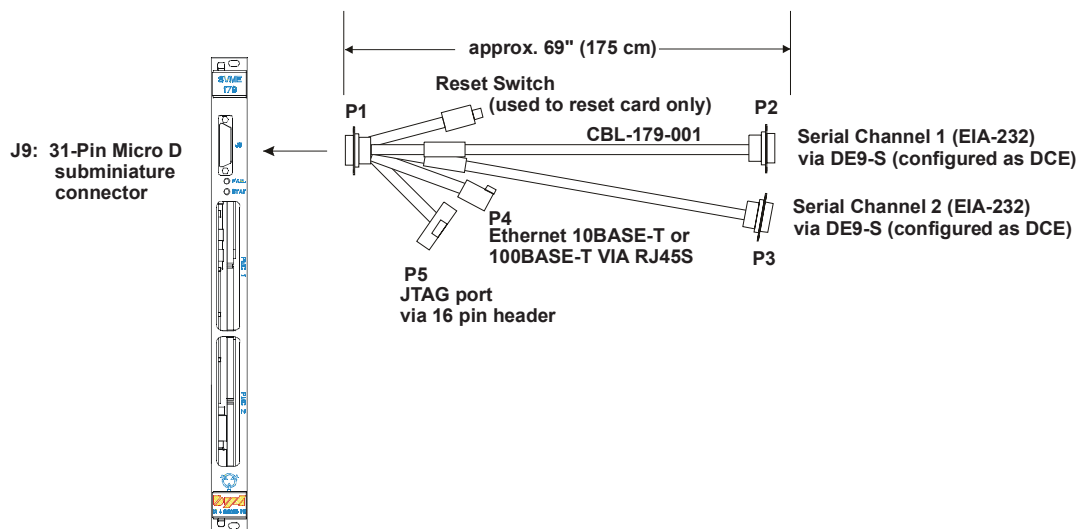


Figure 3.1 SVME-179 Front Panel I/O Cable (CBL-179-001)

Table 3.1: Front Panel Cable P1 to P2 and P3 Signal Mapping

Signal Name	SVME/DMV-179 J9 Connector	Cable P1 Connector	Serial Channel 1 P2 Connector	Serial Channel 2 P3 Connector
GND	J9-8	P1-8	P2-5	P3-5
CH2RTS	J9-9	P1-9		P3-7
CH2CTS	J9-10	P1-10		P3-8
CH2RXD	J9-11	P1-11		P3-3
CH2TXD	J9-12	P1-12		P3-2
CH1DSR	J9-13	P1-13	P2-4	
CH1RXD	J9-14	P1-14	P2-3	
CH1DCD	J9-15	P1-15	P2-1	
CH1TXD	J9-16	P1-16	P2-2	

**Note**

As a minimum, three EIA-232 pins (TXDA, RXDA and GND) must be connected if software handshaking (XON, XOFF) is used. If software handshaking is not used, CTS and RTS must be connected.

**Cross Reference**

The CH1DSR signal is used to control the power-up sequence. See “Controlling the Power-Up Sequence” on page 3-20 for more information.

Table 3.2: Front Panel Cable P1 to P4 Signal Mapping

Signal Name	SVME/DMV-179 J9 Connector	Cable P1 Connector	Ethernet P4 Connector
ENET_RXD-	J9-17	P1-17	P4-6
ENET_RXD+	J9-18	P1-18	P4-3
ENET_TXD+	J9-19	P1-19	P4-1
ENET_TXD-	J9-20	P1-20	P4-2
ENET_UTP2	J9-21	P1-21	P4-7, P4-8
ENET_UTP1	J9-22	P1-22	P4-4, P4-5

Table 3.3: Front Panel Cable P1 to P5 Signal Mapping

Signal Name	SVME/DMV-179 J9 Connector	Front Panel P1 Connector	COP/JTAG P5 Connector
COPS_TDI	J9-1	P1-1	P5-3
COPS_TMS	J9-2	P1-2	P5-9
COPS_TCK	J9-3	P1-3	P5-7
COPS_CKSTP-	J9-4	P1-4	P5-15
COPS_SRST-	J9-5	P1-5	P5-11
COPS_JTRST	J9-6	P1-6	P5-4
COPS_HRST-	J9-7	P1-7	P5-13
GND	J9-8	P1-8	P5-12,16
COPS_PWR	J9-29	P1-29	P5-6
COPS_TDO	J9-31	P1-31	P5-1
COPS_CPU_QACK	J9-23	P1-23	P5-2

Table 3.4: Front Panel Cable P1 to PB1 Signal Mapping

Signal Name	Front Panel P1 Connector	Reset Pushbutton PB1 Switch
GND	P1-8	PB1-1
FP_RST-	P1-24	PB1-2

Serial Communications via the P2 Connector

Connection from the SVME/DMV-179 P2 interface to the terminal is provided by the 179 P2 I/O cable assembly, CBL-179-003.

Install the 179 P2 I/O cable assembly by carefully pressing the cable P1 connector onto the P2 backplane connector stakes. The cable J4 connector is used to attach to a terminal. Other SVME/DMV-179 basecard-related functions are available via this cable, as described below in Figure 3.2.

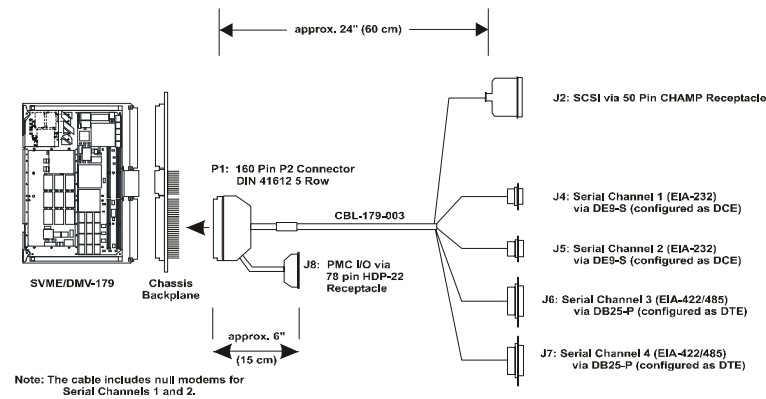


Figure 3.2

SVME/DMV-179 P2 Cable Connections

Table 3.5:

CBL-179-003 P2 I/O Cable Connector Information

Connector	Manufacturer's Part Number	Description
P1	ERNI 024070	Conn., DIN 41612, 5 row
J2	AMP 2-552476-1	Conn., Recept, CHAMP, 50 way
J4, J5	AMP 205203-1	Conn., D-Sub Receptacle DE9-S
J6, J7	AMP 205208-1	Conn., D-Sub Plug DB25-P
J8	AMP 78569-1	Conn., Receptacle HDP-22

**CBL-179-003 J4, J5
(Serial Channels 1, 2)
Connector Pinouts**

The mapping of Serial Channels 1 and 2 EIA-232 signals from the CBL-179-003 P1 connector to the DE9-S connectors (CBL-179-003 J4, J5) is shown in Table 3.6.

Table 3.6: CBL-179-003 P1 to J4, J5 (Serial Channels 1 and 2) Signal Mapping

P1 Pin (From)	J4, J5 Pin (To)	P1 Signal Name	Comments
P1-D17	J4-2	CH1TXD	EIA-232
P1-D18	J4-3	CH1RXD	EIA-232
P1-D19	J4-4	CH1DSR	EIA-232
P1-B31	J4-5	GND	Signal Ground
P1-D30	J5-2	CH2TXD	EIA-232
P1-Z19	J5-3	CH2RXD	EIA-232
P1-B31	J5-5	GND	Signal Ground

CBL-179-003 J6, J7 (Serial Channels 3, 4) Connector Pinouts

The mapping of Serial Channels 3 and 4 EIA-422 signals from the CBL-179-003 P1 connector to the DB25-P connectors (CBL-179-003 J6, J7) is shown in Table 3.7.

Table 3.7: CBL-179-003 P1 to J6, J7 (Serial Channels 3 and 4) Signal Mapping

P1 Pin (From)	J6, J7 Pin (To)	P1 Signal Name	Comments
P1-D11	J6-2	CH3TXD_A	EIA-422
P1-D12	J6-14	CH3TXD_B	EIA-422
P1-D14	J6-3	CH3RXD_A'	EIA-422
P1-D13	J6-16	CH3RXD_B'	EIA-422
P1-D15	J6-15	CH3TXCOUT_A	EIA-422
P1-D16	J6-24	CH3TXCOUT_B	EIA-422
P1-Z21	J6-17	CH3RXC_A'	EIA-422
P1-Z23	J6-25	CH3RXC_B'	EIA-422
P1-B12	J6-7	GND	Signal Ground
P1-D26	J7-2	CH4TXD_A	EIA-422
P1-D27	J7-14	CH4TXD_B	EIA-422
P1-D21	J7-3	CH4RXD_A'	EIA-422
P1-D20	J7-16	CH4RXD_B'	EIA-422
P1-D22	J7-15	CH4TXCOUT_A	EIA-422
P1-D23	J7-24	CH4TXCOUT_B	EIA-422
P1-D25	J7-17	CH4RXC_A'	EIA-422
P1-D24	J7-25	CH4RXC_B'	EIA-422
P1-B22	J7-7	GND	Signal Ground



Note

The EIA-422 TXD and RXD signals on the cable J6 and J7 connectors are **not** on the pins specified by the EIA-530 standard. If you require connectors compliant with EIA-530, build a cable adapter using the information in Table 3.8.

Table 3.8: Signal Mapping from Cable J6 or J7 Connector to EIA-530

Signal Name	Cable J6 or J7 Connector	EIA-530 Standard Connector (without null modem)	EIA-530 Standard Connector (with null modem)
TXD_A	2	2	5
TXD_B	14	14	17
RXD_A'	3	5	2
RXD_B'	16	17	14
GND	7	7	7

CBL-179-003 J2 (SCSI) Connector Pinouts

The mapping of SCSI signals from the CBL-179-003 P1 connector to the 50 Pin CHAMP Receptacle (CBL-179-003 J2 Connector) is shown in Table 3.9:

Table 3.9: CBL-179-003 P1 to J2 (SCSI 50-Pin CHAMP) Signal Mapping

P1 Pin (From)	J2 Pin (To)	Signal Name	P1 Pin (From)	J2 Pin (To)	Signal Name
P1-B2	J2-1	GND	P1-Z1	J2-26	-DB(0)
P1-B2	J2-2	GND	P1-Z3	J2-27	-DB(1)
P1-B2	J2-3	GND	P1-Z5	J2-28	-DB(2)
P1-B2	J2-4	GND	P1-Z7	J2-29	-DB(3)
P1-B2	J2-5	GND	P1-Z9	J2-30	-DB(4)
P1-B2	J2-6	GND	P1-Z11	J2-31	-DB(5)
P1-B2	J2-7	GND	P1-Z13	J2-32	-DB(6)
P1-B2	J2-8	GND	P1-Z15	J2-33	-DB(7)
P1-B2	J2-9	GND	P1-Z17	J2-34	-DB(P)
P1-B2	J2-10	GND	P1-B22	J2-35	GND
P1-B12	J2-11	GND	P1-B22	J2-36	GND
P1-B12	J2-12	RESERVED	P1-B22	J2-37	GND
P1-B12	J2-13	OPEN	P1-D10	J2-38	TERMPWR (fused +5V)
P1-B12	J2-14	RESERVED	P1-B22	J2-39	GND
P1-B12	J2-15	GND	P1-B22	J2-40	GND
P1-B12	J2-16	GND	P1-D1	J2-41	-ATN
P1-B12	J2-17	GND	P1-B22	J2-42	GND
P1-B12	J2-18	GND	P1-D2	J2-43	-BSY
P1-B12	J2-19	GND	P1-D3	J2-44	-ACK
P1-B12	J2-20	GND	P1-D4	J2-45	-RST
P1-B22	J2-21	GND	P1-D5	J2-46	-MSG
P1-B22	J2-22	GND	P1-D6	J2-47	-SEL
P1-B22	J2-23	GND	P1-D7	J2-48	-C/D
P1-B22	J2-24	GND	P1-D8	J2-49	-REQ
P1-B22	J2-25	GND	P1-D9	J2-50	-I/O



The SVME/DMV-179 provides TERMPWR as specified in SCSI-2.

When connecting a SCSI peripheral, make sure to configure the peripheral to accept TERMPWR from the SCSI bus. This is a jumper setting on the SCSI peripheral.

CBL-179-003 J8 (PMC Site 2 I/O) Connector Pinouts

The mapping of PMC I/O signals from the CBL-179-003 P1 connector to the 78 contact HDP-22 receptacle (CBL-179-003 J8) is shown in Table 3.10.

Table 3.10:

CBL-179-003 P1 to J8 (PMC I/O) Signal Mapping

P1 Pin (From)	J8 Pin (To)	Signal Name
P1-C1	J8-9	PMC_01
P1-A1	J8-16	PMC_02
P1-C2	J8-31	PMC_03
P1-A2	J8-17	PMC_04
P1-C3	J8-10	PMC_05
P1-A3	J8-18	PMC_06
P1-C4	J8-3	PMC_07
P1-A4	J8-19	PMC_08
P1-C5	J8-11	PMC_09
P1-A5	J8-20	PMC_10
P1-C6	J8-6	PMC_11
P1-A6	J8-36	PMC_12
P1-C7	J8-12	PMC_13
P1-A7	J8-37	PMC_14
P1-C8	J8-13	PMC_15
P1-A8	J8-1	PMC_16
P1-C9	J8-29	PMC_17
P1-A9	J8-2	PMC_18
P1-C10	J8-14	PMC_19
P1-A10	J8-4	PMC_20
P1-C11	J8-30	PMC_21
P1-A11	J8-21	PMC_22
P1-C12	J8-42	PMC_23
P1-A12	J8-22	PMC_24
P1-C13	J8-77	PMC_25
P1-A13	J8-5	PMC_26
P1-C14	J8-23	PMC_27
P1-A14	J8-24	PMC_28
P1-C15	J8-69	PMC_29
P1-A15	J8-43	PMC_30
P1-C16	J8-45	PMC_31
P1-A16	J8-62	PMC_32
P1-C17	J8-71	PMC_33

Table 3.10: CBL-179-003 P1 to J8 (PMC I/O) Signal Mapping (Continued)

P1 Pin (From)	J8 Pin (To)	Signal Name
P1-A17	J8-38	PMC_34
P1-C18	J8-48	PMC_35
P1-A18	J8-60	PMC_36
P1-C19	J8-70	PMC_37
P1-A19	J8-61	PMC_38
P1-C20	J8-49	PMC_39
P1-A20	J8-44	PMC_40
P1-C21	J8-15	PMC_41
P1-A21	J8-39	PMC_42
P1-C22	J8-50	PMC_43
P1-A22	J8-55	PMC_44
P1-C23	J8-32	PMC_45
P1-A23	J8-56	PMC_46
P1-C24	J8-51	PMC_47
P1-A24	J8-57	PMC_48
P1-C25	J8-74	PMC_49
P1-A25	J8-40	PMC_50
P1-C26	J8-68	PMC_51
P1-A26	J8-41	PMC_52
P1-C27	J8-33	PMC_53
P1-A27	J8-58	PMC_54
P1-C28	J8-78	PMC_55
P1-A28	J8-59	PMC_56
P1-C29	J8-73	PMC_57
P1-A29	J8-75	PMC_58
P1-C30	J8-63	PMC_59
P1-A30	J8-76	PMC_60
P1-C31	J8-64	PMC_61
P1-A31	J8-65	PMC_62
P1-C32	J8-66	PMC_63
P1-A32	J8-67	PMC_64

Expanded I/O via the P0 Connector

Connection with the SVME/DMV-179 P0 interface is provided by the 179 P0 I/O cable assembly, CBL-179-002, DY 4 part number 901434-000.

Install the 179 P0 I/O cable assembly by carefully pressing the cable P1 connector onto the P0 backplane connector stakes. The cable J3 connector is used to attach to a terminal. Other SVME/DMV-179 basecard-related functions are available via this cable, as described below in Figure 3.2.

Exercise care when inserting and extracting the P0 I/O cable as the P0 backplane pins can be easily bent if the cable connector is not properly aligned.



Warning

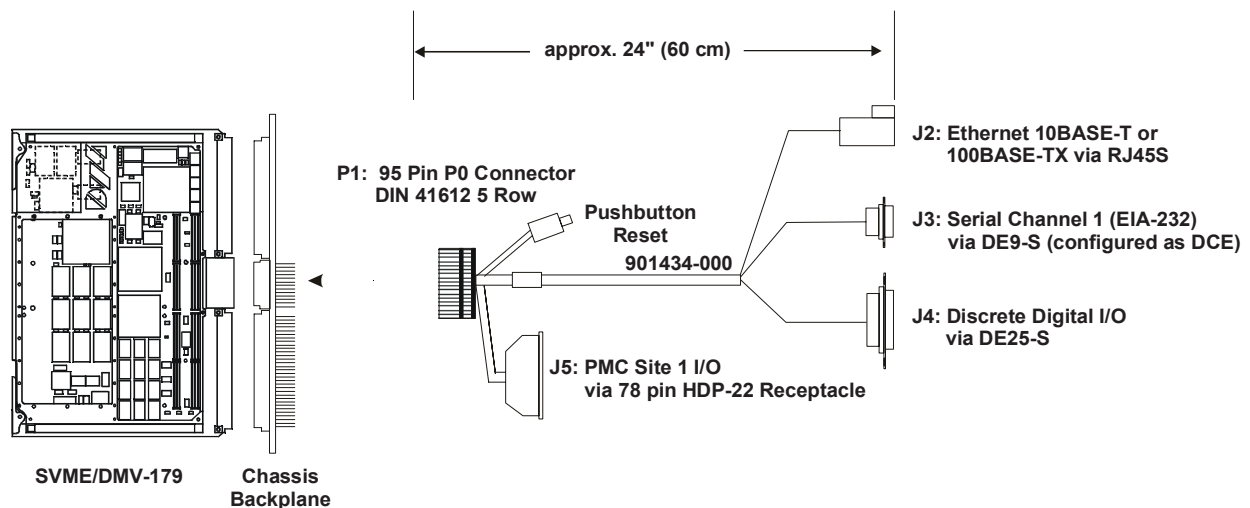


Figure 3.3 Expanded I/O via P0

Table 3.11: CBL-179-002 P0 I/O Cable Connector Information

Connector	Manufacturer's Part Number	Description
P1	AMP 98-3165-087-1	Conn., DIN 41612, 5 row
J2	Panduit CJ5581W	Conn., RJ45S
J3	AMP 205203-1	Conn., D-Sub Receptacle DE9-S
J4	AMP 205207-1	Conn., D-Sub Receptacle DB25-S
J5	AMP 78569-1	Conn., HDP-22 Receptacle 78 pins

CBL-179-002 J2 (Ethernet) Connector Pinout



The mapping of Ethernet signals from the CBL-179-002 P1 connector to the RJ45S connector (CBL-179-002 J2) is shown in Table 3.12.

Note that while the CBL-179-002 cable includes a branch for the Ethernet signals, Ethernet via the P0 connector is not a standard feature of the SVME/DMV-179, and is only provided on a special order basis. Please consult the Product Release Note for your version of the SVME/DMV-179 for further information.

Table 3.12: CBL-179-002 P1 to J2 (Ethernet RJ45S) Signal Mapping

P1 Pin (From)	J2 Pin (To)	Signal Name	Comments
P1-B2	J2-1	ENET_TXD+	Ethernet (IEEE 802.3)
P1-A2	J2-2	ENET_TXD-	Ethernet (IEEE 802.3)
P1-B3	J2-3	ENET_RXD+	Ethernet (IEEE 802.3)
P1-A3	J2-6	ENET_RXD-	Ethernet (IEEE 802.3)
P1-A1	J2-4, 5	ENET_UPT2	Ethernet (IEEE 802.3)
P1-B1	J2-7, 8	ENET_UPT1	Ethernet (IEEE 802.3)

CBL-179-002 J3 (Serial Channel 1) Connector Pinout

The mapping of Serial Channel 1 EIA-232 signals from the CBL-179-002 P1 connector to the DE9-S connector (CBL-179-002 J3) is shown in Table 3.13.

Table 3.13: CBL-179-002 P1 to J3 (Serial Channel 1) Signal Mapping

P1 Pin (From)	J3 Pin (To)	Signal Name	Comments
P1-C1	J3-2	CH1TXD	EIA-232
P1-D1	J3-3	CH1RXD	EIA-232
P1-E1	J3-4	CH1DSR	EIA-232
P1-C2	J3-5	GND	Signal Ground

CBL-179-002 J4 (Parallel I/O) Connector Pinout

The mapping of parallel I/O signals from the CBL-179-002 P1 connector to the DB25-s connector (CBL-179-002 J4) is shown in Table 3.14.

Table 3.14:

CBL-179-002 P1 to J4 (Parallel I/O) Signal Mapping

P1 Pin (From)	J4 Pin (To)	Signal Name
P1-A9	J4-1	PIO(0)
P1-B9	J4-3	PIO(1)
P1-B10	J4-5	PIO(2)
P1-C9	J4-7	PIO(3)
P1-C10	J4-9	PIO(4)
P1-D9	J4-11	PIO(5)
P1-D10	J4-13	PIO(6)
P1-E9	J4-14	PIO(7)
P1-E10	J4-16	PIO(8)
P1-E2	J4-18	PIO(9)
P1-E3	J4-20	PIO(10)
P1-A19	J4-22	PIO(11)
P1-C2	J4-2, 6, 10, 17, 21	GND

CBL-179-002 J5 (PMC Site 1 I/O) Connector Pinout

The mapping of PMC Site 1 I/O signals from the CBL-179-002 P1 connector to the 78-pin PMC Site 1 I/O connector (CBL-179-002 J5) is shown in Table 3.15.

Table 3.15:

CBL-179-002 P1 to J5 (PMC Site 1 I/O) Signal Mapping

P1 Pin (From)	J5 Pin (To)	Signal Name
P1-E4	J5-9	PMC_01
P1-D4	J5-16	PMC_02
P1-C4	J5-31	PMC_03
P1-B4	J5-17	PMC_04
P1-A4	J5-10	PMC_05
P1-E5	J5-18	PMC_06
P1-D5	J5-3	PMC_07
P1-C5	J5-19	PMC_08
P1-B5	J5-11	PMC_09
P1-A5	J5-20	PMC_10
P1-E6	J5-6	PMC_11
P1-D6	J5-36	PMC_12
P1-C6	J5-12	PMC_13

Table 3.15: CBL-179-002 P1 to J5 (PMC Site 1 I/O) Signal Mapping (Continued)

P1 Pin (From)	J5 Pin (To)	Signal Name
P1-B6	J5-37	PMC_14
P1-A6	J5-13	PMC_15
P1-E7	J5-1	PMC_16
P1-D7	J5-29	PMC_17
P1-C7	J5-2	PMC_18
P1-B7	J5-14	PMC_19
P1-A7	J5-4	PMC_20
P1-E8	J5-30	PMC_21
P1-D8	J5-21	PMC_22
P1-C8	J5-42	PMC_23
P1-B8	J5-22	PMC_24
P1-A8	J5-77	PMC_25
P1-E12	J5-5	PMC_26
P1-D12	J5-23	PMC_27
P1-C12	J5-24	PMC_28
P1-B12	J5-69	PMC_29
P1-A12	J5-43	PMC_30
P1-E13	J5-45	PMC_31
P1-D13	J5-62	PMC_32
P1-C13	J5-71	PMC_33
P1-B13	J5-38	PMC_34
P1-A13	J5-48	PMC_35
P1-E14	J5-60	PMC_36
P1-D14	J5-70	PMC_37
P1-C14	J5-61	PMC_38
P1-B14	J5-49	PMC_39
P1-A14	J5-44	PMC_40
P1-E15	J5-15	PMC_41
P1-D15	J5-39	PMC_42
P1-C15	J5-50	PMC_43
P1-B15	J5-55	PMC_44
P1-A15	J5-32	PMC_45
P1-E16	J5-56	PMC_46
P1-D16	J5-51	PMC_47
P1-C16	J5-57	PMC_48
P1-B16	J5-74	PMC_49
P1-A16	J5-40	PMC_50
P1-E17	J5-68	PMC_51

Table 3.15: CBL-179-002 P1 to J5 (PMC Site 1 I/O) Signal Mapping (Continued)

P1 Pin (From)	J5 Pin (To)	Signal Name
P1-D17	J5-41	PMC_52
P1-C17	J5-33	PMC_53
P1-B17	J5-58	PMC_54
P1-A17	J5-78	PMC_55
P1-E18	J5-59	PMC_56
P1-D18	J5-73	PMC_57
P1-C18	J5-75	PMC_58
P1-B18	J5-63	PMC_59
P1-A18	J5-76	PMC_60
P1-E19	J5-64	PMC_61
P1-D19	J5-65	PMC_62
P1-C19	J5-66	PMC_63
P1-B19	J5-67	PMC_64



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