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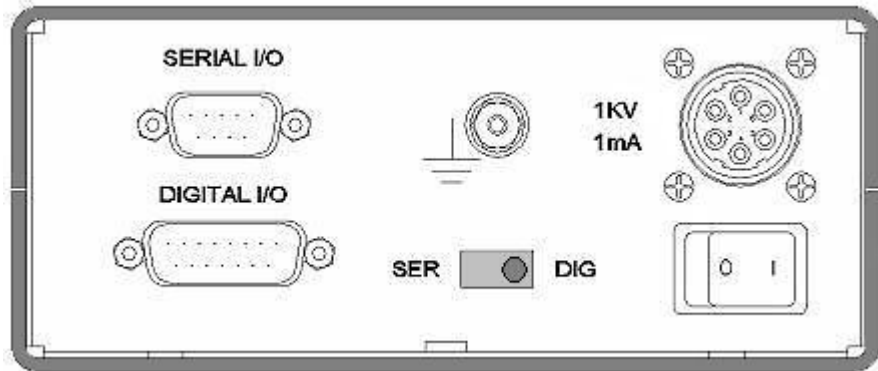
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Gripping Power, Inc.



EPS300 Front Panel

RS-232 Serial Interface. The RS-232 serial interface is a 9-pin, D-style, subminiature female connector, located on the front panel. All serial communications meet RS-232C requirements. The baud rate can be 9600 or 19,200 baud. Data length is 8 bits, no parity and one stop bit is required. Modem control lines, such as RTS and CTS, are not provided.

<u>Pin</u>	<u>Description</u>	<u>Direction</u>
2	Transmit Data	Output
3	Receive Data	Input
5	Common	

Digital/Analog Remote Interface. The digital and analog interface is a 15-pin, D-style subminiature male connector, located on the rear panel. The remote high voltage go on, analog monitors, digital outputs, and program select bits are contained in this connector.

<u>Pin</u>	<u>Description</u>	<u>Direction</u>
1	HV analog setpoint	Input (0-10 VDC)
2	Wafer sense digital output*	Output (500mA, source or sink)
3	Measured HV current output	Output ($\pm 10V$ scale = $\pm 1000\mu A$)
4	Digital return/common	Return for pins 5, 9, 13, 14 and 15
5	Wafer sense enable*	Input (12-24 VDC, opto isolated)
6	Wafer sense analog output*	Output (0-10V)
7	Auxiliary digital output 1	Output (500mA, source or sink)

8	Auxiliary digital output 2	Output (500mA, source or sink)
9	HV go on	Input (12-24 VDC, opto isolated)
10	DC/signal common	Ground/power supply common
11	DC power input	Power (18-30 VDC)
12	Measured HV voltage output	Output ($\pm 10\text{V}$ scale = $\pm 1000\text{VDC}$)
13	Program select bit D0	Input (12-24 VDC, opto isolated)
14	Program select bit D1	Input (12-24 VDC, opto isolated)
15	Program select bit D2	Input (12-24 VDC, opto isolated)

Analog I/O (pins 3 and 12) is configured for 0-10 VDC bipolar operation. Consult the factory for applications that require a 0-1 VDC, 0-5 VDC or unipolar interface.

Pins 4 and 10 are tied together internally. All digital I/O should be referenced to pin 10 and all analog I/O should be referenced to pin 4. An external common connection between pins 4 and 10 will not alter the performance of the EPS300.

* Wafer detection circuitry is optional and is not included in the basic EPS300.

Note: The above specifications are applicable to all EPS300 and EPS300LX power supplies manufactured after July 1, 2002.



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