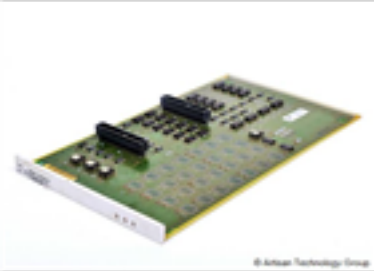
AT&T CPP1

Memory Expansion Module



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INTERFACE CIRCUIT PACKS

Processor Interface TN765 (G3sV2, G3vsV2, and G3iV2)

Provides four data links to interface to the 3B2 Message Server Adjunct (MSA), DCS, ISDN, and AUDIX service. Terminates BX.25 and ISDN (LAPD) protocols. The multicarrier cabinet supports two Processor Interface circuit packs that provide a total of eight links. A single carrier system supports one such circuit pack and provides four links to the processor.

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