# Force Computers CPU-30BE/16 SYS68K Single Board Computer



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TECHNOLOGIES & STANDARDS

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PERFECT FIT

**SYS 68K CPU-30** 

COMPACTPCI

Low to mid-range 68K performance for control applications

PCI/ISA DMCC SYSTEMS **Benefits** 

VME

AVAILABILITY

SERVICES

SOFTWARE

WHITE PAPERS & DATA SHEETS The CPU-30 provides an ideal platform for embedded applications where real-time response is crucial. The CPU-30 is targeted at low- to mid-range industrial applications and allows 68K users to protect their software investments while moving to the newest component technology and guaranteeing longevity of supply. The CPU-30 is supported by a broad and mature range of real-time OS including VxWorks, pSOS+, VRTX-32, OS-9 and LynxOS.



#### Solid performance in a single slot.

The MC68030 is binary compatible to the 680x0 family series and therefore ensures that all the existing 680x0 user software can run on the CPU-30. The CPU-30 is a solid performing, fully featured single board computer for real-time embedded control applications.

#### SPECIFICATIONS >



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## PRODUCTS & SERVICES

VME PRODUCTS

# **SYS 68K CPU-30**

#### **Specifications**

1.

**Processor** 

MC68030 at 25 MHz with MMU

Coprocessor

1.

MC68882 at 25 MHz

3.

Cache

Data and Instruction, each 256 KByte

**DMA Controller** 

1.

On-board via the FGA-002B

5.

**Shared Memory** 

16 MByte DRAM with Byte Parity

6.

System Flash

4 MByte, 32-bit

**Boot Memory** 

Two sockets for up to 512 KByte flash memory devices per socket

2. Standard: 128 KByte

8.

**Local SRAM** 

Standard: 32 KByte

9.

SCSI-2

MB 87034, DMA support, I/O on P2

10.

**Ethernet** 

Am 79C90, 10Base5 on front panel

11.

Floppy Disk

WD 37C65C, I/O routed to P2

12.

```
Serial I/O
        1.
           Two channels via 85C30 SCC, preconfigured as RS-232 RS-422 and RS-485
           as field upgrade option I/O on front panel or P2
13.
    Parallel I/O
       1.
           20-bit available via optional 5-row VMEbus P2 connector
14.
    RTC
        1.
           Interrupt capability, battery back-up
15.
    Counters/Timers
        1.
           Two 24-bit, 500 ns resolution
16.
    Firmware
           VMEPROM installed
    VMEbus Interface
        1.
           32-bit master/slave
               1.
                  Master
                         A32, A24, A16 D32, D16, D8 UAT, RMW
               2.
                  Slave
                      1.
                         A32, D32, D16, D8 UAT, RMW
               3.
                  Interrupt Handler
                         7 VME levels
               4.
                  Arbiter
                      1.
                         Single-level, Arbitration watchdog
               5.
                  Mailbox
                         8 Interrupts
               6.
                  Force Message Broadcast
                         1 FIFO with 8 Byte
                         1 FIFO with 1 Byte
                  P2 Connector
                      1.
                         3-row, option: 5-row
```

18. Additional Features

Reset and abort switches, Status LEDs, Watchdog Timer

#### **Power Consumption**(CPU-30DZBE without Ethernet connection)

+5V 3,0 2,4 A

2. +12V 0,1 A

3. -12V 0,1 A

20.

#### **Environmental Conditions**

**Temperature (Operating)** 

 $0^{\circ}$ C to  $+55^{\circ}$ C

### **Temperature (Storage)**

$$-40^{\circ}$$
C to  $+85^{\circ}$ C

3.

#### Humidity

1.

0% to 95% noncondensing

21.

#### Measurements

1.

6U VMEbus form factor

1 VMEbus slot



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