

EST VisionPROBE

On-Chip Software Debugger for Motorola PowerPC, 68K, and ColdFire Microprocessors



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Limited Availability
Used and in Excellent Condition

Open Web Page

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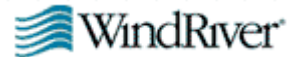
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visionPROBE

The visionPROBE "cable" provides hardware-assisted on-chip software debugging designed specifically for embedded systems software developers and testers.

*I'd like
more information*

Datasheet

Flexible Connection to your target system hardware from UNIX Workstations or Windows PC based platforms; remote operation over LAN is also supported.



Fast Download!
Measured speed of 600 Kbytes/sec (BDM) and 100Kbytes/sec (JTAG). Speed you need* to cut development time!

For stable target systems or software development platforms equipped with ethernet hardware, such as our:

- [Modular Development Platform](#)
- [Single Board Computers](#)

the **new!** [visionWARE](#) utilities can also be used to load memory images.

In very large applications (VLA) the ability to use a TCP/IP network to "blast" a new application image to your target at LAN speed offers a substantial* leap in developer productivity.

* Consider the average number of re-link, load and go cycles that occur daily and the number of engineers performing this task.

Target Hardware Control [BDM/JTAG]:

- start/stop target processor
- set internal hardware and software breakpoints
- take a target snapshot
- reset the target
- step one statement / instruction into function calls
- step over a function call
- step out of a function

RTOS Awareness



visionPROBE is seamlessly integrated with both [Tornado/VxWorks](#) and [pSOSystem](#) providing a hardware-assisted High-Level Language source and machine-language debugging environment

exposing machine state detail at: system, task and function levels.

API support for [proprietary Real-Time Operating Systems](#).

Our [RTOS API kit](#) enables support of proprietary (non-commercial) and unsupported RTOSs by extending [visionCLICK's](#) functionality. It facilitates your ability to obtain detailed information on any RTOS object and automatically display the RTOS object in the visionCLICK [Symbol Explorer Window](#).

PC configuration for non-IDE or non-networked users. As the native user-interface to visionPROBE you are afforded extended capabilities, such as:

Statistical Performance Analysis will [graphically identify](#) system bottlenecks and resource over-consumption. Profile your software's performance and view a symbolic display, either in chart or histogram format. Quickly identify what candidate functions which offer best return-on-investment by continuing algorithm optimization.

Internal Register Configuration Utility -

Automatically configures SIM registers for you and offers a friendly way to quickly make changes. No need to consult a voluminous databook for bit definitions with our extensive on-line help. [View example here.](#)

and Production / Manufacturing / Test department(s). The need to socket Flash or pull and burn new revisions is eliminated.

Built-in Hardware Diagnostics - including a comprehensive suite of RAM tests, bus tests, scope loops, and CRC checks.

Open Test API supports writing custom production test and repair diagnostics.

Processor Support

SILICON VENDOR	PRODUCT	
	visionPROBE	visionPROBE-II
MOTOROLA	PowerPC® 5xx 6xx 7xx 8xx PowerQUICC -I 860 PowerQUICC -II 8240 PowerQUICC -II 8260	PowerPC® 5xx 6xx 7xx 8xx PowerQUICC -I 860 PowerQUICC -II 8240 PowerQUICC -II 8260
	ColdFire® 5202, 5204, 5206, 5206e 5307	ColdFire® 5202, 5204, 5206, 5206e 5307
	CPU-32 68330, 68331, 68332, 68333, 68336, 68338, 68340, 68341, 68349, 68360, 68376, 68396	
IBM	PowerPC® 403GCX, 405GP	PowerPC® 403GCX, 405GP

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