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**Melsec-Output Module**



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# **Intelligent Communication Module**

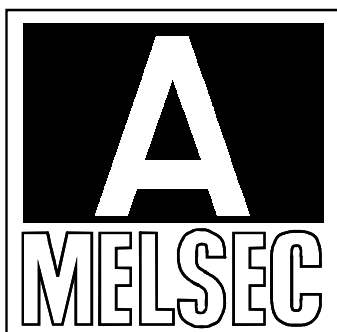
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**User's Manual**  
(Hardware)

**A1SD51S**

Thank you for buying the Mitsubishi general-purpose programmable logic controller MELSEC-A Series

Prior to use, please read both this manual and detailed manual thoroughly and familiarize yourself with the product.



|                           |              |
|---------------------------|--------------|
| MODEL                     | A1SD51S-U-HW |
| MODEL<br>CODE             | 13JP69       |
| IB(NA)-0800311-A(0509)MEE |              |

## ● SAFETY PRECAUTIONS ●

(Always read these instructions before using this equipment.)

Before using this product, please read this manual and the relevant manuals introduced in this manual carefully and pay full attention to safety to handle the product correctly.

These precautions apply only to this product. For the PLC system safety precautions, refer to the user's manual of your CPU module.

In this manual, the safety instructions are ranked as "DANGER" and "CAUTION".



**DANGER**

Indicates that incorrect handling may cause hazardous conditions, resulting in death or severe injury.



**CAUTION**

Indicates that incorrect handling may cause hazardous conditions, resulting in medium or slight personal injury or physical damage.

Note that the **CAUTION** level may lead to a serious consequence according to the circumstances.

Always follow the instructions of both levels because they are important to personal safety.

Please save this manual to make it accessible when required and always forward it to the end user.

### [DESIGN PRECAUTIONS]



**DANGER**

- Please refer to the manual for each data link concerning the operating status of each station when communications errors occur in the data link. There is danger of accidents due to wrong outputs or wrong operations.
- When connecting a peripheral device to the CPU module or performing control of a PLC which is being run through a BASIC program, configure an interlock circuit in the sequence program so that the system overall is operating on the safe side at all times. Also, when performing other control (program modifications, changing the operating status (status control)) of a PLC that is currently running, read the manual thoroughly and proceed only after taking adequate safety precautions. Particularly in cases where the above control is performed toward PLC in remote locations from the opposite party's device, there may be occasions where it is impossible to respond immediately to trouble on the PLC side resulting from data communications errors. At the same time as you are configuring an interlock circuit in the sequence program, decide methods for the system to use to treat data communications errors between the opposite party's device and the PLC CPU.

## [DESIGN PRECAUTIONS]

### **DANGER**

- Do not write data to the "System Area" in the intelligent function module's buffer memory. Also, do not turn the output for signals for "Use Prohibited" signals among the output signals from the PLC CPU to the intelligent function module (ON). If data are written to the "System Area" or output in response to "Use Prohibited" signals, there is danger that the PLC system will malfunction.

### **CAUTION**

- Do not bunch the control wires or communication cables with the main circuit or power wires, or install them close to each other. They should be installed 100mm (3.94 in.) or more from each other. Not doing so could result in noise that may cause malfunction.
- If a BASIC program is registered in the EEPROM in the module, do not perform a power OFF or PLC CPU reset operation at the station where the module is installed during registration. If a power OFF or PLC CPU reset operation is performed at the station where the module is installed during registration, the data contents in the EEPROM will be indefinite and it will be necessary to reset the setting values, etc. in the buffer memory and register them again in the EEPROM. It could also cause the module to break down or malfunction.

## [INSTALLATION PRECAUTIONS]

### **CAUTION**

- Use the PLC in an environment that meets the general specifications contained in the user's manual of the CPU module to use. Using this PLC in an environment outside the range of the general specifications may cause electric shock, fire, malfunction, and damage to or deterioration of the product.
- Insert the module fixing projection into the fixing hole in the base unit to press the module using the hole as the fulcrum, and then tighten the fixing screw with the specified torque.  
When no screw is tightened, even if the module is installed correctly, it may cause malfunctions, a failure or a drop of the module.
- Tighten the screws within the range of specified torque. If the screws are loose, it may cause the module to fallout, short circuits, or malfunction. If the screws are tightened too much, it may cause damage to the screw and/or the module, resulting in fall out, short circuits or malfunction.
- Shut off all phases of the external power supply in the system before mounting or dismounting the module. Failure to do so may damage the product.
- Do not directly touch the conductive area or electronic components of the module. Doing so may cause malfunction or failure in the module.

## [WIRING PRECAUTIONS]

### CAUTION

- If the module is turned on and operated after installation or wiring operations, etc., be sure install the terminal cover provided with this product. If the terminal cover is not installed, there is danger of malfunction.
- When connecting wires to the connector used for external connections, be sure to crimp, pressure weld or solder the wires correctly using the tool specified by the manufacturer.  
If connections are not perfect, it could result in short circuits, fire or malfunction.
- Securely install the connector to the module.
- Be sure to fix communication cables leading from the module by placing them in the duct or clamping them.  
Cables not placed in the duct or without clamping may hang or shift, allowing them to be accidentally pulled, which may cause a module malfunction and cable damage.
- When connecting cables, be sure to do so correctly after confirming the type of interface you are connecting to. If connection is made to a different interface or if wiring is faulty, it could cause the module or external device to break down.
- Tighten the terminal screws within the range of specified torque.  
If the terminal screws are loose, it may result in short circuits or malfunction.  
If the screws are tightened too much, it may cause damage to the screw and/or the module, resulting in the module falling out, short circuits or malfunction.
- When disconnecting the communications cable or power cable that is connected to the module, do not disconnect it by grasping the cable with your hand and pulling it.  
Disconnect cables with connectors attached by taking hold of the connector at the connection with the module and pulling the connector. For cables connected to a terminal block, remove the cable after loosening the terminal block screws.  
If the cable is pulled while it is connected to the module, it could cause malfunction or damage the module or the cable.
- Be careful not to let foreign objects such as swarf or wire chips get inside the module.  
They may cause fires, failure or malfunction.

## **[STARTING AND MAINTENANCE PRECAUTIONS]**

### **CAUTION**

- Do not disassemble or modify the each module.  
Doing so could cause failure, malfunction injury or fire.
- Shut off all phases of the external power supply in the system before mounting or dismounting the module. Failure to do so may cause failure or malfunction of the module.
- Do not touch the connector while the power is on.  
Doing so may cause malfunction.
- Shut off all phases of the external power supply in the system before cleaning or retightening the terminal screws or module fixing screws.  
Not doing so may cause failure or malfunction of the module.  
If the screws are loose, it may cause the module to fallout, short circuits, or malfunction.  
If the screws are tightened too much, it may cause damages to the screws and or/the module, resulting in fall out, short circuits or malfunction.
- Always make sure to touch the grounded metal to discharge the electricity charged in the body, etc., before touching the module.  
Failure to do so may cause a failure or malfunctions of the module.

## **[OPERATING PRECAUTIONS]**

### **CAUTION**

- When performing control (in particular, changing data, changing a program or changing the operation status (status control)) of the PLC during operation using a BASIC program, do so only after reading the user's manual thoroughly and taking adequate safety precautions.  
If there are errors when changing data, changing a program or in status control, it could result in system malfunction, or cause mechanical damage or accidents.

## **[DISPOSAL PRECAUTIONS]**

### **CAUTION**

- When disposing of this product, treat it as industrial waste.

## Revisions

\*The manual number is noted at the lower right of the top cover.

| Print Date | *Manual Number   | Revision       |
|------------|------------------|----------------|
| Sep., 2005 | IB(NA)-0800311-A | First printing |
|            |                  |                |

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### **About the Manuals**

The following product are available for this equipment.  
Refer to the table given below to choose suitable manuals.

#### **Related Manual**

| Manual name                                                                                       | Manual No.<br>(Model code) |
|---------------------------------------------------------------------------------------------------|----------------------------|
| Type A1SD51S Intelligent Communication Module<br>User's Manual                                    | IB-66551<br>(13JE90)       |
| AD51H-BASIC Programming Manual (Command)<br>(For QD51(-R24),A1SD51S,AD51H-S3)                     | SH-080090<br>(13JF63)      |
| AD51H-BASIC Programming Manual (Debug and<br>Compile)<br>(For QD51(-R24),A1SD51S,AD51H-S3)        | SH-080091<br>(13JF64)      |
| AD51H-BASIC Package Type SW1IVD-AD51HP-E<br>(For QD51(-R24),A1SD51S,AD51H-S3) Operating<br>Manual | IB-66698<br>(13J910)       |

### **Conformation to the EMC Directive and Low Voltage Instruction**

For details on making Mitsubishi PLC conform to the EMC directive and low voltage instruction when installing it in your product, please see Chapter 3, "EMC Directive and Low Voltage Instruction" of the User's Manual (Hardware) of the CPU module to use or that of the PLC CPU included with base unit. The CE logo is printed on the rating plate on the main body of the PLC that conforms to the EMC directive and low voltage instruction. By making this product conform to the EMC directive and low voltage instruction, it is not necessary to make those steps individually.

## 1. Overview

This manual explains the specifications and part names of the A1SD51S intelligent communication module that is used in combination with the MELSEC-A series compact building block type PLC CPU.

After unpacking, make sure that the following is included.

| Model   | Product name                             | Quantity |
|---------|------------------------------------------|----------|
| A1SD51S | A1SD51S intelligent communication module | 1        |

## 2. Performance Specifications

### 2.1 Performance Specifications

| Item                               |                | Specification                                                                                                                                                                                                       |
|------------------------------------|----------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Programming language               |                | • AD51H-BASIC                                                                                                                                                                                                       |
| Number of tasks                    |                | • Max. two tasks                                                                                                                                                                                                    |
| Conditions for starting tasks      |                | <ul style="list-style-type: none"> <li>• Started by power ON</li> <li>• Started by an interrupt from the PC CPU (Not started in Compile BASIC)</li> <li>• Started by the start command from another task</li> </ul> |
| Internal memory                    |                | • Program memory: Max. 64 K bytes/two takes (Tasks size can be set to 16K, 32K, 48K or 64K bytes)                                                                                                                   |
|                                    |                | • Common memory: 8 K bytes                                                                                                                                                                                          |
|                                    |                | • Expansion register: 1024 points (2 K bytes)                                                                                                                                                                       |
|                                    |                | • Expansion relay: 1024 points                                                                                                                                                                                      |
| General-purpose I/O                |                | <ul style="list-style-type: none"> <li>• Input: 27 points</li> <li>• Output: 23 points</li> </ul>                                                                                                                   |
| Buffer                             |                | • 3 K words (6 K bytes)                                                                                                                                                                                             |
| Built-in interfaces                | RS-422/485 I/F | • Conforms to RS-422 Channel 3 Used with a connector<br>Transmission distance: within 500m                                                                                                                          |
|                                    | RS-232C I/F    | • Conforms to RS-232C Channels 1 and 2 Used with a connector<br>Transmission distance: within 15m                                                                                                                   |
| Memory backup                      |                | • Backup capability provide (common memory, extension relay, extension register) *1                                                                                                                                 |
| Writing a user program to the ROM  |                | • Disabled (However, a built-in EEPROM is installed in the body of the A1SD51S.) *2                                                                                                                                 |
| Console                            |                | <ul style="list-style-type: none"> <li>• PC/AT</li> <li>• VG-620 (manufactured by Victor Data Systems)</li> <li>• VT-382 (manufactured by the Japan Digital Equipment Inc.)</li> </ul>                              |
| Number of occupied I/O points      |                | • 32 points                                                                                                                                                                                                         |
| 5V DC Internal current consumption |                | • 0.4A                                                                                                                                                                                                              |
| External dimensions                |                | • 130 (5.12)(H)×34 (1.39)(W) ×93.6 (3.69)(D) [mm(in.)]                                                                                                                                                              |
| Weight                             |                | • 0.3 kg (0.66 lb)                                                                                                                                                                                                  |

For general specifications, refer to the user's manual of your CPU module.

#### \*1 Precautions for data backup

When data is backed up using the common memory, extended register (ED) and extended relay (EM), be careful for the following points. When data is cleared at the A1SD51S start-up (SW9 is ON), the installation position is not limited.

##### (1) Module installation position

Install A1SD51S at the final slot of the main base or the extension base. (Make sure that the right side of A1SD51S has no module or blank cover (A1SG60).)

### Note

A1SD51S uses the super-capacitor (capacitor) for backup.

When the super-capacitor is used at a high temperature, the backup time becomes short. To reduce the temperature of the super-capacitor, be sure to observe the installation position.

The super-capacitor has a life span.

When exceeding its life span, replace the super-capacitor.

#### (2) Backup time

MIN. 48 hours (assurance value)

TYP. 120 hours (practical value)

#### (3) Other items

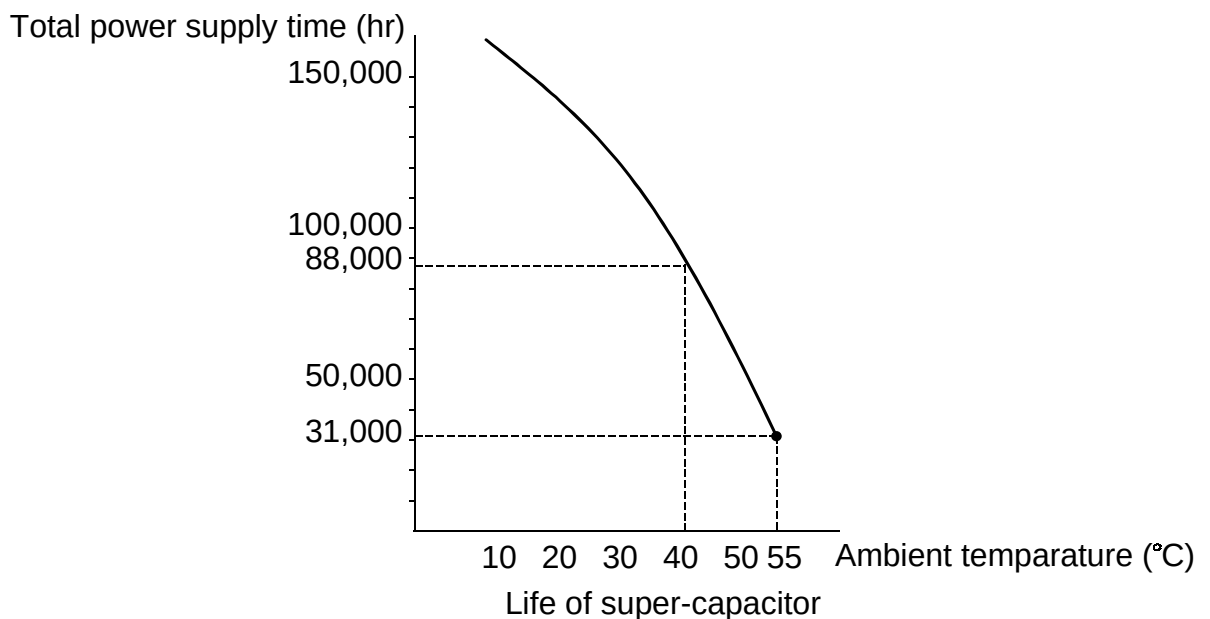
(a) If backup exceeding the backup time is required, backup the data with the PLC CPU.

(A program that transfers data with the PLC CPU is required.)

(b) The charging time of the super-capacitor is one hour.

(When the charging time is less than 1 hour, the backup time is short.)

(c) The life of the super-capacitor is shown in the diagram below. When the super-capacitor exceeds the service life (total power supply time) below, replace the super-capacitor. (When replacing a super-capacitor, please consult your local Mitsubishi service or representative. When requesting replacement, store the data of the common memory, extended register (ED) and extended relay (EM).)



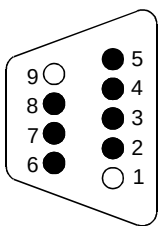
\*2 The EEP-ROM can be rewritten 10000 times.

Rewriting is performed with the following system commands:

- MSAVE
- SET

## 2.2 Connector Specifications

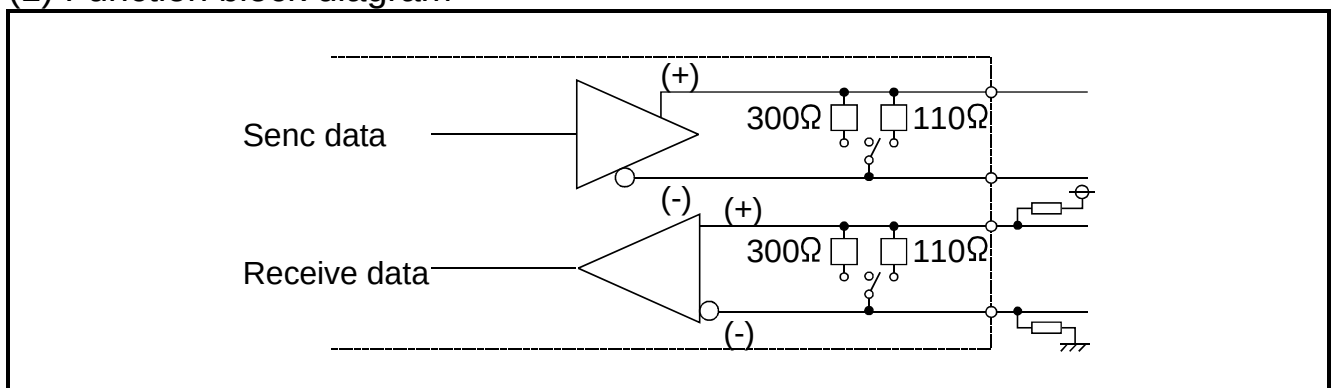
### 2.2.1 RS-232C interface (CH1/CH2)

| Item                                |                                 | Specification                                                                      |         |                     |                     |                                                 |
|-------------------------------------|---------------------------------|------------------------------------------------------------------------------------|---------|---------------------|---------------------|-------------------------------------------------|
| Applicable device                   | Debugger port                   | VG-620, VT-382, PC/AT                                                              |         |                     |                     |                                                 |
|                                     | Console port                    | (Console port is CH1 only.)                                                        |         |                     |                     |                                                 |
|                                     | General -purpose port           | External devices with the RS-232C interface                                        |         |                     |                     |                                                 |
| Transmission method                 |                                 | Conforms to RS-232C                                                                |         |                     |                     |                                                 |
| Synchronization method              |                                 | Asynchronous system                                                                |         |                     |                     |                                                 |
| Transmission specification settings | Transmission speed              | Selectable from 300, 600, 1200, 2400, 4800, 9600 and 19200bps                      |         |                     |                     |                                                 |
|                                     | Parity bit                      | Yes or no (odd parity/even parity)                                                 |         |                     | Selectable          |                                                 |
|                                     | Stop bit                        | 1, 1.5, 2 bits                                                                     |         |                     |                     |                                                 |
|                                     | Character data bit              | 5, 6, 7, 8 bits                                                                    |         |                     |                     |                                                 |
|                                     | Communication control           | DTR/DSR(ER/DR) control or DC code control                                          |         |                     |                     |                                                 |
| Connector of the A1SD51S            | Model                           | 17JE-13090-02(D8A) manufactured by DDK                                             |         |                     |                     |                                                 |
|                                     | Pin arrangement and pin numbers | End view of connector                                                              | Pin No. | Signal abbreviation | Signal name         | Signal direction<br>A1SD51S↔<br>External device |
|                                     |                                 |  | 2       | RD(RXD)             | Receive data        | ←                                               |
|                                     |                                 |                                                                                    | 3       | SD(TXD)             | Send data           | →                                               |
|                                     |                                 |                                                                                    | 4       | ER(DTR)             | Data terminal ready | →                                               |
|                                     |                                 |                                                                                    | 5       | SG                  | Signal ground       | ↔                                               |
|                                     |                                 |                                                                                    | 6       | DR(DSR)             | Dataset ready       | ←                                               |
|                                     |                                 |                                                                                    | 7       | RS(RTS)             | Request to send     | →                                               |
|                                     |                                 |                                                                                    | 8       | CS(CTS)             | Clear to send       | ←                                               |

## 2.2.2 RS-422/485 interface (CH3)

| Item                                |                                 | Specification                                                 |         |                     |             |                                                 |
|-------------------------------------|---------------------------------|---------------------------------------------------------------|---------|---------------------|-------------|-------------------------------------------------|
| Applicable device                   | Debugger port                   | PC/AT                                                         |         |                     |             |                                                 |
|                                     | Console port                    |                                                               |         |                     |             |                                                 |
|                                     | General -purpose port           | External devices with the RS-422/485 interface                |         |                     |             |                                                 |
| Transmission method                 |                                 | Conforms to RS-422                                            |         |                     |             |                                                 |
| Synchronization method              |                                 | Asynchronous system                                           |         |                     |             |                                                 |
| Transmission specification settings | Transmission speed              | Selectable from 300, 600, 1200, 2400, 4800, 9600 and 19200bps |         |                     |             |                                                 |
|                                     | Parity bit                      | Yes or no (odd parity/even parity)                            |         |                     | Selectable  |                                                 |
|                                     | Stop bit                        | 1, 1.5, 2 bits                                                |         |                     |             |                                                 |
|                                     | Character data bit              | 5, 6, 7, 8 bits                                               |         |                     |             |                                                 |
|                                     | Communication control           | CS(CTS) control                                               |         |                     |             |                                                 |
| Connector of the A1SD51S            | Model                           | 17L-10250-27-D9A manufactured by DDK                          |         |                     |             |                                                 |
|                                     | Pin arrangement and pin numbers | End view of connector                                         | Pin No. | Signal abbreviation | Signal name | Signal direction<br>A1SD51S↔<br>External device |
|                                     |                                 |                                                               |         |                     |             |                                                 |

### (1) Function block diagram



## 3. Loading and Installation

### 3.1 Precautions for handling

- (1) Since the module case is made of resin, do not drop it or subject it to shock.
- (2) Do not remove the PC board from the module case. This may cause a fault.
- (3) Take precautions to prevent wire scraps or foreign material from falling into the top of the module.
- (4) Tighten the module mounting screws (usually unnecessary) with a torque in the following range:

| Screw                                                         | Tightening Torque Range |
|---------------------------------------------------------------|-------------------------|
| Module mounting screw (usually unnecessary)<br>(M4×0.7 screw) | 78 to 118N•cm           |

- (5) Load the module to the base with its hooks completely engaged with the base.  
To remove the module, first disengage the hooks from the base, then pull the module toward you.  
(For details, refer to the user's manual for a small building block type CPU.)

### 3.2 Installation conditions

Do not install the A1SD51S in the following locations.

- (1) Locations where the ambient temperature is outside the range 0°C to 55°C.
- (2) Locations where the ambient humidity is outside the range 10% RH to 90% RH.
- (3) Locations where dewing occurs due to sudden temperature changes.
- (4) Locations exposed to corrosive or combustible gases.
- (5) Locations exposed to large amounts of highly conductive dust, iron powder, oil mist, salt or organic solvents.
- (6) Locations where the module is exposed to direct sunlight.
- (7) Locations where a strong electric or magnetic field is generated.
- (8) Locations where the module will be subject to direct vibration or impact.

## 4. External Wiring

### 4.1 RS-232C Connection

The standard method for the RS-232C cable connection is indicated below. For details on the connection method, refer to the user's manual of the A1SD51S intelligent communication module.

(1) External wiring example for DTR/DSR (ER/DR) control or DC code control

| A1SD51S |         | Cable Connection and Signal Direction (Connection Example) | External Device |
|---------|---------|------------------------------------------------------------|-----------------|
| Signal  | Pin No. |                                                            | Signal          |
| RD(RXD) | 2       |                                                            | RD(RXD)         |
| SD(TXD) | 3       |                                                            | SD(TXD)         |
| ER(DTR) | 4       |                                                            | ER(DTR)         |
| SG      | 5       |                                                            | SG              |
| DR(DSR) | 6       |                                                            | DR(DSR)         |
| RS(RTS) | 7       |                                                            | RS(RTS)         |
| CS(CTS) | 8       |                                                            | CS(CTS)         |

For this wiring, enable the DC code control or DTR/DSR (ER/DR) control with Processing Code 18 of the ZCNTL command.

(2) External wiring example for DC code control

| A1SD51S |         | Cable Connection and Signal Direction (Connection Example) | External Device |
|---------|---------|------------------------------------------------------------|-----------------|
| Signal  | Pin No. |                                                            | Signal          |
| RD(RXD) | 2       |                                                            | RD(RXD)         |
| SD(TXD) | 3       |                                                            | SD(TXD)         |
| ER(DTR) | 4       |                                                            | ER(DTR)         |
| SG      | 5       |                                                            | SG              |
| DR(DSR) | 6       |                                                            | DR(DSR)         |
| RS(RTS) | 7       |                                                            | RS(RTS)         |
| CS(CTS) | 8       |                                                            | CS(CTS)         |

For this wiring, enable the DC code control with Processing Code 18 of the ZCNTL command.



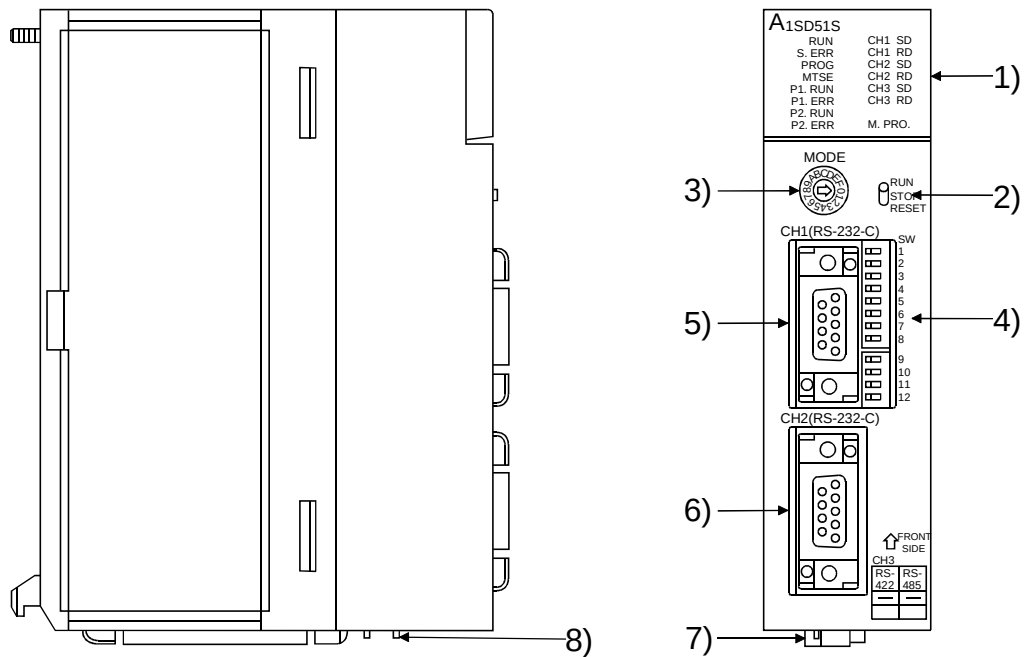
## 4.2 RS-422/485 Connection

The standard method for one-to-one (1:1) connection of the RS-422/485 cable is indicated below.

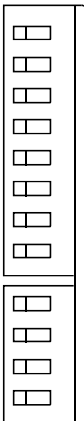
| A1SD51S |         | Cable Connection and Signal Direction (Connection Example) | External Device |
|---------|---------|------------------------------------------------------------|-----------------|
| Signal  | Pin No. |                                                            | Signal          |
| SDA     | 3       |                                                            | RDA             |
| SDB     | 16      |                                                            | RDB             |
| RDA     | 2       |                                                            | SDA             |
| RDB     | 15      |                                                            | SDB             |
| CSA     | 5       |                                                            | RSA             |
| CSB     | 18      |                                                            | RSB             |
| RSA     | 4       |                                                            | CSA             |
| RSB     | 17      |                                                            | CSB             |
| RR      | 21      |                                                            | -               |
| SG      | 20      |                                                            | SG              |

For the terminating resistor setting and an external wiring example for multi-drop (1:n) connection, refer to the user's manual of the A1SD51S intelligent communication module.

## 5. Part Names And Setting

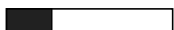
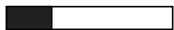
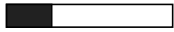
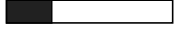
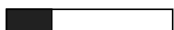
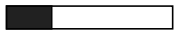
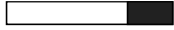
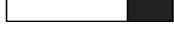
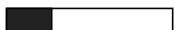
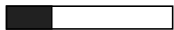
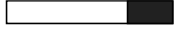
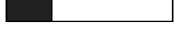
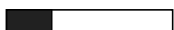
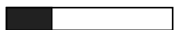
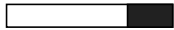
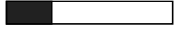
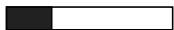
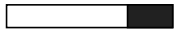
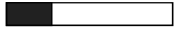
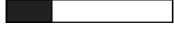


| No. | Name                                                                                                                                                                                                                                                                                                                              | Description |                                                                 |
|-----|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------|-----------------------------------------------------------------|
| (1) | <div> <div>LED</div> <div> <div>RUN</div> <div>S. ERR</div> <div>PROG</div> <div>MTSE</div> <div>P1. RUN</div> <div>P1. ERR</div> <div>P2. RUN</div> <div>P2. ERR</div> </div> <div> <div>CH1 SD</div> <div>CH1 RD</div> <div>CH2 SD</div> <div>CH2 RD</div> <div>CH3 SD</div> <div>CH3 RD</div> <div>M. PRO.</div> </div> </div> | RUN         | Normal operation<br>Normal: ON    Error: OFF                    |
|     |                                                                                                                                                                                                                                                                                                                                   | S.ERR       | System error<br>Normal: OFF    Error: ON                        |
|     |                                                                                                                                                                                                                                                                                                                                   | PROG        | Programming mode<br>In programming mode: ON                     |
|     |                                                                                                                                                                                                                                                                                                                                   | MTSE        | Multitask setting error<br>Setting error occurred: ON           |
|     |                                                                                                                                                                                                                                                                                                                                   | P1.RUN      | Task 1 execution<br>Task 1 being executed: ON                   |
|     |                                                                                                                                                                                                                                                                                                                                   | P1.ERR      | Task 1 error<br>Error occurred in task 1: ON                    |
|     |                                                                                                                                                                                                                                                                                                                                   | P2.RUN      | Task 2 execution<br>Task 2 being executed: ON                   |
|     |                                                                                                                                                                                                                                                                                                                                   | P2.ERR      | Task 2 error<br>Error occurred in task 2: ON                    |
|     |                                                                                                                                                                                                                                                                                                                                   | CH1 SD      | CH1 send status<br>Data being sent: Flashing                    |
|     |                                                                                                                                                                                                                                                                                                                                   | CH1 RD      | CH1 receive status<br>Data being received: Flashing             |
|     |                                                                                                                                                                                                                                                                                                                                   | CH2 SD      | CH2 send status<br>Data being sent: Flashing                    |
|     |                                                                                                                                                                                                                                                                                                                                   | CH2 RD      | CH2 receive status<br>Data being received: Flashing             |
|     |                                                                                                                                                                                                                                                                                                                                   | CH3 SD      | CH3 send status<br>Data being sent: Flashing                    |
|     |                                                                                                                                                                                                                                                                                                                                   | CH3 RD      | CH3 receive status<br>Data being received: Flashing             |
|     |                                                                                                                                                                                                                                                                                                                                   | M.PRO.      | Memory protect status<br>Memory protected (when SW10 is ON): ON |

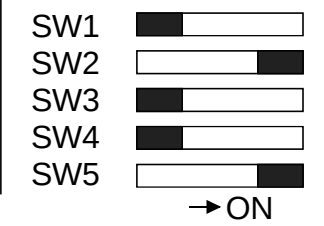
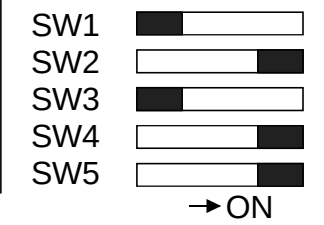
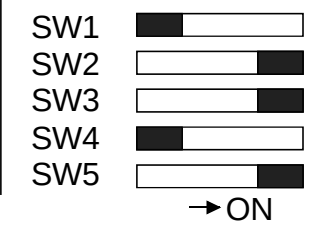
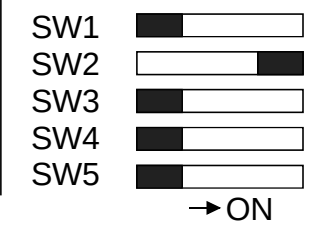
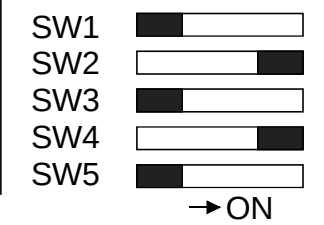
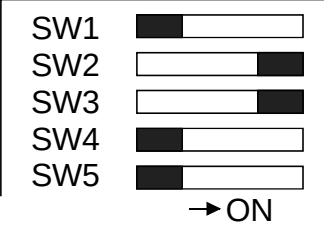
| No.    | Name                                                                                                                                                                                                                       | Description                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |               |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
|--------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------|-------------|--------|----------------|----|-----|--------------------------|-------------------------------|-------------|------------------|--------|-----------------|---|---|---|-----------------------------------------------------------------|---------|----------|---|---------------|---------|----------|---|-------------------------------------|--------|-------|---|-------------------|--------------------|-------------|----|-------------------------------|-----------|---------------|----|----------|---|---|----|
| (2)    | <div>RUN switch</div> <div></div>                                                                                                         | <div>Used to execute/stop tasks or to reset the hardware.</div> <div><div>•RUN : Executes tasks.</div><div>(Enabled only in execution/debugging mode)</div><div>•STOP : Stops tasks.</div><div>(Enabled only in execution/debugging mode)</div><div>•RESET : Resets the hardware.</div></div>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |               |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
| (3)    | <div>Mode setting switch 1</div> <div><div>MODE</div><div></div></div>                                                                    | <div>Mode setting (Factory setting: 0)</div> <table><tr><th>Mode</th><th>Description</th></tr><tr><td>0</td><td rowspan="2">Execution mode</td></tr><tr><td>1</td></tr><tr><td>2</td><td rowspan="2">Multitask debugging mode</td></tr><tr><td>3</td></tr><tr><td>4</td><td>Programming mode</td></tr><tr><td>5 to F</td><td>Use not allowed</td></tr></table>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            | Mode          | Description | 0      | Execution mode | 1  | 2   | Multitask debugging mode | 3                             | 4           | Programming mode | 5 to F | Use not allowed |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
| Mode   | Description                                                                                                                                                                                                                |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |               |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
| 0      | Execution mode                                                                                                                                                                                                             |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |               |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
| 1      |                                                                                                                                                                                                                            |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |               |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
| 2      | Multitask debugging mode                                                                                                                                                                                                   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |               |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
| 3      |                                                                                                                                                                                                                            |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |               |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
| 4      | Programming mode                                                                                                                                                                                                           |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |               |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
| 5 to F | Use not allowed                                                                                                                                                                                                            |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |               |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
| (4)    | <div>Mode setting switch 2</div> <div><div></div><div>1<br/>2<br/>3<br/>4<br/>5<br/>6<br/>7<br/>8<br/>9<br/>10<br/>11<br/>12</div></div> | <div>Consol and other settings</div> <table><tr><th rowspan="2">SW</th><th rowspan="2">Setting</th><th colspan="2">Status</th></tr><tr><th>ON</th><th>OFF</th></tr><tr><td>1</td><td rowspan="5">Console/Debugger port setting</td><td colspan="2" rowspan="5">Refer to *1</td></tr><tr><td>2</td></tr><tr><td>3</td></tr><tr><td>4</td></tr><tr><td>5</td></tr><tr><td>6</td><td>BASIC program stop by pressing [Break] key or [Ctrl] + [C] keys</td><td>Enabled</td><td>Disabled</td></tr><tr><td>7</td><td>PLC CPU reset</td><td>Enabled</td><td>Disabled</td></tr><tr><td>8</td><td>Accessible time after PLC CPU reset</td><td>2000ms</td><td>200ms</td></tr><tr><td>9</td><td>Backup area clear</td><td>Cleared at startup</td><td>Not cleared</td></tr><tr><td>10</td><td>EEP-ROM write protect setting</td><td>Protected</td><td>Not protected</td></tr><tr><td>11</td><td rowspan="2">Not used</td><td rowspan="2">-</td><td rowspan="2">-</td></tr><tr><td>12</td></tr></table> | SW            | Setting     | Status |                | ON | OFF | 1                        | Console/Debugger port setting | Refer to *1 |                  | 2      | 3               | 4 | 5 | 6 | BASIC program stop by pressing [Break] key or [Ctrl] + [C] keys | Enabled | Disabled | 7 | PLC CPU reset | Enabled | Disabled | 8 | Accessible time after PLC CPU reset | 2000ms | 200ms | 9 | Backup area clear | Cleared at startup | Not cleared | 10 | EEP-ROM write protect setting | Protected | Not protected | 11 | Not used | - | - | 12 |
| SW     | Setting                                                                                                                                                                                                                    | Status                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |               |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
|        |                                                                                                                                                                                                                            | ON                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | OFF           |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
| 1      | Console/Debugger port setting                                                                                                                                                                                              | Refer to *1                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |               |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
| 2      |                                                                                                                                                                                                                            |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |               |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
| 3      |                                                                                                                                                                                                                            |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |               |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
| 4      |                                                                                                                                                                                                                            |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |               |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
| 5      |                                                                                                                                                                                                                            |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |               |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
| 6      | BASIC program stop by pressing [Break] key or [Ctrl] + [C] keys                                                                                                                                                            | Enabled                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   | Disabled      |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
| 7      | PLC CPU reset                                                                                                                                                                                                              | Enabled                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   | Disabled      |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
| 8      | Accessible time after PLC CPU reset                                                                                                                                                                                        | 2000ms                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    | 200ms         |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
| 9      | Backup area clear                                                                                                                                                                                                          | Cleared at startup                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | Not cleared   |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
| 10     | EEP-ROM write protect setting                                                                                                                                                                                              | Protected                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 | Not protected |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
| 11     | Not used                                                                                                                                                                                                                   | -                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | -             |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |
| 12     |                                                                                                                                                                                                                            |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |               |             |        |                |    |     |                          |                               |             |                  |        |                 |   |   |   |                                                                 |         |          |   |               |         |          |   |                                     |        |       |   |                   |                    |             |    |                               |           |               |    |          |   |   |    |

# \*1 Consol/debugger port setting

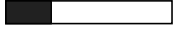
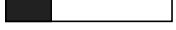
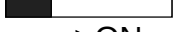
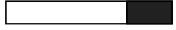
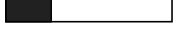
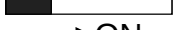
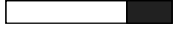
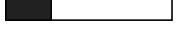
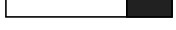
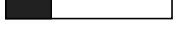
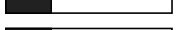
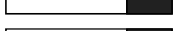
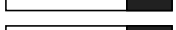
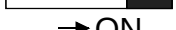
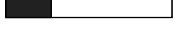
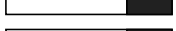
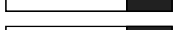
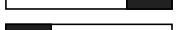
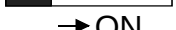
## (1) When connecting console to CH3

| Console                                       | Debugger                                                  | Setting                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
|-----------------------------------------------|-----------------------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| The following is connected to CH3:<br>• PC/AT | Not connected                                             | <div>           SW1  <br/>           SW2  <br/>           SW3  <br/>           SW4  <br/>           SW5  <br/>           → ON         </div>           |
|                                               | The following is connected to CH1:<br>• VT-382<br>• PC/AT | <div>           SW1  <br/>           SW2  <br/>           SW3  <br/>           SW4  <br/>           SW5  <br/>           → ON         </div>           |
|                                               | The following is connected to CH1:<br>• VG-620            | <div>           SW1  <br/>           SW2  <br/>           SW3  <br/>           SW4  <br/>           SW5  <br/>           → ON         </div>           |
|                                               | The following is connected to CH2:<br>• VT-382<br>• PC/AT | <div>           SW1  <br/>           SW2  <br/>           SW3  <br/>           SW4  <br/>           SW5  <br/>           → ON         </div> |
|                                               | The following is connected to CH2:<br>• VG-620            | <div>           SW1  <br/>           SW2  <br/>           SW3  <br/>           SW4  <br/>           SW5  <br/>           → ON         </div> |

## (2) When connecting console to CH1

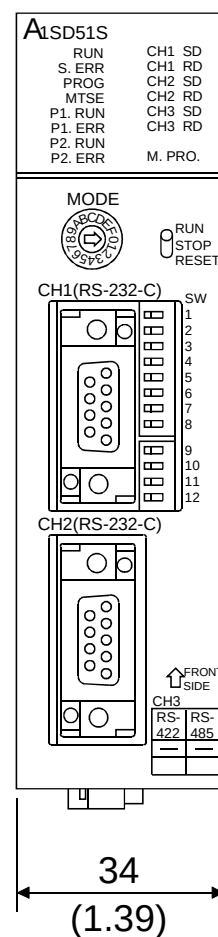
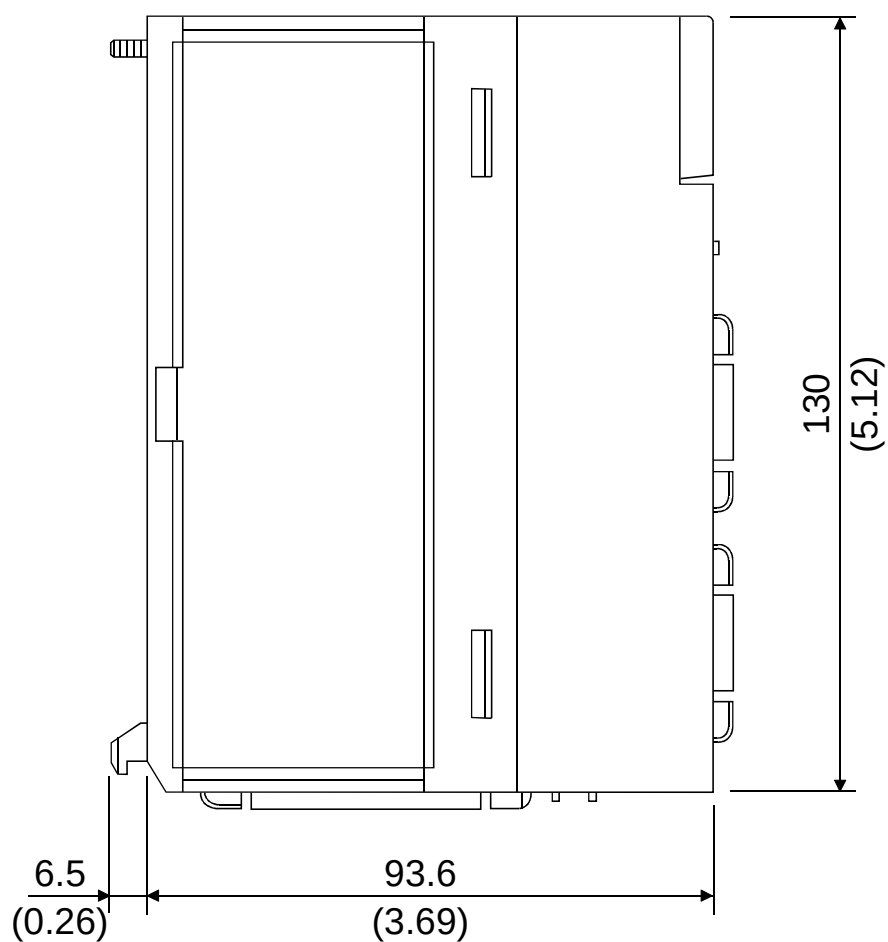
| Consol                                                    | Debugger                                                  | Setting                                                                               |
|-----------------------------------------------------------|-----------------------------------------------------------|---------------------------------------------------------------------------------------|
| The following is connected to CH1:<br>• VT-382<br>• PC/AT | Not connected                                             |    |
|                                                           | The following is connected to CH3:<br>• PC/AT             |    |
|                                                           | The following is connected to CH2:<br>• VT-382<br>• PC/AT |    |
| The following is connected to CH1:<br>• VG-620            | Not connected                                             |   |
|                                                           | The following is connected to CH3:<br>• PC/AT             |  |
|                                                           | The following is connected to CH2:<br>• VG-620            |  |

### (3) When connecting no console

| Console       | Debugger                                                  | Setting                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
|---------------|-----------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Not connected | Not connected                                             | <div> SW1  SW2  SW3  SW4  SW5  <br/>→ ON </div>           |
|               | The following is connected to CH3:<br>• PC/AT             | <div> SW1  SW2  SW3  SW4  SW5  <br/>→ ON </div>           |
|               | The following is connected to CH1:<br>• VT-382<br>• PC/AT | <div> SW1  SW2  SW3  SW4  SW5  <br/>→ ON </div>           |
|               | The following is connected to CH1:<br>• VG-620            | <div> SW1  SW2  SW3  SW4  SW5  <br/>→ ON </div>     |
|               | The following is connected to CH2:<br>• VT-382<br>• PC/AT | <div> SW1  SW2  SW3  SW4  SW5  <br/>→ ON </div> |
|               | The following is connected to CH2:<br>• VG-620            | <div> SW1  SW2  SW3  SW4  SW5  <br/>→ ON </div> |

| No. | Name                                 | Description                                                                                                                                                                                 |
|-----|--------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| (5) | RS-232C interface (CH1)              | • A console, debugger or external device, etc. is connected to this.                                                                                                                        |
| (6) | RS-232C interface (CH2)              | • A debugger or external device, etc. is connected to this.                                                                                                                                 |
| (7) | RS-422 interface (CH3)               | • A console, debugger or external device, etc. is connected to this.                                                                                                                        |
| (8) | Terminating resistor selector switch | <ul style="list-style-type: none"> <li>• Terminating resistor setting for RS-422/485 is selected.</li> <li>RS-422 : 330Ω</li> <li>Neutral : Not connected</li> <li>RS-485 : 110Ω</li> </ul> |

## 6. External Dimensions



Unit:mm (inch)



## Warranty

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