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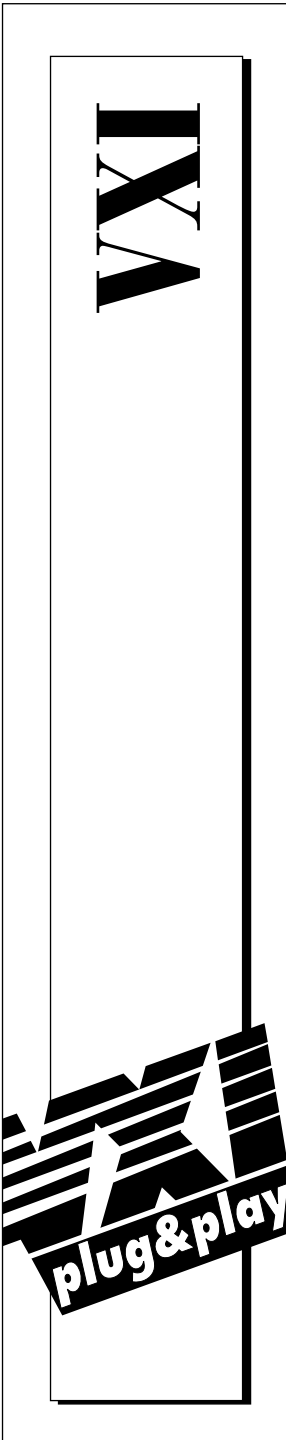
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Getting Started with Your VXIpc™ 800/700 Series

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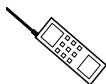
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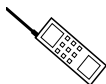
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Federal Communications Commission

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

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If necessary, consult National Instruments or an experienced radio/television technician for additional suggestions. The following booklet prepared by the FCC may also be helpful: *Interference to Home Electronic Entertainment Equipment Handbook*. This booklet is available from the U.S. Government Printing Office, Washington, DC 20402.

Canadian Department of Communications

This Class A digital apparatus meets all requirements of the Canadian Interference-Causing Equipment Regulations.

Cet appareil numérique de la classe A respecte toutes les exigences du Règlement sur le matériel brouilleur du Canada.

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You can use this manual to help you get a quick start with VXIpc 800/700 Series embedded computers and the NI-VXI software. This manual summarizes the setup instructions and default settings for the hardware and software. You may find that these sections contain as much information as you need to get started with your VXIpc 800/700 kit.

Organization of This Manual

This manual is organized as follows:

- Chapter 1, *Introduction*, describes the VXIpc 800/700 Series of embedded VXI computers along with the NI-VXI software, lists what you need to get started, lists optional software, and gives an overview of the directory structure on your hard drive.
- Chapter 2, *Setup*, contains basic instructions for setting up the VXIpc 800/700 and the NI-VXI software.
- Chapter 3, *Default Settings*, summarizes the hardware and software default settings for the VXIpc 800/700 Series kit. Because you can also access the **VXI/VME-MXI-2 Configuration Editor** to configure a VXI-MXI-2 or a VME-MXI-2 through the `VXIedit` program, this chapter also summarizes the software default settings for the VXI/VME-MXI-2.
- The *Customer Communication* appendix contains forms you can use to request help from National Instruments or to comment on our products and manuals.
- The *Glossary* contains an alphabetical list and description of terms used in this manual, including abbreviations, acronyms, metric prefixes, mnemonics, and symbols.
- The *Index* contains an alphabetical list of key terms and topics in this manual, including the page where you can find each one.

Conventions Used in This Manual

	The following conventions are used in this manual:
◆	The ◆ symbol indicates that the text following it applies only to a specific product, a specific operating system, or a specific software version.
bold	Bold text denotes the names of menus, menu items, or dialog box buttons or options.
bold monospace	Bold text in this font denotes the messages and responses that the computer automatically prints to the screen.
<i>italic</i>	Italic text denotes emphasis, a cross reference, or an introduction to a key concept. This font also denotes text for which you supply the appropriate word or value, as in Windows 3.x.
monospace	Text in this font denotes text or characters that are to be literally input from the keyboard, sections of code, programming examples, and syntax examples. This font is also used for the proper names of disk drives, paths, directories, programs, subprograms, subroutines, device names, functions, variables, filenames, and extensions.
VXIpc 700 Series	The terms <i>VXIpc 700 Series</i> and <i>VXIpc-700</i> refer to a series of C-size, single-slot, VXI controllers. Currently, this series consists of the VXIpc-740 and VXIpc-745.
VXIpc 800 Series	The terms <i>VXIpc 800 Series</i> and <i>VXIpc-800</i> refer to a series of C-size, dual-slot, VXI controllers. Currently, this series consists of the VXIpc-850 and VXIpc-860, with various processors and in different speeds.
VXIpc 800/700 Series	The term <i>VXIpc 800/700 Series</i> refers to all models of the VXIpc 800 and VXIpc 700 Series.
	Abbreviations, acronyms, metric prefixes, mnemonics, symbols, and terms are listed in the <i>Glossary</i> .

How to Use This Documentation Set

Begin by reading this manual, *Getting Started with Your VXIpc 800/700 Series*, for basic instructions on setting up the hardware and software. This is a brief quick start manual that describes how to get started with your kit using the default hardware and software settings. Refer to the following manuals for more information about the hardware or software.

The *VXIpc 800/700 Series User Manual* contains more details about changing the hardware installation or configuration from the defaults, and using the hardware.

The *NI-VXI Software Manual for the VXIpc 800/700 Series* contains more details about changing the NI-VXI software installation or configuration from the defaults, and using the NI-VXI software on the VXIpc 800/700. This manual is installed on your hard drive in the `c:\images\manuals` directory. Use the Acrobat Reader program to open the file called `NIVXISM.pdf`.

When you are familiar with the material in the previous manuals, you can begin to use the *NI-VXI User Manual*. This manual presents the concepts of VXI and prepares you for detailed explanations of the NI-VXI functions. Study the descriptions of each function given in the *NI-VXI Programmer Reference Manual* to fully understand the purpose and syntax of each function. These two manuals are also available in the `c:\images\manuals` directory under the names `NIVXIUM.pdf` and `NIVXIIPRM.pdf`, respectively.

You can also access online help for Windows 95/NT/3.1 in the NI-VXI folder.

Refer to the *NI-VXI Graphical Utilities Reference Manual* and the *NI-VXI Text Utilities Reference Manual* to learn more about the NI-VXI utilities.

Related Documentation

The following documents contain information that you may find helpful as you read this manual:

- ANSI/IEEE Standard 1014-1987, *IEEE Standard for a Versatile Backplane Bus: VMEbus*
- ANSI/IEEE Standard 1155-1993, *IEEE VMEbus Extensions for Instrumentation: VXIbus*
- ANSI/VITA 1-1994, *VME64*
- VXI-6, *VXIbus Mainframe Extender Specification*, Rev. 1.0, VXIbus Consortium

Customer Communication

National Instruments wants to receive your comments on our products and manuals. We are interested in the applications you develop with our products, and we want to help if you have problems with them. To make it easy for you to contact us, this manual contains comment and configuration forms for you to complete. These forms are in the *Customer Communication* appendix at the end of this manual.

Introduction

Chapter

1

This chapter describes the VXIpc 800/700 Series of embedded VXI computers along with the NI-VXI software, lists what you need to get started, lists optional software, and gives an overview of the directory structure on your hard drive.

What You Need to Get Started

- ☐ VXIpc 800 Series or VXIpc 700 Series embedded controller (hereafter described together as the VXIpc 800/700 Series)
- ☐ VXIbus mainframe
- ☐ Keyboard (and included adapter cable)
- ☐ Serial mouse (or PS/2 for VXIpc 800 Series)
- ☐ Monitor with VGA or better resolution
- ☐ NI-VXI software media for the VXIpc 800/700 Series

The NI-VXI software is already installed on your VXIpc 800/700 computer. It is also included on disk in the event that you need to reinstall your software.

Hardware Description

The VXIpc 800/700 Series are C-size embedded computers based on the Peripheral Component Interface (PCI) bus and Industry-Standard Architecture (ISA). These computers are high-performance, easy-to-use platforms for controlling VXIbus systems, featuring complete VXI functionality through interactive utilities and C function calls. These embedded computers can take advantage of the VXI high-performance backplane capabilities and give you direct control of VXI registers, memory, interrupts, and triggers.

All models in the VXIpc 800/700 Series are fully *VXIplug&play* compliant and can be used with PC-compatible software tools, the National Instruments LabVIEW and LabWindows®/CVI application software, and the NI-VXI, NI-VISA, and NI-488.2 bus interface software.

For in-depth details on the VXIpc 800/700 hardware (including a description of the differences between the various models in the series), consult the *VXIpc 800/700 Series User Manual*.

Software Description

The NI-VXI bus interface software for the VXIpc 800/700 Series includes a Resource Manager, graphical and text-based versions of an interactive VXI resource editor program, a comprehensive library of software routines for VXI/VME programming, and a VXI interactive control program. You can use this software to create applications that seamlessly control multiple-mainframe configurations. These applications have software compatibility across a variety of VXI/VME controller platforms.

The NI-488.2 software kit gives you accessibility to the industry-standard NI-488.2 software for controlling external GPIB instruments through the GPIB port on the front panel (or through a PCMCIA-GPIB card on models that do not include GPIB as a standard feature). The GPIB interface on your VXIpc controller is fully compatible with the NI-488.2 driver for a variety of operating systems. Any software using NI-488.2 will run on the VXIpc 800/700.

For more information on installing and configuring the NI-VXI software, refer to the *NI-VXI Software Manual for the VXIpc 800/700 Series*. This manual is installed on your hard drive in the `c:\images\manuals` directory. Use the Acrobat Reader program to open the file called `NIVXISM.pdf`. This manual describes each field in the **VXIpc Configuration Editor** and the **VXI/VME-MXI-2 Configuration Editor** of the `VXIedit` software utility. You can use the *NI-VXI Graphical Utilities Reference Manual* and the *NI-VXI Text Utilities Reference Manual* to get more information about the `VIC` or `VICtext` utilities and the other configuration editors in `VXIedit`. Refer to the *NI-VXI User Manual* and the *NI-VXI Programmer Reference Manual* for details about NI-VXI function calls. You can also access online help for Windows 95/NT/3.1 in the NI-VXI folder.

Software Configurations

There are four software configurations described in this manual:

- NI-VXI for DOS/Windows 3.1—You can use this version of the software to develop and run 16-bit DOS/Windows 3.1 applications. You can also use this software under Windows 95 if you intend to use 16-bit applications only.
- NI-VXI Upgrade for Windows 95—This is a compatibility release that extends your NI-VXI for DOS/Windows 3.1 to allow 32-bit applications running in Windows 95 to use the 16-bit driver. In this configuration you can run both 16-bit and 32-bit applications; however, the core of the driver is 16-bit. Applications developed using this driver will run with NI-VXI for Windows NT without the need to recompile.
- ◆ **VXIpc-860 users**—The VXIpc-860 is designed for use with Windows 95/NT only.
 - NI-VXI for Windows 95—This is a fully 32-bit native Plug and Play driver for Windows 95. You can run *only* 32-bit applications with this driver. You cannot use this driver in conjunction with either NI-VXI for DOS/Windows 3.1 or the NI-VXI Upgrade for Windows 95 to run 16-bit applications. Applications developed using this driver will run with NI-VXI for Windows NT without the need to recompile.
 - NI-VXI for Windows NT—This is a 32-bit driver designed for Windows NT. You can use this version to develop and run 32-bit applications for Windows 95/NT. Applications developed using this driver will run with NI-VXI for Windows 95 without the need to recompile.

Optional Software

Your VXIpc 800/700 kit includes the NI-VXI bus interface software. In addition, you can use the National Instruments LabVIEW and LabWindows/CVI application programs and instrument drivers to ease your programming tasks. These standardized programs match the modular virtual instrument capability of VXI and can reduce your VXI/VMEbus software development time. These programs are fully *VXIplug&play* compliant and feature extensive libraries of VXI instrument drivers which are modular, source-code programs that handle the communication with your instrument to speed your application development.

LabVIEW and LabWindows/CVI include all the tools needed for instrument control, data acquisition, analysis, and presentation. When you order the LabVIEW Full Development System for Windows or the LabWindows/CVI Full Development System for Windows, you also get more than 500 complete instrument drivers, written to take full advantage of the VXI bus.

Files and Directories Installed on Your Hard Drive

Your hard drive includes a directory called `images` in its root that contains software and soft copies of manuals for the operating system and for the peripherals. The directory structure under the `images` directory is logically organized into several levels.

In the `images` directory itself, you will find a `manuals` directory, an `os` directory, and directories for each of the peripherals of your computer.

The `manuals` directory contains quick reference guides, technical reference manuals, and National Instruments software manuals, all in Adobe Acrobat format. To access any of these manuals, change your directory to `c:\images\manuals` and list the contents of that directory. You will see several directories, one corresponding to each peripheral. Within each of those directories is the manual. In the case where there are separate manuals depending on the operating system, you will need to enter the directory of the appropriate operating system.

For example, you should find the PCMCIA manual for Windows 3.11 in `c:\images\manuals\pcmcia\win311`.

The `os` directory contains a subdirectory corresponding to the operating system installed on your computer. That subdirectory contains an image of the CD from which your operating system was installed. As a result you do not have to insert the CD when you install a new peripheral. When you are asked to insert the CD, you can simply direct the system to look in the `images\os` directory instead.

For example, if your system has Windows 95 installed, you should find an image of the Windows 95 CD in `c:\images\os\win95`.

The rest of the directories correspond to each of the peripherals in your system. Within each of these directories are the drivers for the peripherals. These files and directories are copied exactly from the

distribution disks of the manufacturers, so the naming conventions vary from peripheral to peripheral.

Because the naming conventions may not be completely straightforward, National Instruments provides a text file named `drivers.txt` in the `images` directory. The `drivers.txt` file explains how to install support for each peripheral. You may want to print this file for reference when you install your peripheral device drivers.

This chapter contains basic instructions for setting up the VXIpc 800/700 and the NI-VXI software.

You can use this material as a guide to quickly configure and operate your VXI system using the VXIpc 800/700. This chapter assumes that you intend to perform a basic configuration as follows:

- You have one VXIbus chassis in which you will be using the VXIpc 800/700 as the Resource Manager (logical address 0).
- You will be using the NI-VXI software for initialization, configuration, and device interaction.
- You will use the default hardware and software settings.

Refer to Chapter 3, *Default Settings*, for a complete listing of the hardware and software default settings. If you need more information, or if you want to try a different configuration, please refer to the *VXIpc 800/700 Series User Manual* for information about the hardware, or to the *NI-VXI Software Manual for the VXIpc 800/700 Series* for information about the NI-VXI software.

Hardware Installation

To prevent electrostatic discharge, touch the antistatic plastic package to a metal part of your VXIbus chassis before removing the VXIpc 800/700 from the package. Install the VXIpc 800/700 in the first slot of a VXI chassis (slot 0). In its default configuration, VXIpc 800/700 Series automatically detects whether it should be the VXIbus system controller. The VXIbus system controllers operate certain VXIbus lines as required for VXI systems. Verify that any other VXI devices with system controller capability that are located in the same chassis are not configured as system controller. Having more than one device configured as system controller will damage the VXI system.

For VXI systems that include VME devices, ensure that the VME devices are not configured in the upper 16 KB (starting from 0xC000)

of the A16 address space. This region is reserved for VXI device configuration registers which are used for initializing, configuring, and interacting with VXI devices.

Also ensure that no VXI devices in your system are configured for logical address 0, which is the default configuration for the VXIpc 800/700.

Setting up the VXIpc 800/700

The VXIpc 800/700 kit works with Windows 95/NT/3.1 or DOS—with the exception of the VXIpc-860, which works with Windows 95/NT only—but the operating systems have different installation and configuration requirements. Be sure to observe any specific instructions for Windows 95/NT/3.1 or DOS in the following information. For more details about installing the NI-VXI software, refer to Chapter 2, *NI-VXI Software Installation*, in the *NI-VXI Software Manual for the VXIpc 800/700 Series*.

Windows Users

At Windows 95/NT/3.x startup, you are prompted to insert a disk to configure the system for LabVIEW or LabWindows/CVI. If you ordered either of these programming environments, select the appropriate checkbox and insert the configuration disk. Follow the instructions as prompted.

Do not select either checkbox if you did not order LabVIEW or LabWindows/CVI. Instead you should click on the **Next** button to continue with the installation.

You will need to initialize your VXIbus system by performing the following steps.

1. **Windows 3.x users**—Locate the NI-VXI group in the Program Manager and run the **VXIinit** item. This utility initializes the VXIpc 800/700 hardware. Windows 3.x is not applicable for the VXIpc-860.

Windows 95/NT users—Because Windows 95 supports the plug and play architecture, you do not need to run **VXIinit** to initialize the VXIpc 800/700. Similarly, you do not need to run **VXIinit** under Windows NT unless **RESMAN** fails. Proceed to step 2.

2. Execute the **Resman** item, which is located within the same NI-VXI group.

DOS Users

Although the VXIpc 800/700 Series default configuration can get Windows users up and running without any changes, DOS users must reconfigure the VXIpc 800/700 Series to operate with applications that will use the NI-VXI software for DOS. You must use the VXI Resource Editor program, either `VXIedit` or `VXItdedit`, to make these necessary changes. Remember, you cannot use the VXIpc-860 under DOS.

1. Run the `VXIedit` or `VXItdedit` utility.
2. Select the **VXIpc Configuration Editor** from the options list.
3. Relocate the VXIpc 800/700 driver window to below 1 MB. Notice that the `VXIedit` or `VXItdedit` utility warns you that the driver window is located above 1 MB. While this default setting is acceptable for Windows users, DOS users must enter a memory address below the 1 MB boundary to relocate the VXIpc 800/700 registers temporarily. Select an unused section of the Upper Memory region (usually 0xC800 to 0xE800). Notice that this memory cannot be used by another device (such as a plug-in card) or memory manager (such as `EMM386.EXE`). This placement is valid only while `VXIedit` or `VXItdedit` is running.
4. To permanently place the board at the address, use the **Bus Configuration Editor** in `VXIedit` or `VXItdedit`. Within this editor, enable the **Place below 1 MB** checkbox. In the **Window Base** field, select the address space to which to assign the VXIpc 800/700 registers.
5. Update your configuration in `VXIedit` or `VXItdedit` by selecting the **Update Current Configuration** option from the **VXIpc Configuration Editor** main menu.
6. Reboot your computer by turning it off and on or pressing the reset button.
7. Execute `VXIinit` from the DOS prompt. This utility initializes the VXIpc 800/700 hardware. `VXIinit` also shows where the PCI Configuration Manager has placed your VXIpc 800/700. If this region conflicts with another board in your system, or if you experience any problems with your system, refer to the *User Window and Driver Window* section in Chapter 3, *NI-VXI Configuration Utility*, in the *NI-VXI Software Manual for the VXIpc 800/700 Series*. Notice that if you are using a memory

manager (such as `EMM386.EXE`), you must exclude the region assigned to your VXIpc 800/700. This region may shift if you insert any expansion boards into the PCI expansion slot.

8. Execute the Resource Manager utility, `RESMAN`, to configure your VXI system.

VME Users

`RESMAN` identifies and configures the VXI devices. `RESMAN` does not configure VME devices. The VME specification does not define the initialization and configuration procedures that the VXI specification requires.

However, it is recommended that you enter the information about your VME devices into the `VXIedit` or `VXItdedit` utility. `RESMAN` can then use this information to properly configure the various device-specific VME address spaces and VME interrupt lines. For more information on configuring non-VXI devices in your VXI system, refer to the description of the **Non-VXI Device Configuration Editor** in Chapter 3, *VXI Resource Editor: VXIedit*, of the *NI-VXI Graphical Utilities Reference Manual*.

Device Interaction

After `RESMAN` has detected and configured all VXI/VME devices, you can view specific information on each device in your system by using the `VXIedit` or `VXItdedit` utilities. These utilities include a **Resource Manager Display**, which contains a description for each device, including each VXI device's logical address.

You can interact with your VXI/VME devices by using the `VIC` or `VICtext` utilities. With these utilities, you can interactively control your VXI/VME devices without having to use a conventional programming language, LabVIEW, or LabWindows/CVI.

Try the following in `VIC` or `VICtext`:

At the prompt, type:

```
help vxiinreg
```

This help file shows you the syntax for this command, which reads VXI device configuration registers. The first argument is a logical address, and the second is the offset of the VXI device configuration register to be read.

Now type:

```
vxiiinreg 0,0
```

This should return a value, such as:

```
Return Status (0): SUCCESS.  
value = 0x9ff6
```

If the value ends with `ff6`, you have successfully read the National Instruments manufacturer ID from the ID register for the VXIpc 800/700.

You may now want to read the configuration registers from other VXI devices in your system using the command `vxiiinreg`. This command accesses only the upper 16 KB of A16 space. Try reading the registers from one of the devices listed in the **Resource Manager Display** of either `VXIedit` or `VXItextedit`. In this way, you can verify that your VXIpc 800/700 can access each of the devices in your VXI system successfully. You can also access VXI and VME devices that are configured in A16, A24, and A32 address space by using the `vxiiin` or `vxiiout` commands.

For more information regarding `VIC` operation and commands, refer to the *NI-VXI Graphical Utilities Reference Manual*. For more information regarding `VICtext` operation and commands, refer to the *NI-VXI Text Utilities Reference Manual*.

Default Settings

Chapter 3

This chapter summarizes the hardware and software default settings for the VXIpc 800/700 Series kit. Because you can also access the **VXI/VME-MXI-2 Configuration Editor** to configure a VXI-MXI-2 or a VME-MXI-2 through the `VXIedit` program, this chapter also summarizes the software default settings for the VXI/VME-MXI-2. If you need more information about a particular setting or if you want to try a different configuration, refer to the hardware and software manuals in your kit.

Refer to the *VXIpc 800/700 Series User Manual* for more detailed information about the hardware default settings and options. The *NI-VXI Software Manual for the VXIpc 800/700 Series* contains more details about the NI-VXI software default settings and options.

Table 3-1. VXIpc 800 Series Hardware Default Settings

Hardware Component	Default Setting
S1—Ethernet EEPROM	Enabled. <i>Do not alter this setting.</i>
S2—Power On Self Configuration (POSC)	Enabled. <i>Do not alter this setting.</i>
S3—CLK10 Source	Source from onboard oscillator
S4—CLK10 SMB Polarity	Not inverted
S5—CLK10 SMB Direction	Receive CLK10
S6—CLK10 SMB Termination	Do not terminate
S7—TrigIn SMB Termination	Do not terminate
S8—GPIB Circuitry Interrupt	Level 11
S9—MITE Configuration EEPROM	Load values from user section

Table 3-1. VXIpc 800 Series Hardware Default Settings (Continued)

Hardware Component	Default Setting
W1—SCSI Termination	Enabled
W2—CMOS Clear	CMOS not cleared
W4—Parallel Port DMA Channel	Channel 1
W13—Slot Detection	Automatically detect slot

Table 3-2. VXIpc 700 Series Hardware Default Settings

Hardware Component	Default Setting
W1—Slot detection	Automatically detect slot
W3—CMOS Clear	CMOS not cleared
W6—Ethernet EEPROM	Enabled. <i>Do not alter this setting.</i>
W7—MITE Configuration EEPROM	Load values from user section
W10—Power On Self Configuration (POSC)	Enabled. <i>Do not alter this setting.</i>
W12—TrigIn SMB Termination	Do not terminate

Table 3-3. Logical Address Configuration Editor Default Settings

Editor Field	Default Setting
Logical Address	0
Device Type	MBD
Address Space	A16
VXI Shared RAM Size	0 KB
Shared RAM Pool	0 KB
Lower Half Window Byte Swapping	Disabled (non-swapped)
Upper Half Window Byte Swapping	Disabled (non-swapped)
Map Upper and Lower Halves to Same Address	Disabled
Resource Manager Delay	5 s

Table 3-4. Device Configuration Editor Default Settings

Editor Field	Default Setting
System IRQ Level	Disabled
Servant Area Size	0
Number of Handlers	1
Number of Interrupters	0
Protocol Register	0xFF0
Read Protocol Response	0x8448

Table 3-5. Bus Configuration Editor Default Settings

Editor Field	Default Setting
Bus Timeout	500
Automatic Retry Protocol	Enabled
Automatic VXI Slave Cycle Retry	Enabled on the VXIpc 800 Series Disabled on the VXIpc 700 Series
A24/A32 Slave Write Posting	Disabled
VXI Transfer Limit	256
Arbiter Type	Priority
Request Level	3
Fair Requester	Disabled
Arbiter Timeout	Disabled
User Window Base	Auto
User Window Size	64 KB
User Window Below 1 MB	No
Driver Window Base	Auto
Driver Window Size	32 KB
Driver Window Below 1 MB	No

Table 3-6. VXI/VME-MXI-2 Configuration Editor Default Settings

Editor Field	Default Setting
Logical Address	1 (set by hardware switch)
LA Selection	Set by hardware switch
Address Space	A24 *
Requested Memory	16 KB *
A16 Write Posting	Disabled
A24/A32 Write Posting	Disabled
Interlocked Mode	Disabled
VXI/VME System Controller	Auto
VXI/VME Bus Timeout Value	125 μ s
VXI/VME Auto Retry	Disabled
VXI/VME Transfer Limit	256
VXI/VME Arbiter Type	Priority
VXI/VME Request Level	3
VXI/VME Fair Requester	Enabled
VXI/VME Arbiter Timeout	Enabled
MXI System Controller	Auto
MXI Bus Timeout Value	1 ms
MXI Auto Retry	Disabled
MXI Transfer Limit	Unlimited
MXI Parity Checking	Enabled

Table 3-6. VXI/VME-MXI-2 Configuration Editor Default Settings (Continued)

Editor Field	Default Setting
MXI Fair Requester	Disabled
MXI CLK10 Signal Direction	Set by hardware switch (VXI-MXI-2 only)
* Assumes no DRAM is installed. If DRAM is installed, the Address Space would be A32, and Requested Memory would match the amount of DRAM.	

Customer Communication

For your convenience, this appendix contains forms to help you gather the information necessary to help us solve your technical problems and a form you can use to comment on the product documentation. When you contact us, we need the information on the Technical Support Form and the configuration form, if your manual contains one, about your system configuration to answer your questions as quickly as possible.

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United Kingdom: 01635 551422

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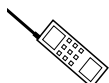
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Canada (Quebec)	514 694 8521	514 694 4399
Denmark	45 76 26 00	45 76 26 02
Finland	09 527 2321	09 502 2930
France	01 48 14 24 24	01 48 14 24 14
Germany	089 741 31 30	089 714 60 35
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Netherlands	0348 433466	0348 430673
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Technical Support Form

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If you are using any National Instruments hardware or software products related to this problem, include the configuration forms from their user manuals. Include additional pages if necessary.

Name _____

Company _____

Address _____

Fax (____) _____ Phone (____) _____

Computer brand _____ Model _____ Processor _____

Operating system (include version number) _____

Clock speed _____MHz RAM _____MB Display adapter _____

Mouse ____yes ____no Other adapters installed _____

Hard disk capacity _____MB Brand _____

Instruments used _____

National Instruments hardware product model _____ Revision _____

Configuration _____

National Instruments software product _____ Version _____

Configuration _____

The problem is: _____

List any error messages: _____

The following steps reproduce the problem: _____

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National Instruments Products

VXIpc 800 Series Hardware Settings

VXIpc 800 Series Model Number _____

Part Number _____

Serial Number _____

Hard Drive Size _____ Video Memory _____

Processor Speed _____

DRAM SIMMs Installed _____

Slot Location _____

W1 Setting: SCSI Termination _____

W2 Setting: CMOS _____

W4 Setting: LPT1 DMA _____

W13 Setting: Slot 0 Detection _____

S1 Setting: Ethernet EEPROM _____

S2 Setting: MITE Self-Configuration _____

S3 Setting: CLK10 Source _____

S4 Setting: Inverted/Non-inverted CLK10 Output _____

S5 Setting: CLK10 SMB _____

S6 Setting: CLK10 Input Termination _____

S7 Setting: External Trigger Input Termination _____

S8 Setting: GPIB IRQ Level _____

S9 Setting: MITE User/Factory Configuration _____

VXIpc 700 Series Hardware Settings

VXIpc 700 Series Model Number _____

Part Number _____

Serial Number _____

Hard Drive Size _____ Video Memory _____

Processor Speed _____
DRAM SIMMs Installed _____
Slot Location _____
W1 Setting: Slot 0 Detection _____
W3 Setting: CMOS _____
W6 Setting: Ethernet EEPROM _____
W7 Setting: MITE User/Factory Configuration _____
W10 Setting: MITE Self-Configuration _____
W12 Setting: External Trigger Input Termination _____

NI-VXI Software Settings

NI-VXI Software Version Number _____
Using VXIedit or VXItdit? _____

VXIpc Configuration Editor Settings (VXIedit)

Logical Address _____
Device Type _____
Address Space _____
VXI Shared RAM Size _____
Shared RAM Pool _____
Byte Swapping for Lower Half Window _____
Byte Swapping for Upper Half Window _____
Mapping Scheme for Lower and Upper Half Windows of VXI Shared RAM _____
Resource Manager Delay _____
System IRQ Level _____
Number of Handlers _____
Number of Interrupters _____
Servant Area Size _____
Protocol Register _____
Read Protocol Response _____
Bus Timeout _____
Automatic Retry Protocol _____
Automatic VXI Slave Cycle Retry _____

A24/A32 Slave Write Posting _____
VXI Transfer Limit _____
Arbiter Type _____
Request Level _____
Fair Requester _____
Arbiter Timeout _____
User Window Base _____
User Window Size _____
User Window Below 1 MB _____
Driver Window Base _____
Driver Window Size _____
Driver Window Below 1 MB _____

VXI/VME-MXI-2 Configuration Editor Settings (VXledit)

Logical Address _____
LA Selection _____
Address Space _____
Requested Memory _____
A16 Write Posting _____
A24/A32 Write Posting _____
Interlocked or Normal Mode _____
VXI/VME System Controller _____
VXI/VME Bus Timeout Value _____
Auto Retry for Cycles from VXI/VMEbus to MXIbus _____
Transfer Limit on VXI/VMEbus _____
VXI/VME Arbiter Type _____
VXI/VME Request Level _____
VXI/VME Fair Requester _____
VXI/VME Arbiter Timeout _____
MXI System Controller _____
MXI Bus Timeout Value _____
Auto Retry for Cycles from MXIbus to VXI/VMEbus _____
Transfer Limit on MXIbus _____
MXI Parity Checking _____

MXI Fair Requester _____

MXI CLK10 Direction (VXI-MXI-2 only) _____

Other Products

Computer make and model _____

Microprocessor _____

Clock frequency or speed _____

Type of video board installed _____

Operating system version _____

Operating system mode _____

Programming language _____

Programming language version _____

Other boards in system _____

Monitor (manufacturer, model) _____

Mouse (manufacturer, model) _____

Keyboard (manufacturer, model) _____

Other peripherals (manufacturer, model) _____

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Prefix	Meaning	Value
n-	nano-	10^{-9}
μ -	micro-	10^{-6}
m-	milli-	10^{-3}
K-	kilo-	10^3
M-	mega-	10^6
G-	giga-	10^9

A

A16 space	VXIbus address space equivalent to the VME 64 KB short address space. In VXI, the upper 16 KB of A16 space is allocated for use by VXI devices configuration registers. This 16 KB region is referred to as VXI configuration space.
A24 space	VXIbus address space equivalent to the VME 16 MB <i>standard</i> address space
A32 space	VXIbus address space equivalent to the VME 4 GB <i>extended</i> address space
address	Character code that identifies a specific location (or series of locations) in memory
address space	A set of 2^n memory locations differentiated from other such sets in VXI/VMEbus systems by six addressing lines known as address modifiers. n is the number of address lines required to uniquely specify

a byte location in a given space. Valid numbers for n are 16, 24, and 32. In VME/VXI, because there are six address modifiers, there are 64 possible address spaces.

address window A portion of address space that can be accessed from the application program

ANSI American National Standards Institute

ASIC application-specific integrated circuit

B

B bytes

backplane An assembly, typically a printed circuit board, with 96-pin connectors and signal paths that bus the connector pins. A C-size VXIbus system will have two sets of bused connectors called J1 and J2. A D-size VXIbus system will have three sets of bused connectors called J1, J2, and J3.

BERR* Bus error signal

BIOS Basic Input/Output System. BIOS functions are the fundamental level of any PC or compatible computer. BIOS functions embody the basic operations needed for successful use of the computer's hardware resources.

BTO See *Bus Timeout Unit*.

Bus Timeout Unit A functional module that times the duration of each data transfer and terminates the cycle if the duration is excessive. Without the termination capability of this module, a bus master attempt to access a nonexistent slave could result in an indefinitely long wait for a slave response.

byte order How bytes are arranged within a word or how words are arranged within a longword. Motorola ordering stores the most significant (MSB) byte or word first, followed by the least significant byte (LSB) or word. Intel ordering stores the LSB or word first, followed by the MSB or word.

C

CLK10	A 10 MHz, ± 100 ppm, individually buffered (to each module slot), differential ECL system clock that is sourced from Slot 0 of a VXIbus mainframe and distributed to Slots 1 through 12 on P2. It is distributed to each slot as a single-source, single-destination signal with a matched delay of under 8 ns.
CMOS	Complementary Metal Oxide Semiconductor; a process used in making chips
Commander	A message-based device that is also a bus master and can control one or more Servants
configuration registers	A set of registers through which the system can identify a module device type, model, manufacturer, address space, and memory requirements. In order to support automatic system and memory configuration, the VXIbus specification requires that all VXIbus devices have a set of such registers.

D

Data Transfer Bus	DTB; one of four buses on the VMEbus backplane. The DTB is used by a bus master to transfer binary data between itself and a slave device.
DMA	Direct Memory Access; a method by which data is transferred between devices and internal memory without intervention of the central processing unit
DRAM	Dynamic RAM (Random Access Memory); storage that the computer must refresh at frequent intervals
driver window	A region of address space that is decoded by the VXIpc 800/700 for use by the NI-VXI software
DTB	See <i>Data Transfer Bus</i> .

E

ECL	Emitter-Coupled Logic
EEPROM	Electrically Erasable Programmable Read Only Memory

embedded controller An intelligent CPU (controller) interface plugged directly into the VXI backplane, giving it direct access to the VXIbus. It must have all of its required VXI interface capabilities built in.

F

fair requester A VXIbus device that will not arbitrate for the VXIbus after releasing it until it detects the bus request signal inactive. This ensures that all requesting devices will be granted use of the bus.

G

GPIB General Purpose Interface Bus (IEEE 488)

H

hex hexadecimal; the numbering system with base 16, using the digits 0 to 9 and letters A to F

Hz hertz; cycles per second

I

IEEE Institute of Electrical and Electronics Engineers

I/O input/output; the techniques, media, and devices used to achieve communication between machines and users

interrupt A means for a device to request service from another device

interrupt handler A VMEbus functional module that detects interrupt requests generated by interrupters and responds to those requests by requesting status and identify information

interrupt level The relative priority at which a device can interrupt

IRQ* Interrupt signal

K

KB kilobytes of memory

L

LED light-emitting diode

logical address An 8-bit number that uniquely identifies each VXIbus device in a system. It defines the A16 register address of a device, and indicates Commander and Servant relationships.

M

master A functional part of a VME/VXIbus device that initiates data transfers on the backplane. A transfer can be either a read or a write.

MB megabytes of memory

MBD Message-Based Device

message-based device An intelligent device that implements the defined VXIbus registers and communication protocols. These devices are able to use Word Serial Protocol to communicate with one another through communication registers.

MITE A National Instruments custom ASIC, a sophisticated dual-channel DMA controller that incorporates the Synchronous MXI and VME64 protocols to achieve high-performance block transfer rates

N

NI-VXI The National Instruments bus interface software for VME/VXIbus systems

Non-Slot 0 device A device configured for installation in any slot in a VXIbus mainframe other than Slot 0. Installing such a device into Slot 0 can damage the device, the VXIbus backplane, or both.

P

PCI	Peripheral Component Interconnect. The PCI bus is a high-performance 32-bit or 64-bit bus with multiplexed address and data lines.
PCMCIA	Personal Computer Memory Card International Association
POSC	Power On Self Configuration

R

RBD	Register-Based Device
register-based device	A Servant-only device that supports VXIbus configuration registers. Register-based devices are typically controlled by message-based devices via device-dependent register reads and writes.
RESMAN	The name of the National Instruments Resource Manager in NI-VXI bus interface software. See <i>Resource Manager</i> .
Resource Manager	A message-based Commander located at Logical Address 0, which provides configuration management services such as address map configuration, Commander and Servant mappings, and self-test and diagnostic management
retry	An acknowledge by a destination that signifies that the cycle did not complete and should be repeated

S

s	seconds
Servant	A device controlled by a Commander; there are message-based and register-based Servants
Shared Memory Protocol	A communication protocol that uses a block of memory that is accessible to both a client and a server. The memory block operates as a message buffer for communications.
SIMM	Single In-line Memory Module

slave	A functional part of a VME/VXIbus device that detects data transfer cycles initiated by a VMEbus master and responds to the transfers when the address specifies one of the device's registers
Slot 0 device	A device configured for installation in Slot 0 of a VXIbus mainframe. This device is unique in the VXIbus system in that it performs the VMEbus System Controller functions, including clock sourcing and arbitration for data transfers across the backplane. Installing such a device into any other slot can damage the device, the VXIbus backplane, or both.
SMB	Sub Miniature Type B connector that features a snap coupling for fast connection

T

trigger	Either TTL or ECL lines used for intermodule communication
TTL	Transistor-Transistor Logic

U

user window	A region of address space reserved by the VXIpc 800/700 Series for use via the NI-VXI low-level function calls. <code>MapVXIAddress()</code> uses this address space to allocate regions for use by the <code>VXIpeek()</code> and <code>VXIpoke()</code> macros.
-------------	-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------

V

VIC or VICtext	VXI Interactive Control Program, a part of the NI-VXI bus interface software package. Used to program VXI devices, and develop and debug VXI application programs.
VME	Versa Module Eurocard or IEEE 1014
VMEbus System Controller	A device configured for installation in Slot 0 of a VXIbus mainframe or Slot 1 of a VMEbus chassis. This device is unique in the VMEbus system in that it performs the VMEbus System Controller functions, including clock sourcing and arbitration for data transfers across the backplane. Installing such a device into any other slot can damage the device, the VMEbus/VXIbus backplane, or both.

VXIbus	VMEbus Extensions for Instrumentation
VXIedit or VXIedit	VXI Resource Editor program, a part of the NI-VXI bus interface software package. Used to configure the system, edit the manufacturer name and ID numbers, edit the model names of VXI and non-VXI devices in the system, as well as the system interrupt configuration information, and display the system configuration information generated by the Resource Manager.
VXIinit	A program in the NI-VXI bus interface software package that initializes the board interrupts, shared RAM, VXI register configurations, and bus configurations

W

Word Serial Protocol	The simplest required communication protocol supported by message-based devices in a VXIbus system. It utilizes the A16 communication registers to transfer data using a simple polling handshake method.
write posting	A mechanism that signifies that a device will immediately give a successful acknowledge to a write transfer and place the transfer in a local buffer. The device can then independently complete the write cycle to the destination.

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