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### SPARCengine CP1500

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<td>FPROM write enable</td>
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<td>1-8</td>
<td>Out</td>
<td>JTAG scan connector</td>
</tr>
<tr>
<td>J1502</td>
<td>1-2</td>
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<td>External clock</td>
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<td>Out</td>
<td>Enable loopback reset</td>
</tr>
<tr>
<td>J3302</td>
<td>1-2</td>
<td>Out</td>
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</tr>
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<td>2-3</td>
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<tr>
<td>J3303</td>
<td>1-2</td>
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<td>Boot from FPROM</td>
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<td>J3303</td>
<td>2-3</td>
<td>Out</td>
<td>Boot from ROMBO</td>
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<td>J3306</td>
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<td>7-segment LED display bias</td>
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<td>Header for I²C EEPROM module</td>
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<td>J3602</td>
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<td>J5102</td>
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### Notes

1. NVRAM 100-5892 is soldered to the CP1500.
2. The NVRAM uses M4T28 SNAPHAT battery 100-5893.
3. The SPARCengine CP1500 does not have a system serial number.

### References

2. *SPARCengine CP Memory Installation and Configuration*, 806-3690.
### Jumper Settings

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3. The CP1500 uses SEEPRM 525-1767.
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### References
2. SPARCengine CP Memory Installation and Configuration, 806-3690.

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### SPARCengine CP1500

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Notes
1. NVRAM 100-5892 is soldered to the CP1500.
2. The NVRAM uses M4T28 SNAPHAT battery 100-5893.
3. The SPARCengine CP1500 does not have a system serial number.

References
2. SPARCengine CP Memory Installation and Configuration, 806-3690.

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### Notes
1. NVRAM 100-5892 is soldered to the CP1500.
2. The NVRAM uses M4T28 SNAPAHAT battery 100-5893.
3. The CP1500 uses SEEPROM 525-1767.
4. The SPARCengine CP1500 does not have a system serial number.

### References
2. SPARCengine CP Memory Installation and Configuration, 806-3690.

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### SPARCengine CP1500

Netra t1 / SPARCengine CP1500

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Notes
1. The minimum SPARCengine CP1500 OS is Solaris 2.6 HW 5/98.
2. The minimum Netra t1 Model 100/105 OS is Solaris 2.6 HW 5/98.

References
3. SPARCengine CP Memory Installation and Configuration, 806-3690.

SPARCengine CP1500
Netra t1 / Netra ct400 / Netra ct800 / SPARCengine CP1500

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<tr>
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<td>write</td>
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<td>Boot from system</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>flash (default)</td>
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<tr>
<td>J3306</td>
<td>2-3</td>
<td>In</td>
<td>7-segment LED display bias</td>
</tr>
<tr>
<td>J3401</td>
<td>1-3</td>
<td>Out</td>
<td>Header for I²C EEPROM module</td>
</tr>
<tr>
<td>J3601</td>
<td>1-3</td>
<td>Out</td>
<td>Header for I²C EEPROM module</td>
</tr>
<tr>
<td>J3602</td>
<td>1-3</td>
<td>Out</td>
<td>Header for I²C EEPROM module</td>
</tr>
<tr>
<td>J4703</td>
<td>1-2</td>
<td>Out</td>
<td>PLD TDO</td>
</tr>
<tr>
<td></td>
<td>2-3</td>
<td>Out</td>
<td>cPCI TDO</td>
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<tr>
<td>J5102</td>
<td>1-2</td>
<td>Out</td>
<td>Thermal diode</td>
</tr>
<tr>
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<td>- pins are not</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>installed</td>
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**Notes**

1. The minimum CP1500 and Netra t1 OS is Solaris 2.6 HW 5/98.
2. The *Operating Environment Installation CD* is required to install Solaris 2.6 HW: 5/98.
3. The minimum Netra ct 400 and ct 800 OS is Solaris 8 HW: 6/00.
4. The Sun Fire 12K / 15K System Controller uses 501-5473 with different OBP.
   Flash update 501-5473 for use as the Sun Fire 12K / 15K System Controller.
5. The 501-6064 and 501-6055 include EDO SDRAM Memory.

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