

MUT BryteBox Fluorescence Interface



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FeliX32™ Software & BryteBox™ Interface

With its own Central Processing Unit and memory, the **BryteBox™** can control virtually every PTI fluorescence acquisition product, including monochromators, photomultipliers, choppers, shutters and our NEW computer controlled slit assemblies. It also has the capability to control selected non-PTI products such as titrators, water baths and cryostats. In addition, alternate non-PTI monochromators can be controlled with the **BryteBox™**.

The **BryteBox™** interfaces to your PC based computer through a dedicated 10 MHz ethernet connection. With a special adaptor it also has the capability to communicate through a USB port. It can be controlled with a laptop computer through USB or PCMCIA, allowing data acquisition in areas where space is limited.



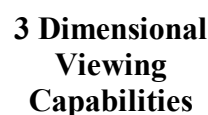
Since this new system concept separates the "speed critical data acquisition electronics" from the host PC, there is NO need for an interface card to be plugged into the host computer bus. The host computer runs a controlling program and communicates instructions to the **BryteBox**. This process off loads the critical data acquisition tasks and frees up the host PC's processor. Since no data capture boards are on the host PC bus, hardware conflicts have essentially been eliminated, thus stabilizing the remote data capture system. [Technical Specifications](#)

Major improvements have been made to the original **FeliX** software, a standard which is being used by thousands of researchers worldwide. Considering the vast number of improvements which have been incorporated, we are now releasing this software package as **FeliX32**.

FeliX32 is compatible with Windows 98, Windows 2000, Windows NT-4 and Windows XP operating systems. The database structure has been greatly improved, allowing for multi user instrument environments. **FeliX32** is also the controlling software for the **BryteBox** interface.

FeliX32 uses full 32-bit implementation with improved graphics capabilities, including

FeliX32 also uses script controlled data acquisition, so specialized experimental routines can be easily created by the end user **FeliX32** macro commands. This allows for unsurpassed flexibility in acquisition, calculation and display of data.



Easy-to-use Hardware Configuration

BryteBox™ Technical Specifications	
Operating Voltage	85-264VAC (47-440 Hz) 120-70VDC
Operating temperature	0 to 40 C

Weight	5.9 kg
Dimensions	360 (W) x 160 (H) x 315 mm (D)
Analog Inputs	
Differential Analog In (Input range)	±50mV; ±500mV; ±100mV; ±1V (selectable)
Signal In; A1-A6 (Input Range)	±5V; ±10V; (selectable)
ADC Parameters	
Resolution	12Bit
Relative Accuracy	±1/2 ±LSB (max.)
Gain Error	±3LSB (max.)
Bipolar Zero Error	±4LSB (max.)
Conversion Time	1.65 µs per Channel
Throughput Time	130 kSPS For 4 Channels
Analog Outputs	
Resolution	12Bit
Output range	-5V to +5V; 0 to +5V; 0 to .5V (selectable)
Relative Accuracy	±1SB (max.)
Gain Error	±2LSB (max.)
Offset Error	±5LSB (max.)
Data Acquisition Rate	1500 Data points per second
Communication	
Rs232	Standard: supporting RTS/CTS Handshake
Ethernet	10 Mbps Ethernet (10 Base-T)
TTL Inputs	
D1, D2	
Input Level	Standard LS-TTL /w 0.4 V Hysteresis
Counter Width	50 Ohm Line Termination (selectable)
Hold Off	32 Bit
Detector Saturation	0 - 5100 ns (software setting)
Max. Frequency	0 - 5100 ns (software setting)
	1/HoIdOff to 25MHz (= 1/2 Fs)
TTL In 1; TTL In 2; MD5020 Calibration	
Flag; Chopper Trigger; Extern Trigger ADC1;	
Extern Trigger ADC2	
Input Level	Standard LS-TTL /w 0.4 V Hysteresis
Max. Frequency	> 10KHz (Software polling)
High Speed TTL IN	
Input Level	Standard LS-TTL /w 0.4 V Hysteresis
Special support	Programmable Interrupt driven
	Pos./neg. edge or level

TTL Outputs	
TTL Out 1; TTL Out 2; Shutter 1-4	Standard LS-TTL Output
Output Level	
Strobe Detector Pulse	Standard LS-TTL Output
Generator Signals	20ns
Output Level	32 Bit
Timing Resolution	86 sec.
Counter Width	
Max. Pulse Width	

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