

VMIC VMIVME-5588DMA-210
Enhanced High-Speed DMA Fiber-Optic 1.2 Gbaud
Reflective Memory with Interrupts



In Stock

Used and in Excellent Condition

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<https://www.artisanng.com/61544-4>

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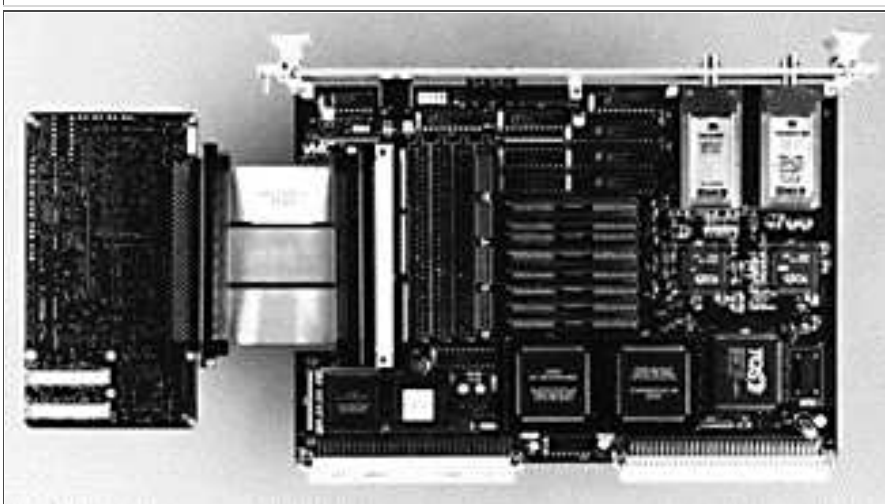
About VMIC



Whitepaper

VMIPMC-5588, PMC HIGH-SPEED FIBER-OPTIC REFLECTIVE MEMORY WITH INTERRUPTS

- PMC Mezzanine PCI Interface
- PCI 32 Transfers, 5 V, Single Width Mezzanine Interface
- High-Speed, Easy-to-Use Fiber-Optic Network (1.2 Gbaud Serially)
- Data Written to Memory in One Node Is Also Written to Memory in All Nodes on the Network
- Up to 1,000 feet Between Nodes and Up to 256 Nodes
- Data Transferred at 29.5 Mbyte/sec without Redundant Transfer
- Data Transferred at 14.8 Mbyte/sec with Redundant Transfer
- Any Node on the Network Can Generate an Interrupt in Any Other Node on the Network or in All Network Nodes with a Single Command
- Error Detection
- Redundant Transmission Mode for Suppressing Errors
- No Processor Overhead
- No Processor Involvement in the Operation of the Network
- Up to 16 Mbyte of Reflective Memory
- Any Node May Reset Any or All Other Nodes
- Communications Link Compatible with Other 5588 Products
- Software Addressable Digital Output bit (Available at Front Panel and P2 Connector) for Interfacing with the [VMIVME-5599](#) Optical Switch Board or Any User-Defined Purpose
- Device Drivers That Include Internet Protocol Are Available for Windows NT. See [VMIPMC/SW-RFM1](#).
- Automatic Fiber-Optic Bypass Switch Option ([VMIVME-5599](#))
- Active Fiber-Optic Bypass Switch Option ([VMIVME-5591](#))
- Optional Multimode/Single-Mode Converter for Driving 6.2 Miles ([VMIVME-5592A](#))

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Information

- Supported by VMIC's Universal I/O Controller (UIOC) ([VMIVME-9300 Series](#)) and Intelligent I/O Controllers (IIOC) ([VMIVME-90XX series](#)) for Host connectivity

Questions or problems regarding this web site should be directed to webmaster@vmic.com.

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