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Installation Guide

CPCI6UX600* Backplane Module

Publication No. CPCI6UX600 Rev. A

abaco SYSTEMS

Document History

Revision	Date	Description
A	January 2018	Rebranding to the Abaco Systems format

Waste Electrical and Electronic Equipment (WEEE) Returns



Abaco Systems is registered with an approved Producer Compliance Scheme (PCS) and, subject to suitable contractual arrangements being in place, will ensure WEEE is processed in accordance with the requirements of the WEEE Directive.

Abaco Systems will evaluate requests to take back products purchased by our customers before August 13, 2005 on a case-by-case basis. A WEEE management fee may apply.

About This Manual

Conventions

Notices

This manual may use the following types of notice:



WARNING

Warnings alert you to the risk of severe personal injury.



CAUTION

Cautions alert you to system danger or loss of data.



NOTE

Notes call attention to important features or instructions.



TIP

Tips give guidance on procedures that may be tackled in a number of ways.



LINK

Links take you to other documents or websites.

Numbers

All numbers are expressed in decimal, except addresses and memory or register data, which are expressed in hexadecimal. Where confusion may occur, decimal numbers have a “D” subscript and binary numbers have a “b” subscript. The prefix “0x” shows a hexadecimal number, following the ‘C’ programming language convention. Thus:

One dozen = 12_D = 0x0C = 1100_b

The multipliers “k”, “M” and “G” have their conventional scientific and engineering meanings of $x10^3$, $x10^6$ and $x10^9$, respectively, and can be used to define a transfer rate. The only exception to this is in the description of the size of memory areas, when “K”, “M” and “G” mean $x2^{10}$, $x2^{20}$ and $x2^{30}$ respectively.

In PowerPC terminology, multiple bit fields are numbered from 0 to n where 0 is the MSB and n is the LSB. PCI terminology follows the more familiar convention that bit 0 is the LSB and n is the MSB.

Text

Signal names ending with a tilde (“~”) denote active low signals; all other signals are active high. “N” and “P” denote the low and high components of a differential signal respectively.

Further Information

Abaco Website

You can find information regarding Abaco products on the following website:



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This document is distributed via the Abaco website. You may register for access to manuals via the website.



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Abaco will log your query in the Technical Support database and allocate it a unique Case number for use in any future correspondence.

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Returns

If you need to return a product, there is a Return Materials Authorization (RMA) form available via the website Embedded Support page.



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https://www.abaco.com/embedded-support

Do not return products without first contacting the Abaco Repairs facility.

Safety Summary

The following general safety precautions must be observed during all phases of the operation, service and repair of this product. Failure to comply with these precautions or with specific warnings elsewhere in this manual violates safety standards of design, manufacture and intended use of this product.

Abaco assumes no liability for the customer's failure to comply with these requirements.

Ground the System

To minimize shock hazard, the chassis and system cabinet must be connected to an electrical ground. A three-conductor AC power cable should be used. The power cable must either be plugged into an approved three-contact electrical outlet or used with a three-contact to two-contact adapter with the grounding wire (green) firmly connected to an electrical ground (safety ground) at the power outlet.

Do Not Operate in an Explosive Atmosphere

Do not operate the system in the presence of flammable gases or fumes. Operation of any electrical system in such an environment constitutes a definite safety hazard.

Keep Away from Live Circuits

Operating personnel must not remove product covers. Component replacement and internal adjustments must be made by qualified maintenance personnel. Do not replace components with power cable connected. Under certain conditions, dangerous voltages may exist even with the power cable removed. To avoid injuries, always disconnect power and discharge circuits before touching them.

Do Not Service or Adjust Alone

Do not attempt internal service or adjustment unless another person capable of rendering first aid and resuscitation is present.

Do Not Substitute Parts or Modify System

Because of the danger of introducing additional hazards, do not install substitute parts or perform any unauthorized modification to the product. Return the product to Abaco for service and repair to ensure that safety features are maintained.

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1 • Handling

1.1 Unpacking Procedures

Any precautions found in the shipping container should be observed. All items should be carefully unpacked and thoroughly inspected for damage that might have occurred during shipment. The board(s) should be checked for broken components, damaged printed circuit board(s), heat damage and other visible contamination. All claims arising from shipping damage should be filed with the carrier and a complete report sent to Abaco Systems Customer Care.

1.2 Handling Precautions

Electronic assemblies use devices that are sensitive to static discharge. Observe anti-static procedures when handling these boards. All products should be in an anti-static plastic bag or conductive foam for storage or shipment. Work at an approved anti-static workstation when unpacking boards.

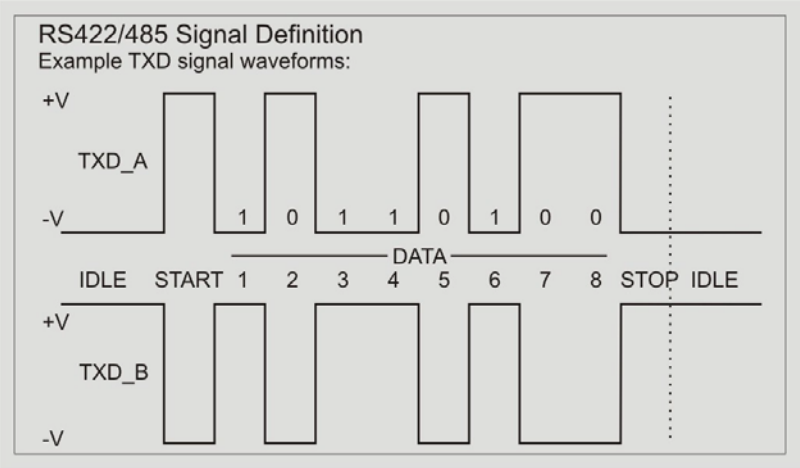
2 • Description

LINK

The information in this document must be used in conjunction with the I/O Modules Manual, publication number RT5154.

The CPCI6UX600 module is a customized interface adapter compatible with 6U CompactPCI backplanes. It fits directly onto the rear P1 to P5 position of the backplane and is suitable for interconnection with the PowerPact6 CP1A host card. See the Compatibility Tables section in the I/O Modules Manual for more information.

The CPCI6UX600 is suitable for fitting in rear guide rails, according to IEEE 1101.11, and can be fitted with a front panel.

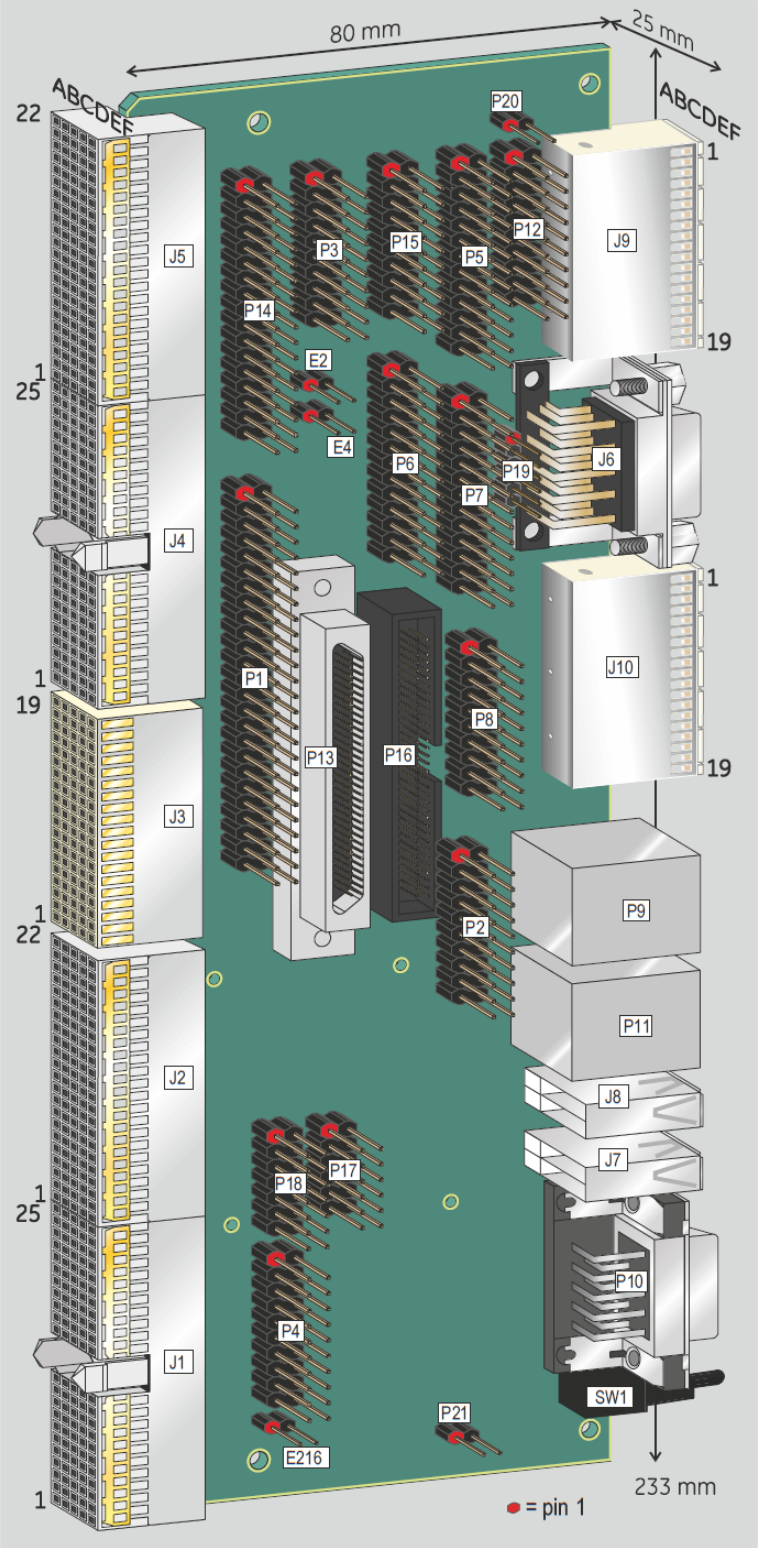


3 • Breakout Panel Connectivity

CPCI6UX600 Header	Breakout Panel	Function
P2	SIOX600	RS232 (2 X 9-way D-type)
P2	SIO2X601	RS232 (2 X 25-way D-type)
P6, P7, P8, P12	SIO3X600	RS422/485
P5	PMKX600	Parallel/Mouse/Keyboard

4 • Connector/Header Designations

Figure 4-1 CPCI6UX600 Connectors and Headers



PCB Male Header ID	Function
P1	AFIX 8-bit SCSI
P2	COM1/COM2 asynchronous
P3	GPIO (PCI Bridge)
P4	JTAG
P5	Keyboard/Mouse/Parallel/GPIO
P6	COM4 synchronous
P7	COM3 synchronous
P8	COM3/COM4 asynchronous
P12	COM5/COM6 asynchronous
P14	Floppy Disk Drive
P15	AFIX I/O
P16	AFIX 16-bit SCSI
P17	AFIX I/O
P18	General I/O
P19	AFIX VGA

Connector	Function	Type
P9	Ethernet Channel 0	RJ-45
P10	COM1	9-way D-type
P11	Ethernet Channel 1	RJ-45
P13	AFIX 16-bit SCSI	68-way D-type
J1, J2 (if fitted)	CP1A interface	110-way, 5-row
J3	CP1A interface	95-way, 5-row
J4, J5	CP1A interface	110-way, 5-row
J6	AFIX VGA	15-way, D-type
J7	USB1	USB
J8	USB2	USB
J9	PMC1	95-way, 5-row
J10	PMC2	95-way, 5-row



NOTES

Not all CPCI backplanes have connector spills for J1 and J2. If these are not present, JTAG and the Reset switch functionality will not be available.

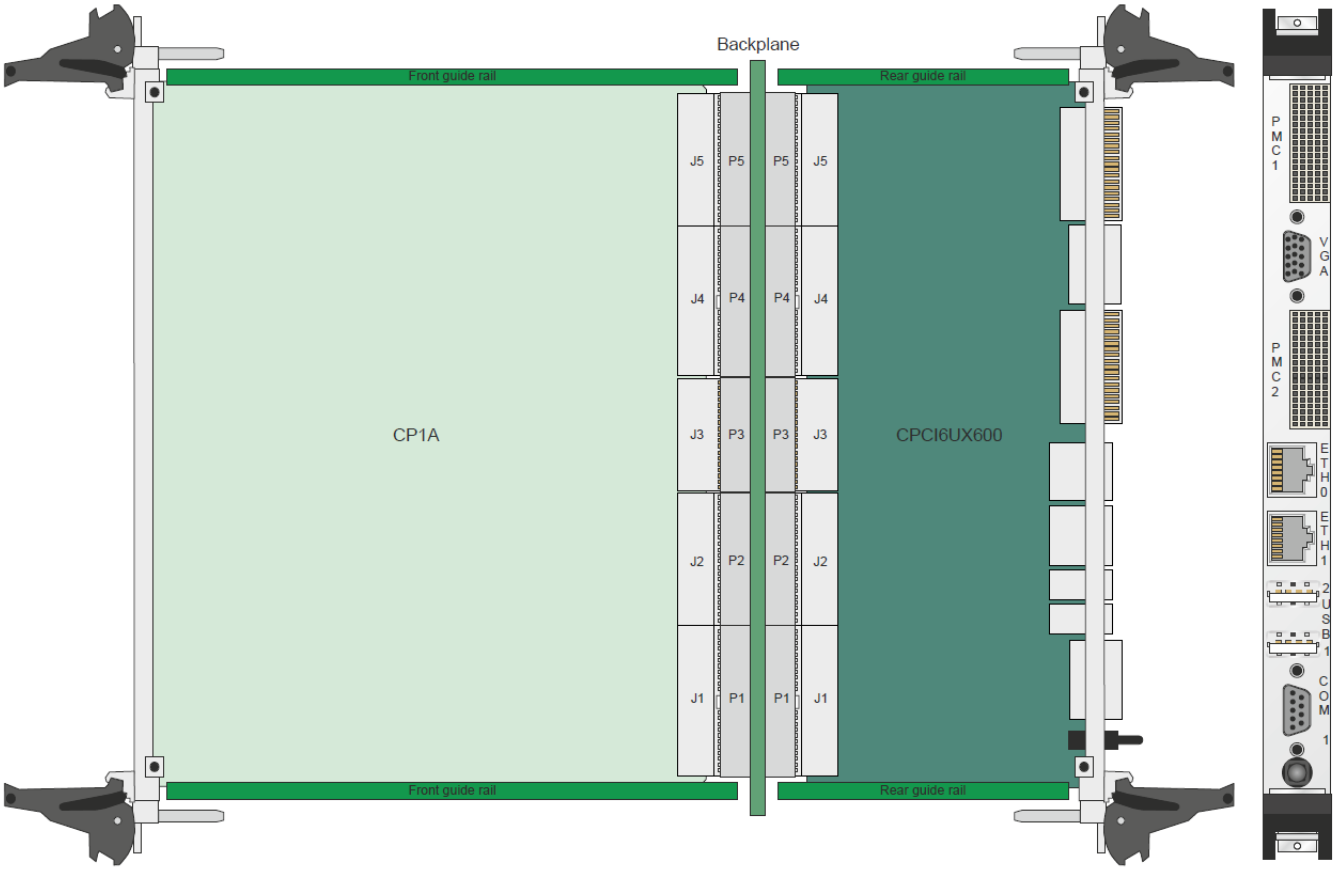
Availability of signals at certain connectors depends on whether and what AFIX module is fitted.

PMC connectors J9 and J10 are provided so that further P0 backplane connectors can be fitted to them (a pin-to-pin converter is also required).

SW1 is a center-biased reset switch.

PCB Male Header ID	Function	Pin	Signal
E2	Speaker	1	SPEAKER_OUT
		2	GND
E4	Keylock	1	KEYLOCK
		2	GND
E216	AUTOWR	1	AUTOWR~
		2	J1_A5
P20	Ground	1	GND
		2	GND
P21	Ground	1	GND
		2	GND

Figure 4-2 CPCI6UX600 in Rear Guide Rails (per IEEE 1101.11), with Optional Front Panel



4.1 Pinouts

4.1.1 P1 (AFIX 8-bit SCSI)

Pin	Signal	Pin	Signal
1	GND	2	SD0~
3	GND	4	SD1~
5	GND	6	SD2~
7	GND	8	SD3~
9	GND	10	SD4~
11	GND	12	SD5~
13	GND	14	SD6~
15	GND	16	SD7~
17	GND	18	SDP0~
19	GND	20	GND
21	GND	22	GND
23	Reserved (GND)	24	GND
25	No connection	26	TERMPWR
27	Reserved (GND)	28	GND
29	GND	30	GND
31	GND	32	SATN~
33	GND	34	GND
35	GND	36	SBSY~
37	GND	38	SACK~
39	GND	40	SRST~
41	GND	42	SMSG~
43	GND	44	SSEL~
45	GND	46	SC_O~
47	GND	48	SREQ~
49	GND	50	SI_O~

4.1.2 P2 (COM1/COM2 Asynchronous)

Pin	Signal	Pin	Signal
1	No connection	2	No connection
3	No connection	4	No connection
5	COM1_TXD~	6	COM1_RXD~
7	COM1_RTS~	8	COM1_CTS~
9	No connection	10	No connection
11	No connection	12	No connection
13	COM2_TXD~	14	No connection
15	No connection	16	COM2_RXD~
17	COM2_RTS~	18	COM2_CTS~
19	GND	20	GND

4.1.3 P3 (PCI Bridge GPIO)

Pin	Signal	Pin	Signal
1	GND	2	GND
3	HB6_IO(0)	4	HB6_IO(1)
5	HB6_IO(2)	6	HB6_IO(3)
7	HB6_IO(4)	8	HB6_IO(5)
9	HB6_IO(6)	10	HB6_IO(7)
11	HB6_IO(8)	12	HB6_IO(9)
13	HB6_IO(10)	14	HB6_IO(11)
15	HB6_IO(12)	16	HB6_IO(13)
17	HB6_IO(14)	18	HB6_IO(15)
19	No connection	20	GND

4.1.4 P4 (JTAG)

Pin	Signal	Pin	Signal
1	TRST~	2	GND
3	TDO	4	GND
5	TDI	6	GND
7	TMS	8	GND
9	TCK	10	GND
11	No connection	12	GND
13	AUTOWR~[a]	14	GND
15	No connection	16	GND
17	No connection	18	GND
19	No connection	20	No connection

a. Enabled by Link E216.

4.1.5 P5 (Keyboard/Mouse/Parallel/GPIO)

Pin	Signal	Pin	Signal
1	STROBE~/GPIO0	2	LPTD0/GPIO1
3	LPTD1/GPIO2	4	LPTD2/GPIO3
5	LPTD3/GPIO4	6	LPTD4/GPIO5
7	LPTD5/GPIO6	8	LPTD6/GPIO7
9	LPTD7/GPIO8	10	ACK~/GPIO9
11	BUSY~/GPIO10	12	PERROR/GPIO11
13	SELECT~/GPIO12	14	AUTOFD~/GPIO13
15	ERR~/GPIO14	16	INT~/GPIO15
17	SLCTIN~	18	KBD_5V
19	GND	20	MOUSE_CLK~
21	MOUSE_DATA~	22	KBD_CLK~
23	GND	24	KBD_DATA~
25	KEYLOCK[a]	26	GND

a. Enabled by Link E216.

4.1.6 P6 and P7 (COMx Synchronous)

P6 (COM4)			
Pin	Signal	Pin	Signal
1	GND	2	GND
3	COM4_TT_A	4	COM4_TT_B
5	COM4_ST_A	6	COM4_ST_B
7	COM4_RT_A	8	COM4_RT_B
9	No connection	10	No connection
11	No connection	12	No connection
13	No connection	14	No connection
15	No connection	16	No connection
17	COM4_RX_A	18	COM4_RX_B
19	COM4_TX_A	20	COM4_TX_B
21	COM4_CTS_A	22	COM4_CTS_B
23	COM4_RTS_A	24	COM4_RTS_B
25	GND	26	GND

P7 (COM3)			
Pin	Signal	Pin	Signal
1	GND	2	GND
3	COM3_TT_A	4	COM3_TT_B
5	COM3_ST_A	6	COM3_ST_B
7	COM3_RT_A	8	COM3_RT_B
9	No connection	10	No connection
11	No connection	12	No connection
13	No connection	14	No connection
15	No connection	16	No connection
17	COM3_RX_A	18	COM3_RX_B
19	COM3_TX_A	20	COM3_TX_B
21	COM3_CTS_A	22	COM3_CTS_B
23	COM3_RTS_A	24	COM3_RTS_B
25	GND	26	GND

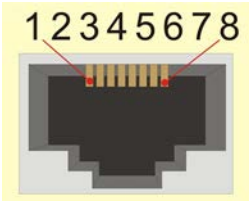
4.1.7 P8 and P12 (COMx Asynchronous)

P8 (COM3/COM4)			
Pin	Signal	Pin	Signal
1	COM3_RX_B (DSR)	2	COM3_TX_B (DTR)
3	COM3_CTS_B (DCD)	4	COM3_RTS_B
5	COM3_TX_A (TX)	6	COM3_RX_A (RX)
7	COM3_RTS_A (RTS)	8	COM3_CTS_A (CTS)
9	No connection	10	No connection
11	COM4_RTS_B	12	COM4_TX_B (DTR)
13	COM4_TX_A (TXD)	14	COM4_RX_B (DSR)
15	COM4_CTS_B (DCD)	16	COM4_RX_A (RX)
17	COM4_RTS_A (RTS)	18	COM4_CTS_A (CTS)
19	GND	20	GND

P12 (COM5/COM6)			
Pin	Signal	Pin	Signal
1	No connection	2	No connection
3	No connection	4	No connection
5	COM5_TXD~	6	COM5_RXD~
7	COM5_RTS~	8	COM5_CTS~
9	No connection	10	No connection
11	No connection	12	No connection
13	COM6_TXD~	14	No connection
15	No connection	16	COM6_RXD~
17	COM6_RTS~	18	COM6_CTS~
19	GND	20	GND

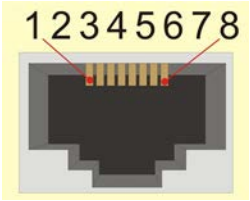
4.1.8 P9 (10/100/1000BaseT Ethernet Channel 0)

Pin	Signal	Pin	Signal
1	ETH0_0+	2	ETH0_0-
3	ETH0_1+	4	ETH0_2+
5	ETH0_2-	6	ETH0_1-
7	ETH0_3+	8	ETH0_3-



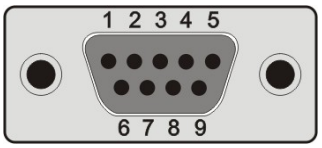
4.1.9 P11 (10/100/1000BaseT Ethernet Channel 1)

Pin	Signal	Pin	Signal
1	ETH1_0+	2	ETH1_0-
3	ETH1_1+	4	ETH1_2+
5	ETH1_2-	6	ETH1_1-
7	ETH1_3+	8	ETH1_3-



4.1.10 P10 (COM1 RS232)

Pin	Signal	Pin	Signal
1	No connection	6	No connection
2	COM1_RXD~	7	COM1_RTS~
3	COM1_TXD~	8	COM1_CTS~
4	No connection	9	No connection
5	GND		



4.1.11 P13 and P16 (AFIX 16-bit SCSI)

P13 is a 68-way D-type connector. P16 is a 68-pin header.

P13			
Pin	Signal	Pin	Signal
1	GND	35	SD12~
2	GND	36	SD13~
3	GND	37	SD14~
4	GND	38	SD15~
5	GND	39	SDP1~
6	GND	40	SD0~
7	GND	41	SD1~
8	GND	42	SD2~
9	GND	43	SD3~
10	GND	44	SD4~
11	GND	45	SD5~
12	GND	46	SD6~
13	GND	47	SD7~
14	GND	48	SDP0~
15	GND	49	GND
16	GND	50	GND
17	TERMPWR	51	TERMPWR
18	TERMPWR	52	TERMPWR
19	Reserved (GND)	53	Reserved (GND)
20	GND	54	Reserved (GND)
21	GND	55	SATN~
22	GND	56	GND
23	GND	57	SBSY~
24	GND	58	SACK~
25	GND	59	SRST~
26	GND	60	SMSG~
27	GND	61	SSEL~
28	GND	62	SC_O~
29	GND	63	SREQ~
30	GND	64	SI_O~
31	GND	65	SD8~
32	GND	66	SD9~
33	GND	67	SD10~
34	GND	68	SD11~

P16			
Pin	Signal	Pin	Signal
1	GND	2	SD12~
3	GND	4	SD13~
5	GND	6	SD14~
7	GND	8	SD15~
9	GND	10	SDP1~
11	GND	12	SD0~
13	GND	14	SD1~
15	GND	16	SD2~
17	GND	18	SD3~
19	GND	20	SD4~
21	GND	22	SD5~
23	GND	24	SD6~
25	GND	26	SD7~
27	GND	28	SDP0~
29	GND	30	GND
31	GND	32	GND
33	TERMPWR	34	TERMPWR
35	TERMPWR	36	TERMPWR
37	Reserved (GND)	38	Reserved (GND)
39	GND	40	Reserved (GND)
41	GND	42	SATN~
43	GND	44	GND
45	GND	46	SBSY~
47	GND	48	SACK~
49	GND	50	SRST~
51	GND	52	SMSG~
53	GND	54	SSEL~
55	GND	56	SC_O~
57	GND	58	SREQ~
59	GND	60	SI_O~
61	GND	62	SD8~
63	GND	64	SD9~
65	GND	66	SD10~
67	GND	68	SD11~

4.1.12 P14 (Floppy Disk Drive)

Pin	Signal	Pin	Signal
1	GND	2	DENSEL
3	GND	4	No connection
5	No connection (key)	6	No connection
7	GND	8	INDEX~
9	GND	10	MOT0~
11	GND	12	DRV1~
13	GND	14	DRV0~
15	GND	16	MOT1~
17	No connection	18	FD_DIR~
19	GND	20	STEP~
21	GND	22	WDATA~
23	GND	24	WGATE~
25	GND	26	TRK0~
27	No connection	28	WPROT~
29	GND	30	RDATA~
31	GND	32	HDSEL~
33	GND	34	DSKCHG~

4.1.13 P15 (AFIX I/O)

Pin	Signal	Pin	Signal
1	GND	2	GND
3	AFIX_16	4	AFIX_17
5	AFIX_18	6	AFIX_19
7	AFIX_20	8	AFIX_21
9	AFIX_22	10	AFIX_23
11	AFIX_24	12	AFIX_25
13	AFIX_26	14	AFIX_27
15	AFIX_28	16	AFIX_29
17	AFIX_30	18	AFIX_31
19	AFIX_32	20	GND

4.1.14 P17 (AFIX I/O)

Pin	Signal	Pin	Signal
1	AFIX_1	2	AFIX_3
3	AFIX_4	4	AFIX_5
5	AFIX_6	6	AFIX_9
7	AFIX_10	8	AFIX_11
9	AFIX_13	10	AFIX_14
11	AFIX_15	12	GND

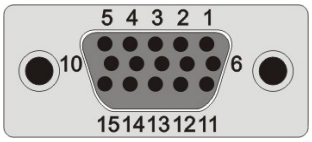
4.1.15 P18 (General I/O)

Pin	Signal	Pin	Signal
1	J4_A19	2	J4_B20
3	EXTABORT~	4	FLASH_WREN~
5	LED1_OP	6	LED2_OP
7	PMC1_VIO	8	PMC2_VIO
9	RTC_STANDBY	10	No connection
11	TCE0~	12	TCE1~
13	TCT0~	14	TCT1~

4.1.16 P19 (AFIX VGA)

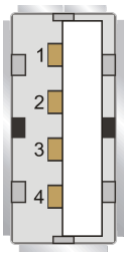
Pin	Signal
1	AFIX_7 (RED)
2	AFIX_2 (GREEN)
3	AFIX_8 (BLUE)
4	AFIX_0 (HSYNC)
5	AFIX_12 (VSYNC)

4.1.17 J6 (AFIX VGA)



Pin	Signal	Pin	Signal	Pin	Signal
1	AFIX_7 (RED)	6	GND	11	No connection
2	AFIX_2 (GREEN)	7	GND	12	No connection
3	AFIX_8 (BLUE)	8	GND	13	AFIX_0 (HSYNC)
4	No connection	9	No connection	14	AFIX_12 (VSYNC)
5	GND	10	GND	15	No connection

4.1.18 J7 and J8 (USB)



J7 (USB1)	
Pin	Signal
1	USB_5V
2	USB1-
3	USB1+
4	GND

J8 (USB2)	
Pin	Signal
1	USB_5V
2	USB2-
3	USB2+
4	GND

4.1.19 J9 (PMC1)

Pin	Row A	Row B	Row C	Row D	Row E	Row F
19	No connection	J5_B10	J5_C10	J5_D10	J5_E10	GND
18	J5_A11	J5_B11	J5_C11	J5_D11	J5_E11	GND
17	J5_A12	J5_B12	J5_C12	J5_D12	J5_E12	GND
16	J5_A13	J5_B13	J5_C13	J5_D13	J5_E13	GND
15	J5_A14	J5_B14	J5_C14	J5_D14	J5_E14	GND
14	J5_A15	J5_B15	J5_C15	J5_D15	J5_E15	GND
13	J5_A16	J5_B16	J5_C16	J5_D16	J5_E16	GND
12	J5_A17	J5_B17	J5_C17	J5_D17	J5_E17	GND
11						GND
10			Keying Area			GND
9						GND
8	J5_A18	J5_B18	J5_C18	J5_D18	J5_E18	GND
7	J5_A19	J5_B19	J5_C19	J5_D19	J5_E19	GND
6	J5_A20	J5_B20	J5_C20	J5_D20	J5_E20	GND
5	J5_A21	J5_B21	J5_C21	J5_D21	J5_E21	GND
4	J5_A22	J5_B22	J5_C22	J5_D22	J5_E22	GND
3	No connection	No connection	No connection	No connection	No connection	GND
2	No connection	No connection	No connection	No connection	No connection	GND
1	No connection	No connection	No connection	No connection	No connection	GND

4.1.20 J10 (PMC2)

Pin	Row A	Row B	Row C	Row D	Row E	Row F
19	No connection	J3_B1	J3_C1	J3_D1	J2_E1	GND
18	J3_A2	J3_B2	J3_C2	J3_D2	J3_E2	GND
17	J3_A3	J3_B3	J3_C3	J3_D3	J3_E3	GND
16	J3_A4	J3_B4	J3_C4	J3_D4	J3_E4	GND
15	J3_A5	J3_B5	J3_C5	J3_D5	J3_E5	GND
14	J3_A6	J3_B6	J3_C6	J3_D6	J3_E6	GND
13	J3_A7	J3_B7	J3_C7	J3_D7	J3_E7	GND
12	J3_A8	J3_B8	J3_C1	J3_D8	J3_E8	GND
11						GND
10			Keying Area			GND
9						GND
8	J3_A9	J3_B9	J3_C9	J3_D9	J3_E9	GND
7	J3_A10	J3_B10	J3_C10	J3_D10	J3_E10	GND
6	J3_A11	J3_B11	J3_C11	J3_D11	J3_E11	GND
5	J3_A12	J3_B12	J3_C12	J3_D12	J3_E12	GND
4	J3_A13	J3_B13	J3_C13	J3_D13	J3_E13	GND
3	No connection	No connection	No connection	No connection	No connection	GND
2	No connection	No connection	No connection	No connection	No connection	GND
1	No connection	No connection	No connection	No connection	No connection	GND

5 • AFIX1553 I/O

If an AFIX1553 is fitted on the CP1A, its signals may be accessed via the P16 and P17 connectors as follows:

Port 0	
1553 Channel A Signal	P16 Pin (Existing Signal)
IDCSAP0	12 (SD0~)
ITCSAP0	14 (SD1~)
ISGNDA0	16 (SD2~)
ITCSAN0	18 (SD3~)
IDCSAN0	20 (SD4~)
1553 Channel B Signal	P16 Pin (Existing Signal)
IDCSBP0	26 (SD7~)
ITCSBP0	28 (SDP0~)
ISGNDB0	42 (SATN~)
ITCSBN0	46 (SBSY~)
IDCSBN0	48 (SACK~)

Port 1	
1553 Channel A Signal	P17 Pin (Existing Signal)
IDCSAP1	3 (AFIX_4)
ITCSAP1	4 (AFIX_5)
ISGNDA1	8 (AFIX_11)
ITCSAN1	11 (AFIX_15)
IDCSAN1	5 (AFIX_6)
1553 Channel B Signal	P17 Pin (Existing Signal)
IDCSBP1	54 (SSEL~)
ITCSBP1	56 (SC_O~)
ISGNDB1	58 (SREQ~)
ITCSBN1	60 (SI_O~)
IDCSBN1	34 or 36 (TERMPWR)

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