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P25X602 Rev3 Backplane Module Edition 1DG

Publication No. P25X602-0HH/1DG

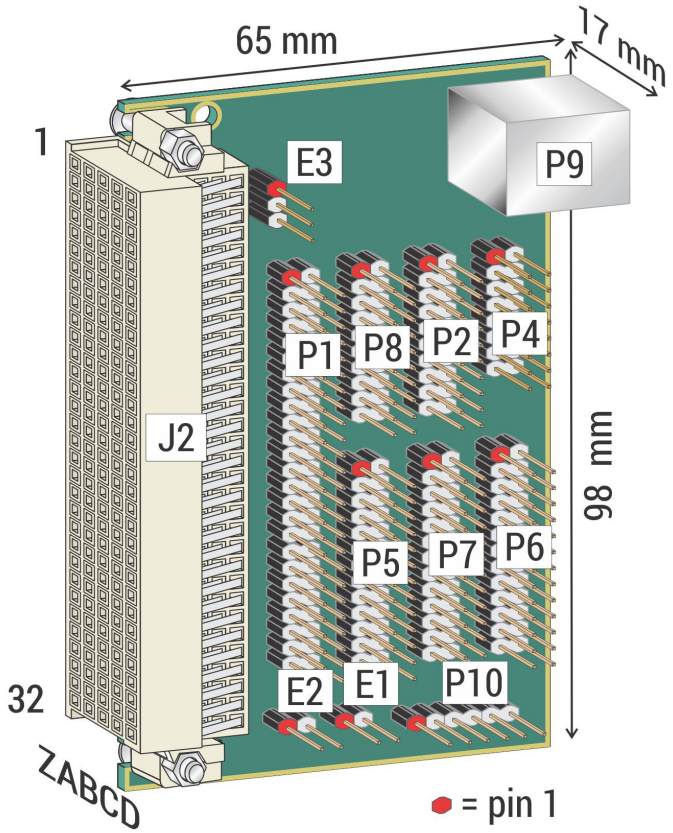
Description

LINK

The information contained in this document must be used in conjunction with the I/O Modules Manual, publication number RT5154.

The P25X602 module is a customised 160-way interface adapter, compatible with 5-row VME backplanes. It fits directly onto the rear P2 position of the backplane and is suitable for interconnection with Abaco host cards PPC2/2A, PPC4/4A, PPC4B, PPC6, and PPCM1. See the I/O Modules Manual - Compatibility Tables for further information.

Figure 1-1 P25X602 Rev3



Connector/Header Designations

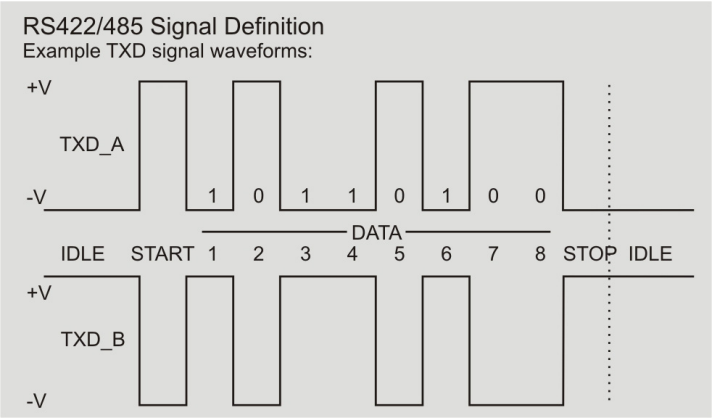
Header ID	Function
E1	Keylock/BIT Fast Start
E2	Speaker
E3	Ethernet AUI Mode Select
P1	SCSI
P2	RS232, COM1/COM2
P3	Ethernet
P5	Mouse, Keyboard, Parallel Port
P6, P7, P10 See Expander Module Mapping	Expander Module Interface COM3/COM4‡, RS422
P8	RS232, COM3/COM4‡
P9	Ethernet RJ45

NOTE

‡ COM3 and COM4 are only available on host cards with this option.

Breakout Panel Connectivity

P25X602 Header	Breakout Panel	Function
P1	CENTX600	SCSI (Centronix)
P1	SCSIX600	SCSI (Honda)
P2, P8	SIOX600	RS232 (2 X 9 WAY D-Type)
P2, P8	SIO2X601	RS232 (2 X 25 WAY D-Type)
P4	10B5X601	10BASE-5 Ethernet
P5	PMKX600	Parallel Port/Keyboard/Mouse
P6, P7	SIO3X600	RS422/485
P9	10BTX600	10/100BASE-T



Pinouts

E1, E2, E3

Header ID	Pin	Function	Notes
E1	1	KEYLOCKIN/BIT Fast Start	
	2	GND	
E2	1	Speaker+	
	2	Speaker GND	
E3	1	GND	Pin 1 to Pin 2 - AUI ground connected to signal ground (10Base5) Pin 2 to Pin 3 - AUI ground connected to chassis ground (Ethernet 1.0) Pin 1 to Pin 3 is not a valid linking combination
	2	ETH_SHIELD	
	3	ETH_SHELL	

P1 (SCSI)

Pin	Signal	Pin	Signal
1	GND	2	DB0~
3	GND	4	DB1~
5	GND	6	DB2~
7	GND	8	DB3~
9	GND	10	DB4~
11	GND	12	DB5~
13	GND	14	DB6~
15	GND	16	DB7~
17	GND	18	DBP~
19	GND	20	GND
21	GND	22	GND
23	GND (See Note)	24	GND (See Note)
25	N/C	26	TERM PWR
27	GND (See Note)	28	GND (See Note)
29	GND	30	GND
31	GND	32	ATN~
33	GND	34	GND
35	GND	36	BSY~
37	GND	38	ACK~
39	GND	40	RST~
41	GND	42	MSG~
43	GND	44	SEL~
45	GND	46	C_D~
47	GND	48	REQ~
49	GND	50	I_O~



NOTE

These pins are defined as reserved in the SCSI specification, but the allowable option to ground them has been used on this module.

P2 (COM1/2 Asynchronous SIO)

Pin	Signal	Pin	Signal
1	COM1_DSR	2	COM1_DTR
3	COM1_DCD	4	COM1_RI
5	COM1_TXD	6	COM1_RXD
7	COM1_RTS	8	COM1_CTS
9	N/C	10	N/C
11	COM2_RI	12	COM2_DTR
13	COM2_TXD	14	COM2_DSR
15	COM2_DCD	16	COM2_RXD
17	COM2_RTS	18	COM2_CTS
19	GND	20	GND

P4 (Ethernet/10BASE-2/10BASE-5)

Pin	Signal	Pin	Signal
1	GND/ETH_SHELL (See Note)	9	COLLISION-
2	COLLISION+	10	TRANSMIT-
3	TRANSMIT+	11	GND
4	GND	12	RECEIVE-
5	RECEIVE+	13	+12V_OUT_FUSED
6	GND	14	GND
7	N/C	15	N/C
8	GND	SHIELD	ETH_SHELL/SHIELD (See Note)



NOTE

Refer to E3 pinout Notes.

P5 (Parallel/Mouse/Keyboard)

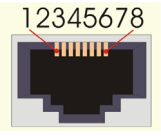
Pin	Signal	Pin	Signal
1	N_STROBE	2	D1
3	D2	4	D3
5	D4	6	D5
7	D6	8	D7
9	D8	10	NACK
11	BUSY	12	PERROR
13	SELECT	14	NAUTOFD
15	NFAULT	16	NINIT
17	NSELECTIN	18	KBD_5V
19	GND	20	MOUSE_CLK
21	MOUSE_DATA	22	KBD_CLK
23	GND	24	KBD_DATA
25	KEYLOCKIN	26	GND

P8 (COM3/4 Asynchronous SIO)

Pin	Signal	Pin	Signal
1	COM3_DSR_A	2	COM3_DTR_A
3	COM3_DCD_A	4	N/C
5	COM3_TXD_A	6	COM3_RXD_A
7	COM3_RTS_A	8	COM3_CTS_A
9	N/C	10	N/C
11	N/C	12	COM4_DTR_A
13	COM4_TXD_A	14	COM4_DSR_A
15	COM4_DCD_A	16	COM4_RXD_A
17	COM4_RTS_A	18	COM4_CTS_A
19	GND	20	GND

P9 (Ethernet 10/100BASE-T)

Pin	Signal	Pin	Signal
1	TRANSMIT-	5	
2	TRANSMIT+	6	COLLISION+
3	COLLISION-	7	
4		8	



Expander Module Mapping

The Expander Module Interface Headers, P6, P7, and P10, are dual-function headers. When connected to host cards with COM3 and COM4 ports fitted, P6 and P7 may be used directly as RS422/485 ports via standard SIO3X600 modules. When connected to host cards without COM3 and COM4, these headers (P6, P7, and P10) are mapped to PMC site 2 signals as shown in the table below:

Pin	COM4 @P6	PMC Signal @ P6	COM3 @P7	PMC Signal @ P7	COM1 @P10	PMC Signal @ P10
1	GND	GND	GND	GND	SPARE1	PMCIO_2
2	GND	GND	GND	GND	SPARE2	PMCIO_5
3	COM4_TT_A	PMCIO_40	COM3_TT_A	PMCIO_22	SPARE3	PMCIO_8
4	COM4_TT_B	PMCIO_42	COM3_TT_B	PMCIO_24	SPARE4	PMCIO_11
5	COM4_ST_A	PMCIO_38	COM3_ST_A	PMCIO_25	COM1_DSR	PMCIO_43
6	COM4_ST_B	PMCIO_41	COM3_ST_B	PMCIO_27	COM1_RI	PMCIO_45
7	COM4_RT_A	PMCIO_44	COM3_RT_A	PMCIO_28		
8	COM4_RT_B	PMCIO_46	COM3_RT_B	PMCIO_30		
9	B_SPARE2	No Connection	B_SPARE4	No Connection		
10	B_SPARE1	No Connection	B_SPARE3	No Connection		
11	COM4_DSR_A	PMCIO_34	COM3_DSR_A	PMCIO_13		
12	COM4_DSR_B	PMCIO_36	COM3_DSR_B	PMCIO_15		
13	COM4_DTR_A	PMCIO_37	COM3_DTR_A	PMCIO_19		
14	COM4_DTR_B	PMCIO_39	COM3_DTR_B	PMCIO_21		
15	COM4_DCD_A	PMCIO_32	COM3_DCD_A	PMCIO_16		
16	COM4_DCD_B	PMCIO_35	COM3_DCD_B	PMCIO_18		
17	COM4_RXD_A	PMCIO_20	COM3_RXD_A	PMCIO_4		
18	COM4_RXD_B	PMCIO_23	COM3_RXD_B	PMCIO_6		
19	COM4_TXD_A	PMCIO_14	COM3_TXD_A	PMCIO_1		
20	COM4_TXD_B	PMCIO_17	COM3_TXD_B	PMCIO_3		
21	COM4_CTS_A	PMCIO_26	COM3_CTS_A	PMCIO_10		
22	COM4_CTS_B	PMCIO_29	COM3_CTS_B	PMCIO_12		
23	COM4_RTS_A	PMCIO_31	COM3_RTS_A	PMCIO_7		
24	COM4_RTS_B	PMCIO_33	COM3_RTS_B	PMCIO_9		
25	GND	GND	GND	GND		
26	GND	GND	GND	GND		

PMCQ1 and EPMCQ2

If PMCQ1 or EPMCQ2 is fitted to PMC Site 2 on a host card without COM3 and COM4, the addition of the QXP2IO Expander Module to P25X602 enables direct access to their COMs signals with the Breakout Panels SIO3X600 (for the RS422/485 signals) and SIOX600 or SIO2X601 (for the RS232 signals). See the QXP2IO section for more details.

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