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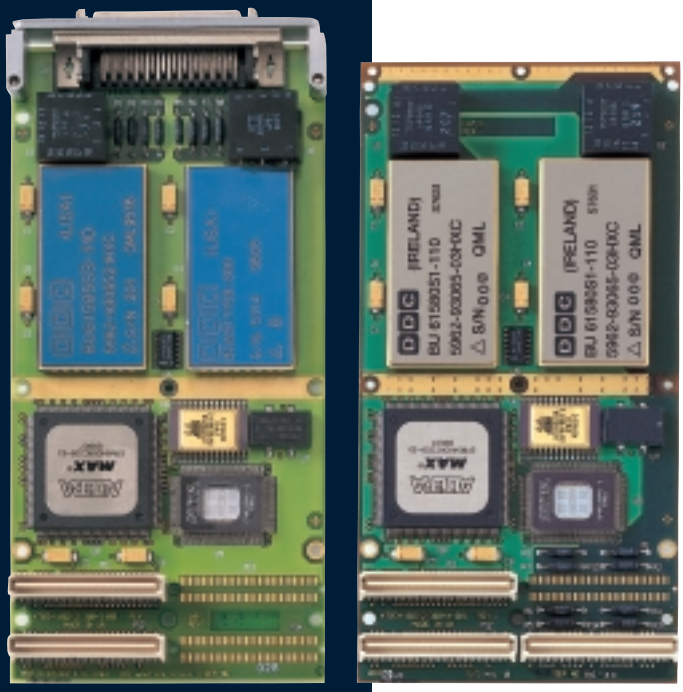
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PMC 1553

RADSTONE MEZZANINE



- Two Independent Dual Redundant MIL-STD-1553B Channels
- Bus Controller (BC), Remote Terminal (RT) or Monitor Terminal (MT)
- Direct or Transformer Coupled
- External BC Trigger/RT Status
- Support for VxWorks and LynxOS
- Comprehensive BIT & JTAG compatible
- Air or Conduction-cooled Builds
- 8-Bit TTL I/O Port

Product Overview

The PMC1553 compliments Radstone's range of PMC compatible Single Board Computers (SBCs) by offering two, dual redundant MIL-STD-1553B interfaces on a single PMC module. The interfaces are compliant with MIL-STD-1553A, MIL-STD-1553B Notice 2 and STANAG 3838. The PMC1553 is compliant with the IEEE P1386.1 PCI Mezzanine Card Standard.

The main features of the PMC1553 are:

- External BC trigger and RT status lines and the 8-bit TTL parallel I/O port gives the PMC1553 it's unique ability to control 1553 terminals.
- Simple system integration is achieved by means of the VxWorks and LynxOS library provided.
- Built-in-Test (BIT) provides diagnostic facilities that give confidence in the sub-system's integrity.

The PMC1553 is available in 4 ruggedization levels to provide optimum solutions for 1553 terminals at all levels of cost and in all types of environment.

BC/RT/MT Option

The PMC1553 provides BC, RT and MT modes of operation that are independently selected on each channel. BC mode includes advanced features, such as automatic re-tries, programmable gap times and frame auto repeat. RT mode has buffering for 3 messages and programmable system test features. MT mode allows word monitoring, selective message monitoring or simultaneous RT and MT modes, as may be required for backup BC operation.

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Why settle for less

RT Only Option

An RT only version of the PMC1553 is available for applications where other modes are not permitted. All RT features are implemented in this variant.

RT Addressing

The RT Address can be set in software allowing run-time system configuration. Hardware setting of the RT address is also available, but this facility overrides the software setting.

Software Support

Radstone provides a PMC1553 software library for VxWorks and LynxOS that simplifies host system integration by offering functional level commands. Other operating systems are also supported by the C source code package that provides: detailed error reporting, improved interrupt handling that minimizes the processing overhead, supports re-entrancy from multiple tasks and RT broadcast sub-addresses. For more details of the 1553 library see data sheet RT225.

Built-In-Test

Full testability is provided as standard by Built-In-Test firmware resident in EEPROM. The BIT functions are performed on command from the host.

Technical Specification

Dimensions		Board size: 143.75 x 74 mm Stacking Height: 10 mm
Power		5V ±0.25V @ 1.9A max. 1553 duty cycle = 100% on both channels.
Mean Time Between Failures (Calculated using MIL-HBK-217F)		1,400,000 hours, Ground Benign 200,000 hours, Naval Sheltered
ACE Device Used	BC, RT & MT option RT only option	BU-61581 BU-65171
EEPROM		32k bytes. Up to 256 bytes for user data
RAM		4k words
Standards Compliance		MIL-STD-1553A, MIL-STD-1553B Notice 2 and STANAG 3838

Ordering Information

Front I/O Connection			
Sales Code	Description	Modes Supported	Ruggedization Level
PMC1553-110	Single channel, MIL-STD-1553B bus	BC, RT and MT	1
PMC1553-120	Single channel, MIL-STD-1553B bus	RT only	1
PMC1553-111	Two channel, MIL-STD-1553B bus	BC, RT and MT	1
Rear I/O Connection			
PMC1553-x33	Two channel, MIL-STD-1553B bus	BC, RT and MT	1-4
PMC1553-x30	Single Channel, MIL-STD-1553B bus	BC, RT and MT	1-4
PMC1553-x40	Single Channel, MIL-STD-1553B bus	RT only	1-4
x = 1, 2, 3 or 4 to indicate ruggedization level			

UK
Radstone Technology
Water Lane
Towcester
Northants NN12 6JN
Telephone: +44 (0) 1327 359444
Facsimile: +44 (0) 1327 359662
E-mail: sales@radstone.co.uk
Videoconferencing: +44 (0) 1327 359495
(by arrangement)

USA
Radstone Technology Corporation
50 Tice Boulevard
Woodcliff Lake, NJ 07677-7645
Telephone: +1 800 368-2738
Facsimile: +1 (201) 391-2899
E-mail: sales@radstone.com

Eastern Area: +1 (201) 391-2700
Mid-Atlantic Region: +1 (201) 391-2700
Southeast Region: +1 (321) 308-0240
Western Region: +1 (909) 974-1141
Central Region: +1 (480) 964 5407

FRANCE
Radstone SARL
10 Av. du Québec
BP 116
91 944 Courtaboeuf Cedex
Telephone: +33 (0) 1 64 46 04 03
Facsimile: +33 (0) 1 69 28 03 40
E-mail: radstonefr@radstone.com

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