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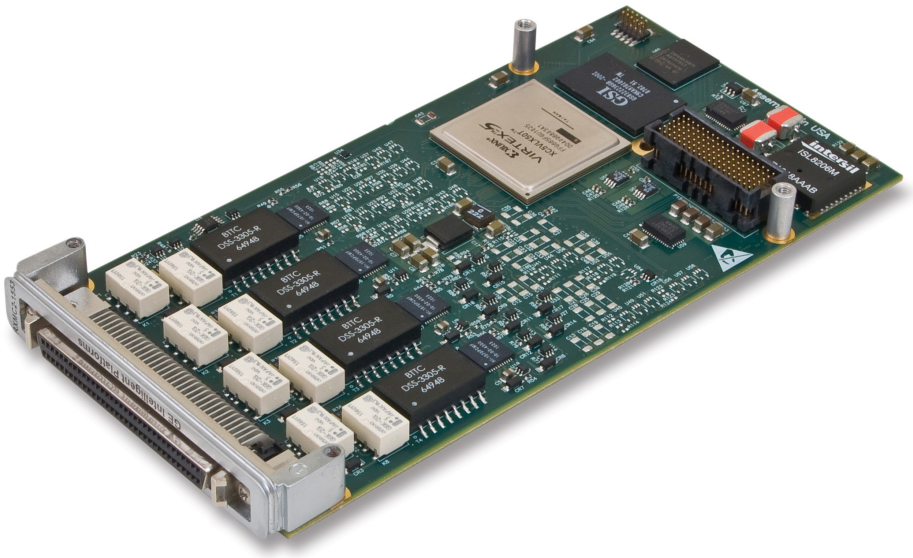
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Find the *Abaco Systems RXMC2-1553* at our website: ***Click HERE***



RXMC2-1553

High Density XMC Module

Abaco Systems' RXMC2-1553 is the latest generation of performance and flexibility for MIL-STD-1553A/B Notice II on a XMC.0 Mezzanine Card. The 4 channel RXMC2-1553 includes advanced API (Application Programming Interface) software that reduces application development time.

Standard features include 1 MByte of RAM per channel, 64-bit, 25 nanosecond message timetagging, triggers, extensive BC & RT link-list structures, error injection/detection, IRIG-B signal Receiver/Generator with GPS synchronization, twelve Avionics digital I/O discretes, automatic/manual RT Status Bit and Mode Code responses, along with advanced BC functionality.

The RXMC2-1553 Bus Monitors provide unparalleled error detection and 100% monitoring of fully loaded buses.

Conformal coating versions available.

Multi-function Interfaces

RXMC2-1553 multi-function interfaces are easily configured to operate with simultaneous Bus Controller, up to 31 Remote Terminals and Bus Monitor functionality.

Dual-function Interfaces

Dual-function RXMC2-1553 interfaces have all the features and functionality of the multi-function versions, with either Bus Monitor and Bus Controller or Bus monitor and 31 Remote terminals.

Software

Abaco provides our advanced 1553 API in source code along with support for Windows 7, 8, 8.1, 10, XP, Vista (32 and 64bit), Server 2012 R1/R2, Linux, Solaris and VxWorks. Contact your local sales person for additional operating system support.

BusTools/1553, Abaco's MIL-STD-1553 bus analysis, simulation and data logging/ monitoring solution is available.

FEATURES:

- 4 dual-redundant MIL-STD-1553A/B Notice II channels
- XMC.3 (4x PCIe) host interface
- Simultaneous Bus Controller, up to 31 Remote Terminals and Bus Monitor
- High-level API for Microsoft® Windows® 7, 8, 8.1, 10, XP, Vista® (32 and 64 bit), Server 2012 R1/R2, Linux®, Solaris and VxWorks® included. Contact your local sales person for additional operating system support.
- 12 Bi-directional Avionics level discretes individually configurable as output triggers
- 2 Differential I/O channels
- Software selectable coupling
- 4x dedicated per channel input triggers
- Flexible hardware remote terminal addressing
- Multi-function and Dual-function versions
- IRIG-B receiver/generator
- 64 bit, 25ns time tagging (virtually unlimited time stamping)
- External time-tag reset and clock inputs
- Complete message programmability
- Flexible message status/interrupt generation
- Error injection/detection
- Transition cabling to 1553 cable jacks optional
- Variable voltage
- RoHS compliant to EU directive 2002/95/EC

abaco SYSTEMS

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RXMC2-1553 *High Density XMC Module*

Specifications

Physical
- XMC Mezzanine Card (74mm x 149 mm without bezel)
- Front I/O interface

Environmental
- Standard air cooled front I/O operating temperature range: -40° C to +85° C
- Relative humidity: 5 to 90% (non-condensing)

Software Support
- API - High-level libraries with source code included for Windows 7, 8, 8.1, 10, XP, Vista (32 and 64 bit), Server 2012 R1/R2, Linux, Solaris and VxWorks. Contact your local sales person for supported software.
- GUI - Optional BusTools/1553 GUI Bus Analysis,Simulation & Data Logging software (multi-function boards using Windows only)

Connections
- Software programmable transformer or direct coupling via relays
- I/O Front panel connections
- Transition cabling to 1553 cable jacks is optional

Multi-function Operational Modes
- Simultaneous BC, 31 RTs and BM

Dual-function Operational Modes
- BM and BC or 31 RTs and BM

Power (4 channels at 75% duty cycle into transformer coupled bus)
- VPWR = +5 or +12 VDC
- 10.2w (VPWR = +12VDC, 75% duty cycle)

On-board Shared RAM
- 1 MByte (per dual-redundant channel)

Timing
- Independent 64-bit, 25ns message time-tagging per channel
- Time can be programmed via the host or by a 1553 trigger
- All timers can be synchronized to 0 via the host
- Timers can independently use IRIG time
- IRIG-B receiver (AM or DC/TTL) and generator (DC/TTL)
- Selectable External time-tag clock input provided
- Selectable External time tag reset input provided

Specifications

Bus Controller
- Programmable control over major and minorframecontent and timing - Inter-message gap times
- Programmable control over response time-out and late response
- Modify messages, data or setup on the fly
- Multiple data buffers
- Insert aperiodic messages into a running BC list
- Conditional message sequencing based on real-time message data or status
- Selectable interrupt generation and status messages on full range of system conditions or all detected errors
- Extensive programmable error injection (on a per word basis)
- Programmable error injection (on a per word basis) see below
- Synchronize BC operation to external time source or trigger
- 64-bit, 25 nanosecond resolution timetagging

Full error detection/injection
- Invalid word
- Bit count error
- High word
- Low word
- Inverted sync
- Manchester
- Late response
- Early response
- No response
- Incorrect RT address
- Parity error
- Invalid command

Remote Terminal
- Multiple RT simulation (up to 31 RTs)
- Programmable error injection (on a per word basis) see below
- Modify data, status words or setup while card is running
- Programmable message content
- Selectable interrupts
- RT Map Monitoring

Bus Monitor
- Capture 100% fully loaded bus traffic with:
  - Time-tagging
  - Word status
  - Error status
  - Message status
- Interrupts can be selected by RT / SA / WC
- Extensive filtering and triggering options
  - By individual RT/subaddress
  - Transmit, receive or broadcast mode codes
  - Internal or external triggering
  - Trigger output on user specified data
- Real-time bus playback with RT edit mode
- IRIG/GPS synchronization
- Programmable error injection (on a per word basis) see below

Optional Configurations
- Conformal coating configurations available
- Dual or Multi-function
- Optional transition cable
- Optional mount on PCIExpress carrier card
- Contact factory for custom requirements

Ordering information

RXMC2-1553-G2-4D4	MIL-STD-1553, XMC interface, dual-function, four dual-redundant channel, 32-bit FW, front I/O, variable volt, ext temp, with IRIG, 12 avionics discretes
RXMC2-1553-G2-4M4	MIL-STD-1553, XMC interface, multi-function, four dual-redundant channel, 32-bit FW, front I/O, variable volt, ext temp, with IRIG, 12 avionics discretes

Optional Hardware

K suffix	Conformal coating
CBL suffix	Transition cable to twinax, 3 lug, CJ70, 1553 cable jacks
PCIE suffix	Mounted on PCIExpress carrier card

Optional Software

BT-1553	MIL-STD-1553 Bus Analysis, Simulation & Data Logging software for Windows
LV-1553	LabVIEW support for MIL-STD-1553

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