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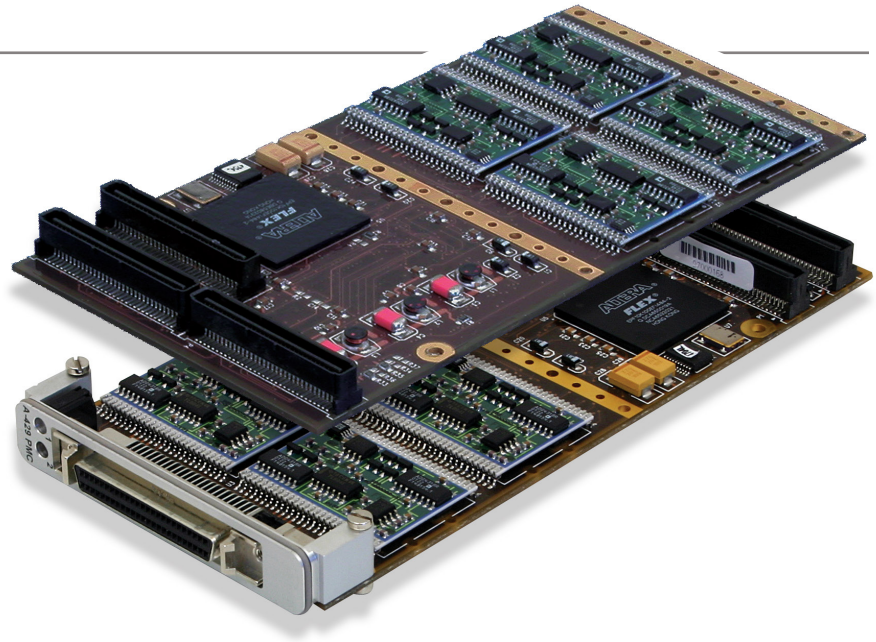
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Find the *Abaco Systems / SBS A429-PMC-8T8R* at our website: ***Click HERE***



A429-PMC

ARINC Interface

A429-PMC-8T8R Series interfaces provide eight fixed receive channels and eight fixed transmit channels of ARINC data to the PMC backplane. Each channel is software configurable for high or low speed (100 kbits or 12.5 kbits per second) and ARINC 429 or 575 protocol requirements. This interface is available in either commercial temperature, extended temperature or conduction cooled versions. As an option, you can have the card configured as eight selectable transmit/receive channels. Contact the factory for details.

A single data structure consisting of a linked list of command blocks governs transmit operations. These command blocks contain encoded information for the transmission of ARINC messages. The user defines a separate data structure for each channel. Transmitter operation is managed by reading and writing control register values.

The current value table, local monitor and/or global monitor buffers receive and filter sink data. Either the Label or Label/SDI identify and sort the ARINC data. The on-board DSP controls the flexible data structures, triggers, interrupts, time stamping and data communications on the ARINC data bus. You can use external triggers for synchronization. The advanced interrupt technology allows real-time event handling by the host processor.

As it receives each 32-bit ARINC 429 data, a 48-bit, 1 µsec time tag time stamps the data word. When placing the receive channel in the monitor mode, a 16-bit status word accompanies the time stamped data. This provides error information regarding each received word.

A transmitter channel allows you to send any number of label sequences on each channel. All transmit channels may also be synchronized for simultaneous transmission. Each transmitted ARINC word has an accompanying control word. The control word implements various error injection capabilities. You can transmit the words in scheduled and/or asynchronous priority methods.

Hardware Overview

Abaco Systems bases the A429 interfaces upon high-speed DSP, programmable logic and dual port RAM. This advanced design delivers a highly reliable hardware platform that is feature rich and user friendly. The 256 Kbyte of dual port RAM allows the host system to access setup, receive, monitor, transmit and change data structures at any time. Definable transmission and receive structures include link list and buffer length sizes. This allows the user to design the data structure optimal for the specific application while maintaining an easy-to-use environment.

FEATURES:

- 8 transmit and 8 receive channels running concurrently and independently. Each channel selectable for:
  - High or low speed
  - Receive and/or bus monitoring
- Versions
  - Commercial (Level C)
  - Extended temperature (Level R)
  - Conduction cooled (Level N)
- Transmitter
  - Advanced bus scheduling
  - Transmission list synchronization
  - Word level error injection
- Receiver
  - Label/SDI current value table
  - 48-bit 1-µsec time stamp
  - Error detection
  - IRIG B option
- Bus Monitor
  - Filterable sequential buffer
  - Link-list buffer chains
  - 48-bit 1-µsec time stamp
  - Error detection
  - External trigger initiation
- Architecture
  - DSP (host off-loading)
  - Memory (large, flexible)
  - FPGA (flexible and upgradeable design)
- Software Support
  - Drivers for most operating systems at no cost
  - Integrated Avionics Library, including source code
  - Programming examples

abaco SYSTEMS

A429-PMC *ARINC Interface*

Specifications

A429 Functionality

Transmitter Function

- Independent channel operations
- Major/minor frame scheduling
- Priority asynchronous message insertion
- Transmission link buffers
- Synchronous word transmission
- On-the-fly transmission list
- Error injection
- Programmable interword gap
- 100 kHz or 12.5 kHz transmission speed

Receiver Function

- Current buffer value
- Time stamped received labels
- SDI and Label differentiation
- Label filter functions
- Messages time stamped with 1 µsec 32-bit clock or optional 48-bit IRIG-B clock
- Multiple triggers and interrupts
- Error detection

Monitor

- Channel sequential monitor
- Global sequential monitor
- Buffer swap notification
- Variable length buffers
- Count detection triggers

Self Test

- Power-up test with status register report
- BIT-DSP and encoder/decoder test
- Run-time health status register
- Loopback Unit Test application
- Transmit wrap

Inputs/Outputs

- External triggers
- External clock
- External clock enable
- IRIG clock input (optional)

PMC Functionality

- PCI bus is 2.1 compliant
- 50 Mbyte per second maximum transfer rate
- 16-bit and 32-bit transfer modes
- Programmable DMA controller
- On-board firmware storage via Flash memory

PCI Mezzanine Connection

- IEEE P1386 PMC connection
- PMC P4 to VMEbus P2 support
- Compatible with VITA/ANSI-20-199x (Draft 1.6)

Interface Connections

- 50-pin connector (non-CC boards)
- J2 connector

Interface Card Specifications

- Maximum power consumption with 400 ohm transmit loads:
  5 V @ 800 mA,
  +12 V @ 520 mA,
  -12 V @ 470 mA
- Mechanical:
  Standard PMC card
  Length 6.0” Width 2.9”

Conductive Cooled Version Available

- Temperature - ruggedization
- Level C (standard commercial, air cooled temperature):
  0° C to +70° C
  ≤ 95% rH non-condensing
- Level R (extended temperature):
  -40° C to +85° C
  ≤ 95% rH non-condensing
- Level N (rugged, conduction cooled temperature):
  -40° C to +85° C
  ≤ 95% rH non-condensing

Software and Documentation Support

- Low-level drivers for most operating systems
- Integrated Avionics Library with source code
- Borland and Microsoft® C Compiler compatible
- Hardware and Integrated Avionics Library documentation included on CD. Hard copies of the documentation are available upon request.

Ordering information

A429-PMC-8R8T	8 Transmit & 8 Receive Channels ARINC to PMC interface
A429-PMCXT-8R8T	8 Transmit & 8 Receive Channels ARINC to PMC interface, Extended Temp
A429-PMCCC-8R8T	8 Transmit & 8 Receive Channels ARINC to PMC interface, Conduction Cooled
A429-PMC-8R8T/I	8 Transmit & 8 Receive Channels ARINC to PMC interface; IRIG B Time Receiver
A429-PCM2-4R4T	
P32-PCM2-4T4R	

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