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IP-CM302

Intelligent IndustryPack Module With Three High Speed Multiprotocol Serial Ports

Application Information

The IP-CM302 is part of the IndustryPack® family of modular I/O components. It is an intelligent multiprotocol communication IndustryPack. The IndustryPack incorporates the Motorola MC68302 Integrated Multiprotocol Processor (IMP) running at 16 MHz. The 68302 contains a 68000 core processor, a microcoded RISC communications processor, 6 dedicated DMA channels, 3 timers, an interrupt controller, three independent full-duplex serial channels, 1152 bytes of dual-port RAM, and a conventional parallel 68000 bus interface as well as an expandable serial interface.

A variety of physical layer standards, such as RS-232-D, RS-422, RS-485, ISDN, are supported by the use of transition modules. The architecture makes it easy for customers to implement special physical interface requirements by substituting their own transition module. Transition module is installed between the IndustryPack I/O connector and other communication terminals.

Internal microcode in the 68302 supports a variety of protocols: BISYNC, HDLC, SDLC, UART, DDCMP, V.110, V.120, X.25, ISDN (2B+D channels) and Transparent Modes.

The IndustryPack implements 256 or 512 Kbytes of high speed volatile static RAM (SRAM) as dual-port memory for both 68302 code and communications data. The code must be downloaded into the SRAM by a host CPU prior to removing reset to the 68302. The SRAM, 68302 registers and on-chip RAM are dual ported. Dual-portable is defined as accessibility by both the 68000 core processor and the host CPU. Both the IndustryPack and the host CPU are capable of generating interrupts to each other.

A pin-grid-array version of the IP-CM302 is available. This mounts the socketted CPU on the back of the board so that an in-circuit emulator may be used to develop and debug 68302 code.

An external baud rate generator clock may be provided from the IndustryPack I/O connector. This clock is used to generate baud rates the base clock is incapable of generating. Typically this is not needed since the 16MHz base clock delivers all the standard baud rates.

Features

- MC68302 Integrated Multiprotocol Communication Processor @ 16 or 25 MHz
- 3 independent full-duplex serial channels
- 6 DMA channels
- 256 Kbytes or 52 Kbytes of dual-port static RAM
- 16-bit interface
- HDLC, SDLC, BISYNC, DDCMP, UART, V.110, V.120, X.25, ISDN (2B+D channels).
- Physical layer implemented with transition modules
- Single-high IndustryPack

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Specifications

Form Factor	Single-wide Type II IndustryPack
IndustryPack Interface	Complies with ANSI/VITA-4
Number of Serial Ports	Three
Signals Supported	RXD, TXD, RXC, TXC, CD, CTS, RTS, BRG
General-Purpose I/O	Four plus external clock
CPU	MC68302-FC (IMP) at 16 MHz (-162) or 25 MHz (-252 and -255)
Memory	256 Kbytes (-162 or -252) or 512 Kbytes (-255) SRAM, dual-ported between CPU and host system
Crystal	Baud rate generation is from CPU clock on –162 variant 25 MHz crystal provides CPU clock and baud rates on –252 and -255
Baud Rates	Up to 2 Mbps sync, depending on protocol and interface
DMA	6 channels dedicated
Timers	Three, 16 bit
ESD Protection	33 Ohm resistors on all I/O
Interrupts	Maskable, vectored. Host can interrupt CPU. CPU can interrupt host.
Dimensions	1.8 inches x 3.9 inches
Power Requirements (no load)	+5 VDC, 350 mA typ; +12 VDC, 0 mA typ;-12 VDC, 0 mA typ
Environmental	Operating temperature: -0 to +70°C ; Storage: –40 to +85°C Humidity: 5 to 95% non-condensing

Ordering Information

IP-CM302-25-256	Serial communication IndustryPack with MC68302 @ 25 MHz, 256 K SRAM
IP-CM302-25-512	Serial communication IndustryPack with MC68302 @ 25 MHz, 512 K SRAM

Associated Products

C-HD50M-HD50M	Six foot HD50 male to HD50 male cable
C-HD50M-CM50M	Six foot Champ50 male to HD50 male cable
IP-TERM	50 screw terminal block with ribbon cable connector
IP-TERM-HD50	50 screw terminal block with HD50 female connector
C-CM50M-IP50M	Two foot cable Champ50 to ribbon male header

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