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IP-HIADC

16-Channel 12-Bit ADC IndustryPack

Application Information

The IP-HiADC provides 16 single-ended simultaneously sampled channels of 12-bit analog to digital conversion on a single-wide IndustryPack. Throughput of up to 800 ksamples/sec allows all channels to be converted at 50 kHz. Sixteen track and hold amplifiers feed one ADC via a multiplexer. Input ranges are software-selectable between ±5 V and ±10 V.

Gain and offset calibration parameters are stored in the board's EEPROM.

Key Features

- Sixteen single-ended simultaneously-sampled 12-bit ADC channels
- Precision on-board reference
- 800 ksamples/sec throughput
- Input ranges of ±5V, ±10V
- 1.25 us conversion time
- Factory calibrated; data in EEPROM

Specifications

Form Factor	Single-wide IndustryPack
IndustryPack Bus Speed	8 MHz
Number of Channels	16 ADC single-ended, simultaneously sampled
Precision	12 bits
Intput Voltage Ranges	±10V, ±5V nominal Selectable by software
Calibration	Offset and gain factory calibrated. Parameters in EEPROM
Conversion Time	1.25 us (800 ksamples/sec thoughput max) All channels at 50 ksamples/sec
Input Impedance	1 MOhm
Dimensions	3.9” x 1.8”
Power Requirements	+5 VDC, 60 mA typical +12 VDC 225 mA typical –12 VDC 225 mA typical
Environmental	Operating temperature: 0 to 70°C Humidity: 5 to 95% non-condensing Storage: –10 to +85°C

Order Information

IP-HIADC	IndustryPack with sixteen 12-bit simultaneously-sampled ADC channels

Associated Products

IP-TERM	Fifty screw terminal block with ribbon connector

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