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Find the *Abaco Systems / SBS / Greenspring IP-MP-Serial* at our website: *Click HERE*

IP-MP-SERIAL

IndustryPack™ Module with Two EIA-232/422/485 High-Speed Multi-Protocol Serial Ports

I/O & Communications

Features

- Two EIA-232/422/485 ports on a single-wide IndustryPack™
- FIFOs on all receivers and transmitters
- Multi-protocol support including SDLC, HDLC, Bisynchronous, NRZ, NRZI, and biphase
- Data rates up to 10 Mb/s synchronous with external clock
- DMA and 32 MHz operation supported
- ND-SDPACK Windows NT® SDpacK IndustryPack driver
- Conduction-cooled and extended temperature range versions are also available; contact SBS for details



IP-MP-SERIAL is an IndustryPack™ module that implements two EIA-232/422/485 high-speed multi-protocol serial ports, each with a full set of handshake and clock signals. Protocols include SDLC, HDLC, bisynchronous, NRZ, NRZI, and Biphase.

IP-MP-SERIAL incorporates the Zilog Z16C30 Universal Multi-Protocol Serial Controller (USC) that includes four 32-byte FIFO buffers. The two channels within the USC are completely independent. Each channel has a baud rate generator, a digital phase-locked loop, a vector register, an interrupt controller, and a protocol controller. Additional capabilities include 16- and 32-bit CRC generation and checking, one to eight bits per character, one address/data bit, MIL-STD-1553B protocol support, sync stripping, preamble generation, and digital phase-locked loop circuits for clock recovery.

IP-MP-SERIAL includes an on-board 3.6864 MHz oscillator to provide all of the standard baud rates. A separate location is provided on the IndustryPack for a user-supplied oscillator to allow non-standard baud rates. The I/O interface provides four external clock lines to permit full-duplex synchronous operation on both channels. Each channel's interface includes an external transmit and a receive clock. The transmit clock can be programmed to be either an input to the IndustryPack or an output from the IndustryPack. The receive clock is an input only.

Independent interrupt vectors and controls are provided for each IP-MP-SERIAL channel.

The RS-232, RS-422, and RS-485 compatible interface circuit facilitates interfacing with a wide variety of equipment. To support RS-485 operation, the RS-422 transmit data (TxD) line of both channels is equipped with an RS-422 transceiver. Under software control this pair of lines can be either transmit data or receive data. The transmit clock (TxC) line of each channel is also equipped with an RS-422 transceiver.

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IP-MP-SERIAL

Specifications

Form Factor
- Single-wide Type II IndustryPack

IndustryPack Interface
- Complies with ANSI/VITA-4

Number of Serial Ports
- Two

Signals Supported
- RXD+, RXD-, TXD+, TXD-, RTS+, RTS-, CTS+, CTS-, DCD+, DCD-, DTR+, DTR-, DSR+, DSR-, TXC+, TXC-, RXC+, RXC-, GND
- Transmit clock can be input or output

General Purpose Inputs
- None

Termination
- Socketed resistor packs

Serial Controller
- Z16C30

Crystal
- 3.6864 MHz

Baud Rates
- 50 to 115.2 kb/s from supplied crystal
- Up to 2 Mb/s asynchronous with external clock
- Up to 4 Mb/s synchronous with external clock on 8 MHz IndustryPack bus with DMA
- Up to 10 Mb/s synchronous with external clock on 32 MHz IndustryPack bus with DMA

Stop Bits
- Programmable in $^{1}/_{16}$ bit increments from 0 to 2

Receiver FIFO
- 32 characters per port

Transmitter FIFO
- 32 characters per port

Timers
- None

ESD Protection
- None

Interrupts
- Maskable, vectored

Power Requirements (RS-422 interface, 32 MHz IP bus, Bisync mode, 3.6864 MHz Tx and Rx Clock, all ports are sending and receiving data, default jumper setting)
- +5 VDC, 390 mA typical
- +12 VDC, 0 mA typical
- -12 VDC, 20 mA typical

Temperature
- Operating: 0˚ to 70˚ C
- Storage: -40˚ to 85˚ C

Humidity
- 5% to 95%, non-condensing

Weight
- 0.06 kg (0.1 lb)

Dimensions
- 1.8" x 3.9"

Ordering Information

Model Number	Configuration
IP-MP-SERIAL	IndustryPack with two EIA-232/422/485 high-speed multiprotocol serial ports
C-IP50F-2D25F	Six foot, 50 conductor ribbon cable to two DB25 females

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