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CP-6212

Eight-Channel High-Speed Serial Board for 6U CompactPCI®

Features

- Eight high-speed serial channels via rear panel
- Full hot swap capable
- Maximum data rate of 8 Mb/sec (synchronous) and 2 Mb/s (asynchronous)
- HDLC, SDLC, LAPB, LAPD, ISDN, PPP, bisync, monosync, and asynchronous protocols
- RS-232, V.35, V.36, RS-449, EIA-530, EIA530A, X.21 physical interfaces
- 306 x 32-bit total FIFO per DSCC4
- PCI bus master

The CP-6212 provides eight channels of multi-protocol high-speed serial data communications on a 6U CompactPCI full hot swap capable card. It can be used in many data communications, LAN/WAN networking and telecommunications applications. A maximum data rate of 8 Mb/s is supported for synchronous protocols using an external clock. Up to 2 Mb/s is supported for asynchronous protocols. Each of the eight channels is fully programmable to support many high-performance serial communications protocols, such as asynchronous, monosync, bisync, HDLC, SDLC, LAPB, LAPD, ISDN, and PPP.

The CP-6212 supports a wide selection of physical interfaces, configurable by software alone. Channels can be individually selected as RS-232, V.35, V.36, RS-449, EIA-530, EIA-530A, X.21. No hardware reconfiguration is required. Ten signal pairs plus two single signals (RL and RI) for each channel are available at the rear I/O connectors J3 and J5. An optional Rear Transition Module, the TM-682, brings all wiring of eight channels to four AMP Small Serial I/O dual stacked 52-pin connectors P/N 1339137-1.

A on-board programmable baud rate generator is provided for each channel. The baud rate generator is capable of generating a wide range of baud rates from 1.2 kb/s to 8192 kb/s.

Interrupts are fully supported for each channel. Interrupts sources include transmission error, reception error, completions of transmit packets, completions of receive packets, and peripheral block.

The CP-6212 uses two high-performance Siemens PEB20534 DMA-supported Serial Communication Controllers (DSCC4) with integral PCI bus interface. These two chips are connected to the CompactPCI backplane through an Intel 21154 PCI-to-PCI bridge.

Each DSCC4 controls four channels. Each channel provides central receive and transmit FIFOs, each 128 deep by 32 bits wide. Additionally, each channel has a local receive FIFO 17 deep by 32 bits wide, and a local transmit FIFO of 8 deep by 32 bits wide. Eight DMA controllers are available to enable the central FIFO to burst into shared memory. The CP-6212 must be plugged into a CompactPCI slot that has bus-mastering capability.

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CP-6212 Specifications

Form Factor	6U CompactPCI
CompactPCI Interface	Hot Swap, 33 MHz, 32/64-bit, master/slave Bus mastering required
Serial Controller	Siemens PEB 20534
Number of Serial Channels	Eight
Maximum Data Rate	Asynchronous: 2 Mb/s Synchronous: 8 Mb/s
Physical Levels Supported	RS-232, V.35, V.36, RS-449, EIA-530, EIA-530A, X.21
Protocols Supported	Asynchronous UART, bisync, monosync, HDLC, SDLC, LAPB, LAPD, PPP, ISDN BRI
Status LEDs	Board power (green), hot swap (blue)
Front-panel I/O	None
Rear-panel I/O	J3 and J5: 10 signal pairs, RL, RI (for each channel)
Dimensions	233 mm x 160 mm (6U CompactPCI)
Power Requirements	++3.3 VDC (typical): 1100 mA EIA-530 mode idle 1590 mA EIA-530 mode maximum traffic 1000 mA EIA-232 mode idle 1500 mA EIA-232 mode maximum traffic +5.0 VDC (typical): 400 mA EIA-530 mode idle 1060 mA EIA-530 mode maximum traffic 400 mA EIA-232 mode idle 630 mA EIA-232 mode maximum traffic +12.0 VDC (typical): 10 mA EIA-530 mode idle 10 mA EIA-530 mode maximum traffic 10 mA EIA-232 mode idle 10 mA EIA-232 mode maximum traffic
Environmental	Operating temperature: 0° to 70° C Humidity: 5% to 95% non-condensing Storage: –40° to +85° C

Ordering Information

CP-6212	Eight-Channel High-Speed Serial Board for CompactPCI
TM-682	Transition Module for CP-6212

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