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VG5 6U Single Slot Single/Dual PowerPC 7457 in RoHS (Version 3.x)

Standard features for all versions:

VME-64 interface
RTC, watchdog, temperature sensor
COM1: front: with handshake, rear: onlyTxD/RxD
COM2: front only without PMC2
COM4: front (w/ Dual CPU) and rear: onlyTxD/RxD

With Node A: COM1=RS232; COM2=RS232/422/485; COM3=RS422; COM4=RS232
With Node A + B: COM1=RS232; COM2=RS232/422/485; COM3=RS422; COM4=RS232; COM5=RS422; COM6=RS422
With Node B: Gigabit-Ethernet (Eth 3) and 10/100 Ethernet (Eth 4) rear

V	**G**	**5**	x	x	x	x	x	x	x	x	x	1		
			S											Single CPU (Node A)
			D											Dual CPU (Node A + B)
				N										MPC7457 867 MHz
				K										MPC7457 1000 MHz
				L										MPC7457 1267 MHz
					7									256 Mbyte ECC DDR DRAM soldered (per Node)
					8									512 Mbyte ECC DDR DRAM soldered (per Node)
						0								Node A: no Flash
						H								Node A: 64 Mbyte Flash (2 banks)
						I								Node A: 128 Mbyte Flash (2 banks)
						J								Node A: 256 Mbyte Flash (2 banks)
							0							Node B: no Flash
							H							Node B: 64 Mbyte Flash (2 banks)
							I							Node B: 128 Mbyte Flash (2 banks)
							J							Node B: 256 Mbyte Flash (2 banks)
								1						PMC1 with full rear I/O (P0) for 3.3V
								5						PMC1 with partly rear I/O (P0) for 3.3V + SATA controller SIL3512
									0					No PMC2
									1					PMC2 with full rear I/O (P2)

									2					PMC2 no rear I/O (P2), + COPA/B + GPIO
										1				Eth 1 = 1G rear, Eth 2 = 10/100M rear
										3				Eth 1 = 1G rear, Eth 2 = 10/100M front
									0	4				Eth 1 = 1G front, Eth 2 = 10/100M front
											1			2MB L3 Cache per Node
												1		standard temp (for temp range see the HW manual)
												3		extended temp (for temp range see the HW manual)
												6		rugged (ext. temp, stiffener bar with coating)
										1		8		rugged (ext. temp, cond. cooling, wedge locks, coating)

Includes earlier versions:

V	G	5	x	x	x	x	x	x	x	x	x	C	W	
			S										Reserviert für W-Nummer (in Ausnahme Verwendung möglich)	Single CPU (Node A)
			F											Single CPU (Node A + B)
			D											Dual CPU (Node A + B)
				H										MPC7455 (600) MHz
				I										MPC7455 (800) MHz
				J										MPC7455 1000 MHz
				K										MPC7457 1000 MHz
				L										MPC7457 1267 MHz
				M										MPC7455 867 MHz
				N										MPC7457 867 MHz
					7									256 MByte ECC DDR DRAM soldered (per Node)
					8									512 MByte ECC DDR DRAM soldered (per Node)
					A									1 GByte ECC DDR DRAM soldered (per Node)
						G								Node A: 32 MByte
						H								Node A: 64 MByte Flash (one bank)
						I								Node A: 128 MByte Flash (2 banks)
						J								Node A: 256 MByte Flash (2 banks)
							0							Node B: 0 MByte (Booting via A (?))
							G							Node B: 32 MByte
							H							Node B: 64 MByte Flash (one bank)
							I							Node B: 128 MByte Flash (2 banks)
							J							Node B: 256 MByte Flash (2 banks)
								1						PMC1 with full rear I/O (P0) configured for 3.3 V (no SATA)
								2						PMC1 with partly rear I/O (P0) configured for 3.3 V + SATA 3112 (non RoHS)

								3						PMC1 with full rear I/O (P0) configured for 5 V/33 MHz only! (no SATA)
								4						PMC1 with partly rear I/O (P0) configured for 5 V/33 MHz only! + SATA 3112 (non RoHS)
								5						PMC1 with partly rear I/O (P0) configured for 3.3 V + SATA 3512
								6						PMC1 with partly rear I/O (P0) configured for 5 V/33 MHz only! + SATA 3512
									0					no PMC2
									1					PMC2 with full rear I/O (P2)
									2					PMC2 with no rear I/O (P2) + COPA/B + UGPIO
										0				no Ethernet
										1				Eth 1 = 1 Gbit rear, Eth 2 = 10 / 100 Mbit rear
									0	**2**				Eth 1 = 1 Gbit front, Eth 2 = 10 / 100 Mbit rear
										3				Eth 1 = 1 Gbit rear, Eth 2 = 10 / 100 Mbit front
									0	**4**				Eth 1 = 1 Gbit front, Eth 2 = 10 / 100 Mbit front
											0			no L3 Cache
											1			2 MB L3 Cache per Node
											2			NOT availabel (4 MB L3 Cache per Node (only with 7457))
												C		0 °C / +70 °C (for details see temp table)
												I		-40 °C / +85 °C (for details see temp table)
												R		I-style with stiffener bar and coating
										1		**N**		I-style without frontp., but conduction cooling, wedge locks, coating
												1		0 °C / +70 °C (for details see temp table)-ROHS
												3		-40 °C / +85 °C (for details see temp table)-ROHS
												6		I-style with stiffener bar and coating-ROHS
										1		**8**		I-style without frontp., but conduction cooling, wedge locks, coating-ROHS

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