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Find the *Abaco Systems / SBS WANic 524-SE* at our website: *Click HERE*

WAN*ic*® 520 Line

Low-cost Single, Dual or Quad Port WAN Adapter for PCI bus

I/O & COMMUNICATIONS

Features

Performance
- 32-bit PCI bus master
- Low latency bus mastering PCI interface
- Up to 4 full-duplex synchronous/asynchronous ports
- Up to 52 Mb/s aggregate throughput

Interface Support
- V.35/X.21/EIA-530
- T1/E1
- HSSI
- DS3/E3

Software Support
- Standard Driver Development Kit
- Linux®
- VxWorks®
- Windows® NT, Windows® 2000
- Solaris™
- FreeBSD
- Frame Relay, PPP, X.25, LAPB, LAPD, BiSYNC, X.21 bis, Annex G

Applications
- Wireless Base Stations
- Multi-Tenant and Subscription Management
- Enterprise Routing
- Enterprise VoIP Gateways
- VPN/Firewall Gateway/Routers
- Remote Access Servers

Product Reliability
- High MTBF
- Two Year Warranty
- Technical support for OEM customers and resellers



WAN*ic* 520 Line consists of low-cost multi-function PCI short form factor WAN adapters featuring a Quad Multi-protocol Controller (QMC4) that supports up to 4 full-duplex ports and an aggregate throughput of up to 52 Mb/s. The WAN*ic* 520 Line supports asynchronous, bisynchronous, and HDLC/SDLC protocols. The QMC4 is responsible for extracting HDLC frames, buffering them in local FIFOs and transferring them into host RAM via the PCI bus. The QMC4 manages link list based descriptors and data buffers directly in host RAM. The WAN*ic* 520 Line features a low latency PCI interface designed to burst into host memory at 132 MB/s.

Modular I/O Design

The WAN*ic* 520 Line is based on a modular design which features a common base adapter and a family of standard SBS physical interface modules. Each adapter has the provision for two interface modules (I/O) allowing the WAN*ic* 520 Line to support a wide range of interfaces including:

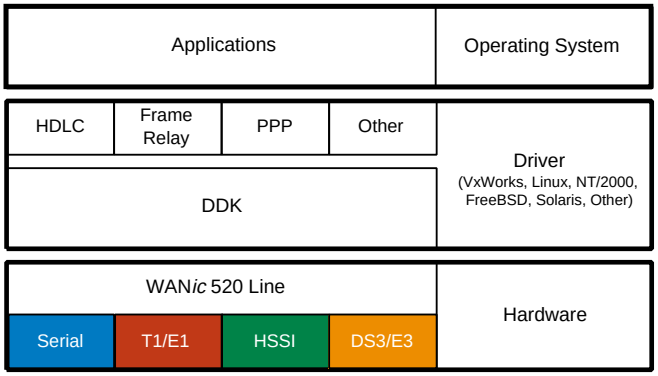
- Single, dual, and quad port serial (V.35, X.21, EIA-530)
- Single, dual, and quad port T1/E1 with embedded CSU/DSU micro for FDL, Statistic Maintenance and API
- Single and dual HSSI
- Single and dual DS3/E3 with embedded CSU/DSU micro for FDL, Statistic Maintenance and API

Almost any mix on these interfaces can be installed on a single adapter.



WAN*ic*® 520 Line

Scalable Architecture



Specifications

Technical Highlights

- PCI 2.1 compliant
- Short PCI form factor
- 32-bit PCI bus master interface
- MTBF: >100,000 hours

Interface Options

- Single, dual, or quad serial (V.35, X.21, EIA-530)
- Single, dual or quad T1, E1, CSU/DSU
- Single or dual HSSI
- Single or dual DS3, E3

T1, E1, J1 Interface Specifications

- Clear Channel and Fractional support
- D4(SF) and ESF framing
- B8ZS and AMI (T1), AMI and HDB3 (E1) line coding
- ANSI T1.403, AT&T 54016 and 62411 compliant (T1)
- ITU G.703 (120 Ohm), G.704, G.706, G.962 (E1)
- Extensive test and loopback features including master and slave clocking
- RJ-48 connectors with status LEDs

DS3, E3 Interface Specifications

- G.703 compliant 75 Ohm BNC interface
- ANSI T1.107-1195, ANSI T1.404-1994 compliant (DS3)
- G.703 & G.751 compliant (E3)

Operating Temperature

- 0˚ to 50˚C

Configurations

Model Number	Configuration
521-SE	Single port software or cable selectable serial (V.35, X.21, EIA-530)
521-T1, E1, J1	Single port T1 or E1 or J1 CSU/DSU
521-HS	Single port HSSI
521-DS, E3	Single port DS3 or E3 CSU/DSU
522-SE	Dual port software or cable selectable serial (V.35, X.21, EIA-530)
522-T1, E1, J1	Dual port T1 or E1 or J1 CSU/DSU
522-HS	Dual port HSSI
522-DS, E3	Dual port DS3 or E3 CSU/DSU
524-SE	Quad port software or cable selectable serial (V.35, X.21, EIA-530)
524-T1, E1, J1	Quad port T1 or E1 or J1 CSU/DSU

Relative Humidity

- To 90% without condensation

Power Requirements

- 1.8 A @ 5 Volts
- 0.2 A @ 12 Volts
- 0.1 A @ -12 Volts

Agency Certifications

- Emissions
- Immunity
- Safety
- Network
- CE Mark

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