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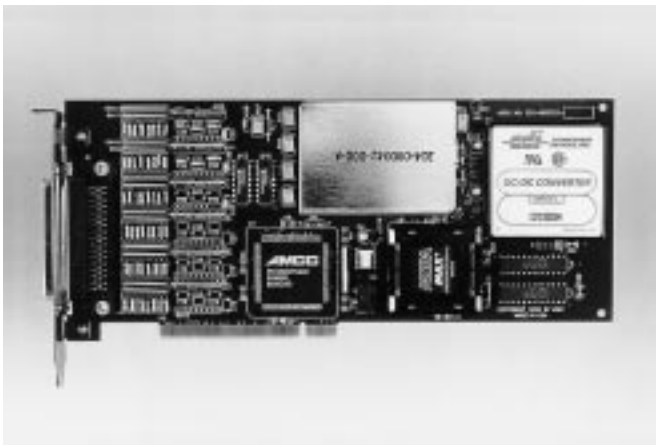
VMIPCI-3322

24-Channel 16-bit PCI Analog-to-Digital Converter (ADC) Board

- PCI local bus compliant
- 24 differential or single-ended inputs
- 16-bit A/D conversion
- Aggregate conversion rate 99.5 kHz
- Program-selectable scanning of 8, 16, or 24 channels
- Sequentially digitizes selected channels and stores the results in RAM registers
- Jumper-selectable A/D ranges of 0 to +5 V, 0 to +10 V, ±2.5 V, ±5 V, and ±10 V
- Optional low pass filters
- Overvoltage protected inputs
- Selectable output coding

APPLICATIONS

- Factory automation and instrumentation
- Process control
- Laboratory instrumentation
- Machine monitoring
- Data acquisition



INTRODUCTION — The VMIPCI-3322 is designed to operate on the industrial standard 5 V PCI local bus and supports up to 24 channels of differential or single-ended inputs within the full-scale range of ±2.5 V to ±10 V. The board continuously scans the selected inputs, converts each sample to a digital value and stores the results in dual-port data registers. The number of channels scanned is program selectable between 8, 16, or 24. The output format is program selectable between straight/offset binary and two's complement.

The VMIPCI-3322 converts at an aggregate rate of 99.5 kHz. The channel sample rate is 99.5 kHz divided by the number of channels scanned.

A functional block diagram is provided in Figure 1 and the ordering information is provided on the first page of this document.

FUNCTIONAL CHARACTERISTICS

(At +25 °C unless otherwise stated.)

PCI Local Bus Compliance: This product complies with the PCI Local Bus Specification, Revision 2.1, for 5 V boards and for active PCI clock frequencies from 10 MHz to 33 MHz

Vendor Identification: The PCI configuration register reserved for the Vendor Identification shall have the value 114A(HEX) which designates VMIC

Device Identification: The PCI configuration register reserved for the Device Identification shall have the value 3322(HEX) which designates the VMIPCI-3322 board

Base Address Register 1: The PCI configuration register called Base Address Register 1 contains the starting address of a group of 128 I/O byte address are reserved for PCI bus access of the VMIPCI-3322 functions. The base address is dynamically specified by the system BIOS. The 128 I/O addresses access the 24 dual-port data registers, 8 spare data registers, and a single Control and Status Register (CSR).

ORDERING INFORMATION

May 14, 1996 800-853322-000 A		A	B	C	–	D	E	F
VMIPCI-3322	–			0	–			

A = Input Filter Option
0 = No Filter
1 = 50 Hz (-3 dB)
2 = 100 Hz (-3 dB)
3 = 500 Hz (-3 dB)

B = Input Option
0 = Differential Analog Inputs
1 = Single-Ended Analog Inputs

C = 0 (Option reserved for future use)

CONNECTOR DATA

The I/O connector of the VMIPCI-3322 is a 50-pin D subminiature receptacle of the type AMP 205870-1. There are several compatible cable plugs and backshells from several vendors. One compatible plug is the AMP 205212-2.

For Ordering Information, Call:
1-800-322-3616 or 1-205-880-0444 • FAX (205) 882-0859
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Control and Status Register (CSR): The CSR contains 6 control bits which control the following:

- Fail/Status LED
- Software Reset
- Autoscan Enable
- Output Data Format
- Test bit
- Selected Number of Channels Scanned (8, 16, 24)

When read back as status, the CSR echo's back the states of the control bits and also includes an A/D calibration busy flag.

Fail/Status LED: The VMIPCI-3322 contains a Fail/Status LED located along the top edge of the board. The LED is only visible when the PC chassis cover is removed. The LED is controlled through a bit in the CSR.

Output Data Format: One control bit in the CSR selects the output data format between two's complement and offset binary

Reset Operations: A board reset occurs in response to a system reset or to a software reset. Immediately upon release of the reset, the A/D Converter performs a self-calibration which takes 41 ms to complete. A reset also clears the CSR to zero so the LED will be on, autoscanning will be disabled, and the output data format is offset binary.

INPUT CHARACTERISTICS

Number of Input Channels: 24 differential or single-ended channels

Full-Scale A/D Ranges: ±2.5 V, ±5 V, ±10 V, 0 to +5 V, and 0 to +10 V, jumper-selectable

Accuracy: Maximum error = ±0.005 percent Reading ±0.005 percent Range ±100 µV

Stability: Temperature drift, per °C = TBD

Input Noise: $\left(0.6 + \frac{0.3}{G}\right)$ mV rms

Input Bias Current: 40 nA maximum at zero input

Input Impedance: 5 MΩ minimum in parallel with 50 pF

Interchannel Crosstalk (DC to 1 kHz):
Adjacent Channel = 50 dB
Alternate Channel = 90 dB

Common-Mode Voltage: $[V_{CM} + (V_{DIFF}/2)]$ = ±12 V

Common-Mode Rejection: DC to 60 Hz with 350 Ω source imbalance

For the ±5 V, ±10 V, and 0 to +10 V ranges:
Minimum = 90 dB
Typical = 100 dB

For the ±2.5 V and 0 to +5 V ranges:
Minimum = 75 dB
Typical = 85 dB

Overvoltage Protection: ±35 V, sustained power On or power Off
±80 V, transient (1 s maximum)

Input Filters: Optional low pass single-pole filters:
-3 dB at 50 Hz
-3 dB at 100 Hz
-3 dB at 500 Hz

The above values apply to differential inputs. The cutoff frequency doubles for single-ended inputs. The cutoff frequency has a tolerance of ±25 percent.

The "no filter" maximum input frequency is the Nyquist limit:
2.073 kHz for 24 channels
3.109 kHz for 16 channels
6.219 kHz for 8 channels

Common-Mode/Floating Input Protection: All inputs are referenced to the on-board analog ground through 22 MΩ resistors.

TRANSFER CHARACTERISTICS

Resolution: 16 bits

Input Sampling: Sequential, starting at channel 0

Input Transfer Function:

$E_{IN} = E_{LO} + [E_{FSR} \times (N_{ADC}/65{,}536)]$

Where: E_{IN} = Input Voltage
E_{LO} = Lower End of Input Range
E_{FSR} = Full-Scale Input Range
N_{ADC} = A/D Converter Reading

Example: For a N_{ADC} value of D99A HEX (55,706 decimal) in the ±10 V Range:
$E_{IN} = -10 + [20.000 \times (55{,}706/65{,}536)] = +7.000$

Integral Nonlinearity: ±0.005 percent maximum from best straight line

Differential Nonlinearity: ±0.0015 percent, no missing codes at 16-bit resolution

A/D Conversion Rate: 99.5 kSPS

Channel Sample Rate: 99.5 ÷ 24 channels = 4.146 kSPS
99.5 ÷ 16 Channels = 6.219 kSPS
99.5 ÷ 8 Channels = 12.430 kSPS

Data Coding: Program selectable as two's complement or straight/offset binary

DATA BUFFER MEMORY

Buffer Size: 32 contiguous 16-bit data words

PCI bus Access Time: 270 ns typical (nonburst only)

PHYSICAL/ENVIRONMENTAL

Temperature: 0 to 70 °C, operating
-40 to +85 °C, storage

Relative Humidity: 10 to 80 percent, noncondensing

Altitude: 0 to 10,000 feet, operating

Cooling: Forced air convection (standard PC slot)

Dimensions: 4.2-in height x 9.4-in length (approximately 3/4 of full size)

I/O Connector: 50-pin D subminiature receptacle (AMP 205870-1)

Power: 2 A @ 5 VDC

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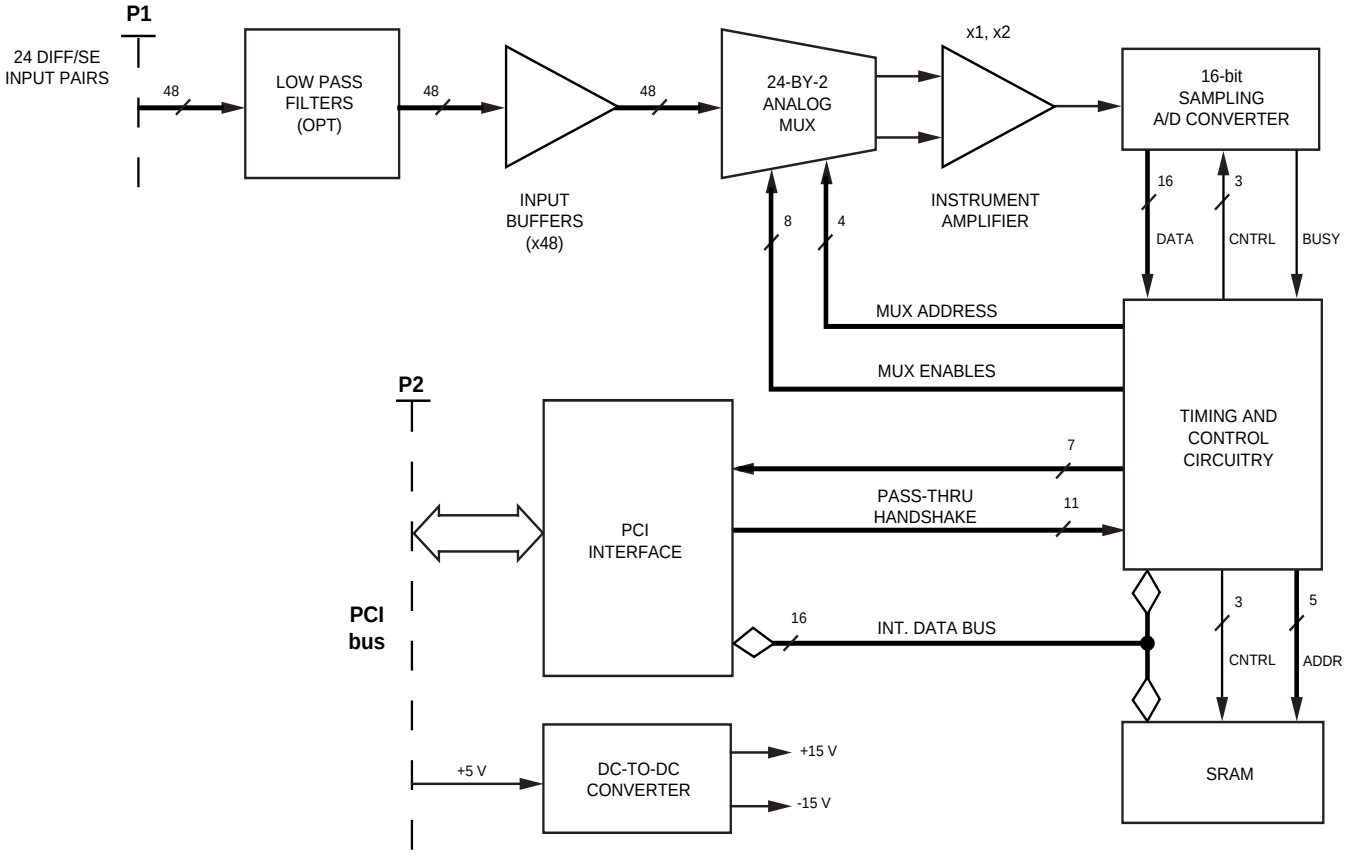


Figure 1. VMIPCI-3322 Functional Block Diagram

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