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VMIVME-2200

32-CHANNEL MERCURY-WETTED/DRY REED RELAY OUTPUT BOARD

INSTRUCTION MANUAL

DOCUMENT NO. 500-002200-000 L

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RECORD OF REVISIONS

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VMIC
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GROUND THE SYSTEM

To minimize shock hazard, the chassis and system cabinet must be connected to an electrical ground. A three-conductor AC power cable should be used. The power cable must either be plugged into an approved three-contact electrical outlet or used with a three-contact to two-contact adapter with the grounding wire (green) firmly connected to an electrical ground (safety ground) at the power outlet.

DO NOT OPERATE IN AN EXPLOSIVE ATMOSPHERE

Do not operate the system in the presence of flammable gases or fumes. Operation of any electrical system in such an environment constitutes a definite safety hazard.

KEEP AWAY FROM LIVE CIRCUITS

Operating personnel must not remove product covers. Component replacement and internal adjustments must be made by qualified maintenance personnel. Do not replace components with power cable connected. Under certain conditions, dangerous voltages may exist even with the power cable removed. To avoid injuries, always disconnect power and discharge circuits before touching them.

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Do not attempt internal service or adjustment unless another person, capable of rendering first aid and resuscitation, is present.

DO NOT SUBSTITUTE PARTS OR MODIFY SYSTEM

Because of the danger of introducing additional hazards, do not install substitute parts or perform any unauthorized modification to the product. Return the product to VME Microsystems International Corporation for service and repair to ensure that safety features are maintained.

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Warnings, such as the example below, precede only potentially dangerous procedures throughout this manual. Instructions contained in the warnings must be followed.

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DANGEROUS VOLTAGES, CAPABLE OF CAUSING DEATH, ARE PRESENT IN THIS SYSTEM. USE EXTREME CAUTION WHEN HANDLING, TESTING, AND ADJUSTING.

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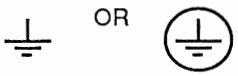
General Definitions of Safety Symbols Used in this Manual.



Instruction manual symbol: the product is marked with this symbol when it is necessary for the user to refer to the instruction manual in order to protect against damage to the system.



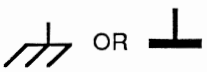
Indicates dangerous voltage (terminals fed from the interior by voltage exceeding 1000 volts are so marked).



Protective conductor terminal. For protection against electrical shock in case of a fault. Used with field wiring terminals to indicate the terminal which must be connected to ground before operating equipment.



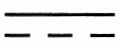
Low-noise or noiseless, clean ground (earth) terminal. Used for a signal common, as well as providing protection against electrical shock in case of a fault. Before operating the equipment, terminal marked with this symbol must be connected to ground in the manner described in the installation (operation) manual.



Frame or chassis terminal. A connection to the frame (chassis) of the equipment which normally includes all exposed metal structures.



Alternating current (power line).



Direct current (power line).



Alternating or direct current (power line).

WARNING

The WARNING sign denotes a hazard. It calls attention to a procedure, practice condition, or the like, which, if not correctly performed or adhered to, could result in injury or death to personnel.

CAUTION

The CAUTION sign denotes a hazard. It calls attention to an operating procedure, practice, condition, or the like, which, if not correctly performed or adhered to, could result in damage to or destruction of part or all of the system.

NOTE:

The NOTE sign denotes important information. It calls attention to procedure, practice, condition or the like, which is essential to highlight.

VMIVME-2200
32-CHANNEL MERCURY-WETTED/DRY REED RELAY OUTPUT BOARD

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A Assembly Drawing, Parts List, and Schematic

SECTION 1

INTRODUCTION

1.1 FEATURES

The VMIVME-2200 is a VMEbus-compatible 32-bit relay output board, shown in Figure 1.1-1. Its features include the following:

a. 8- or 16-bit data transfers
b. 32 mercury wetted or dry reed relays - configured one Form A

Mercury wetted*	Dry reed
Power - 50 VA maximum	Power - 10 VA maximum
Voltage - 350 VDC or peak AC maximum	Voltage - 200 VDC or Peak AC maximum
Current - 0.5 A maximum switched 2 A maximum carry	Current - 0.75 A maximum switched 2 A maximum carry

c. Optional contact protection electronics
d. Double-Wide Eurocard format with front panel
e. Nonprivileged or Supervisory Short I/O access

WARNING

*** MERCURY WETTED RELAYS ARE POSITION SENSITIVE. THEY MUST BE OPERATED IN THE UPRIGHT POSITION.**

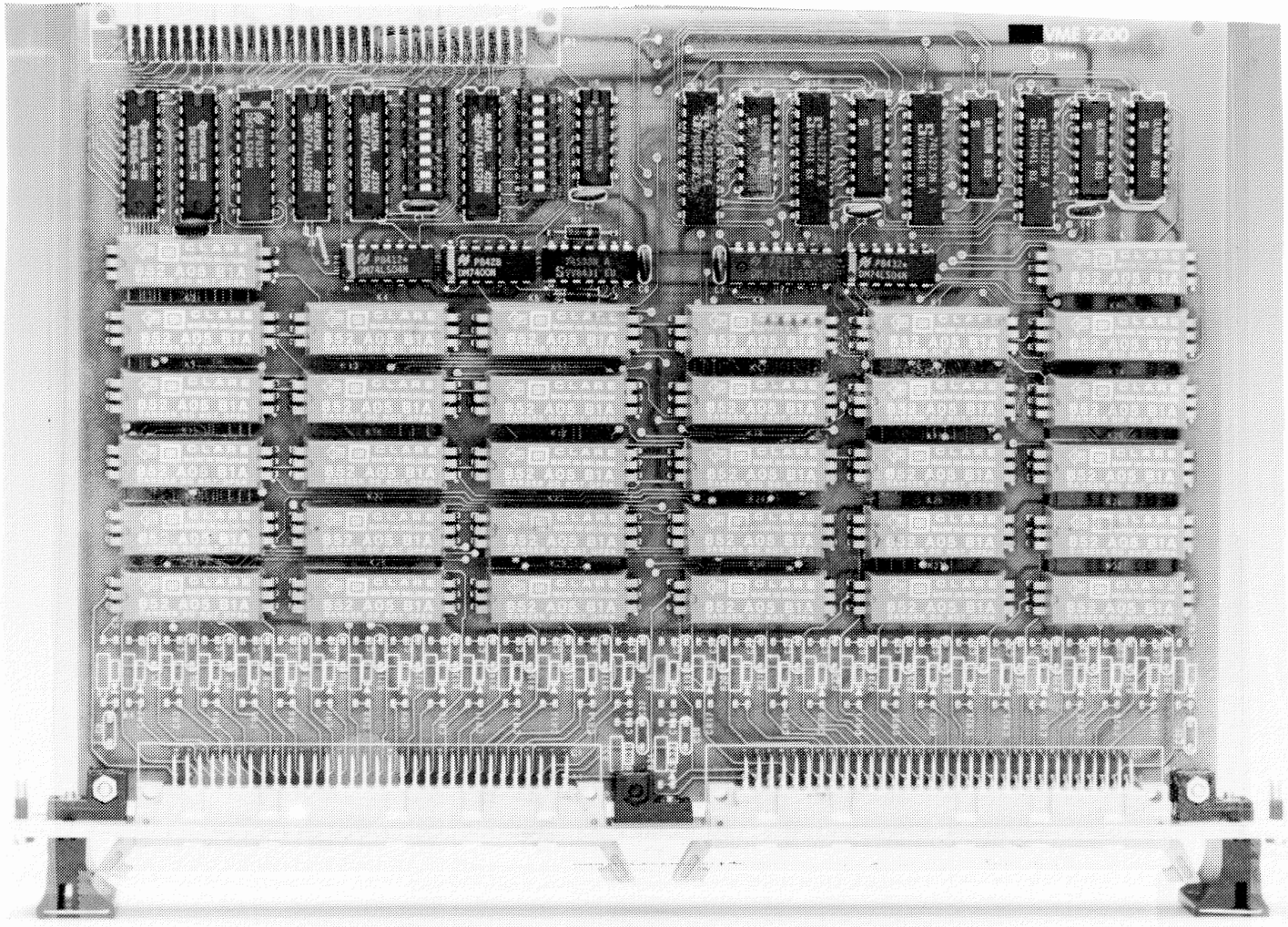
1.2 FUNCTIONAL DESCRIPTION

The Relay Output Board consists of VMEbus foundation logic, data output control logic, four 8-bit Output Registers, and 32 relay outputs. The VMEbus foundation logic contains address decoding logic, and data transfer control logic allowing 8- or 16-bit data transfers. The data output control logic, in conjunction with four 8-bit registers, selects bytes or words for transfer to the 32 output channels. The 32 relay output channels convert TTL voltages into buffered relay outputs.

1.3 REFERENCE MATERIAL

The reader should refer to "The VMEbus Specification" for a detailed explanation of the VMEbus. "The VMEbus Specification" is available from the following source:

VITA
VMEbus International Trade Association
7825 East Gelding Drive, No. 104
Scottsdale, Arizona 85260
(602) 951-8866 FAX: (602) 951-0720
Internet: www.vita.com



M2200/F1.1-1

Figure 1.1-1. VMIVME-2200 32-bit Relay Output Board

The following Application and Configuration Guides are available from VMIC to assist the user in the selection, specification, and implementation of the systems based on VMIC's products.

TITLE	DOCUMENT NO.
Digital Input Board Application Guide	825-000000-000
Change-of-State Board Application Guide	825-000000-002
Connector and I/O Cable Application Guide	825-000000-006
Digital I/O (with Built-in-Test) Product Line Description	825-000000-003

SECTION 2

PHYSICAL DESCRIPTION AND SPECIFICATIONS

REFER TO 800-002200-000 SPECIFICATION

SECTION 3

THEORY OF OPERATION

3.1 INTRODUCTION

The design of the Relay Output Board, as shown in the functional block diagram in Figure 3.1-1, consists primarily of four sections which are:

a. VMEbus foundation logic.
b. Device addressing.
c. Output registers and relay drivers.
d. 32 one Form A relays.

To perform data transfers, data is written to the Output Data Registers. The appropriate outputs are selected via address bit A01, which selects one of the two banks of 16 bits for data outputs. The two data strobes, DS0 and DS1, select word or byte access of the banks. The Data Transfer/Board Select Signal is logically gated with DS0 and DS1 to produce active signals for data transfer. These gated signals activate output latches, which latch the output data for control of the relays.

3.2 DEVICE ADDRESSING

The Relay Output Board is designed to support data transfers in supervisory or nonprivileged short I/O memory space. A jumper is provided, as shown in Figure 3.2-1 (Address Decode Block Diagram), to allow user selection of either I/O access. The jumper (J1) is factory configured (jumper J1 is not installed) to respond to short supervisory I/O access. As shown in Figure 3.2-1, 14 address select switches allow the user to establish a board base address any where in short I/O memory.

3.3 FOUNDATION LOGIC

The VMEbus foundation logic for the Relay Output Board is shown in Figure 3.3-1. This logic consists primarily of data and address buffers and control signal buffers. The DTACK generator is factory preset for maximum data transfer.

3.4 OUTPUT REGISTERS AND RELAY DRIVERS

A functional block diagram of the Output Registers and relay drivers is shown in Figure 3.4-1. Each register/driver function is designed using a 74LS273 8-bit register driving a Sprague:ULN2003A. This functional block diagram depicts the mapping of the 8-bit register outputs into the 7-bit relay driver I.C.s.

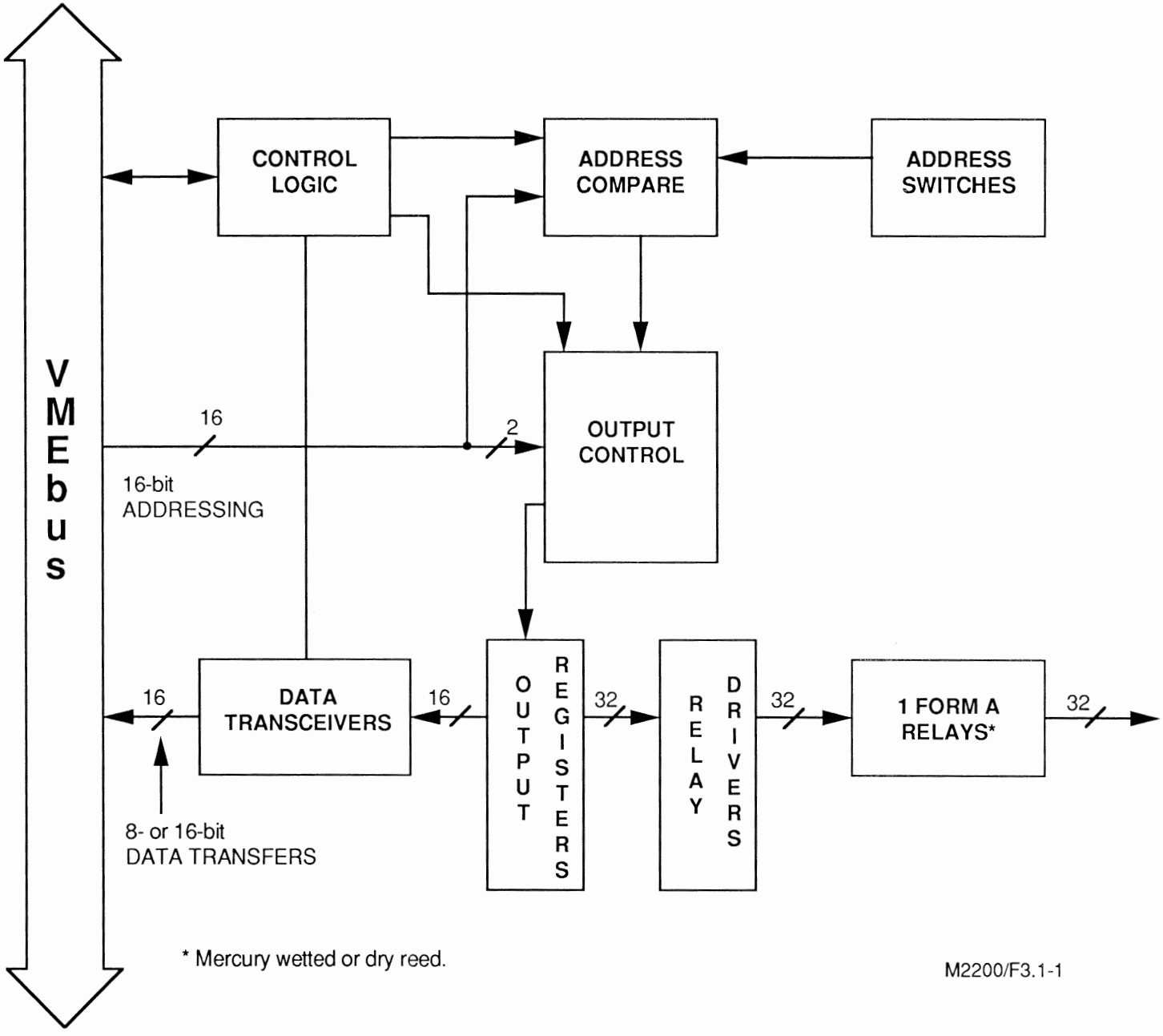


Figure 3.1-1. Functional Block Diagram

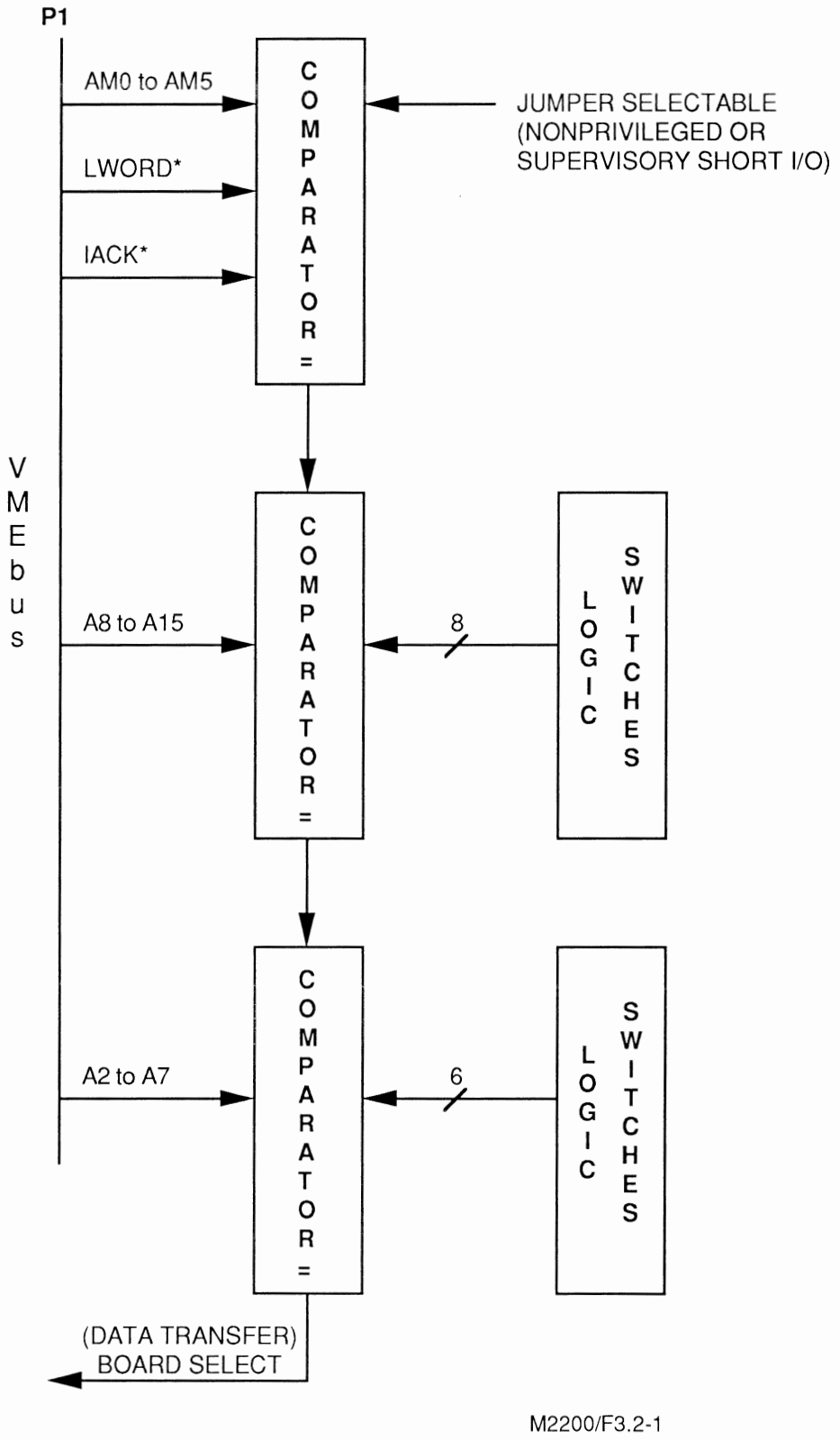


Figure 3.2-1. Address Decode Block Diagram

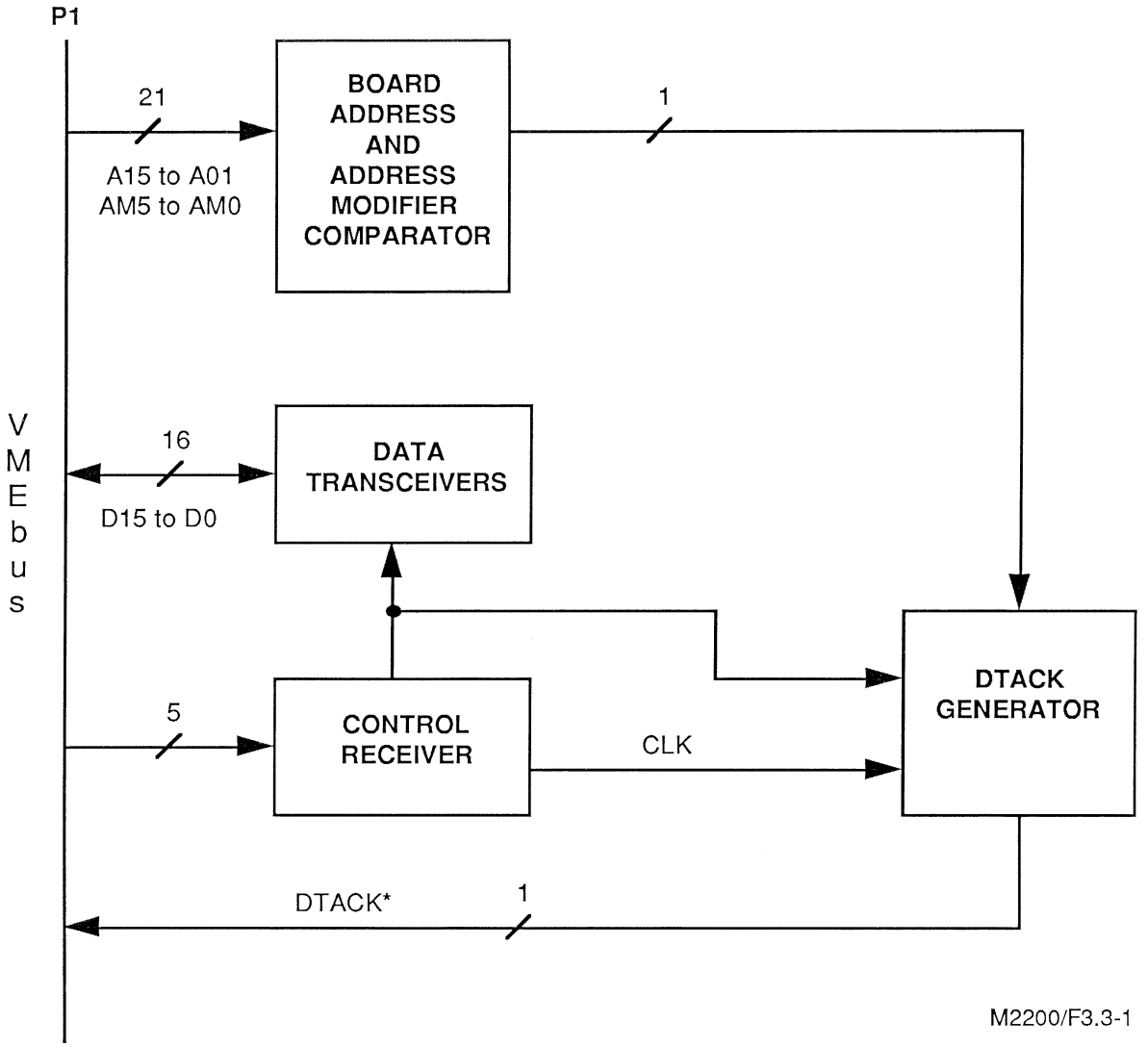


Figure 3.3-1 VMEbus Interface Logic and Interface Signals

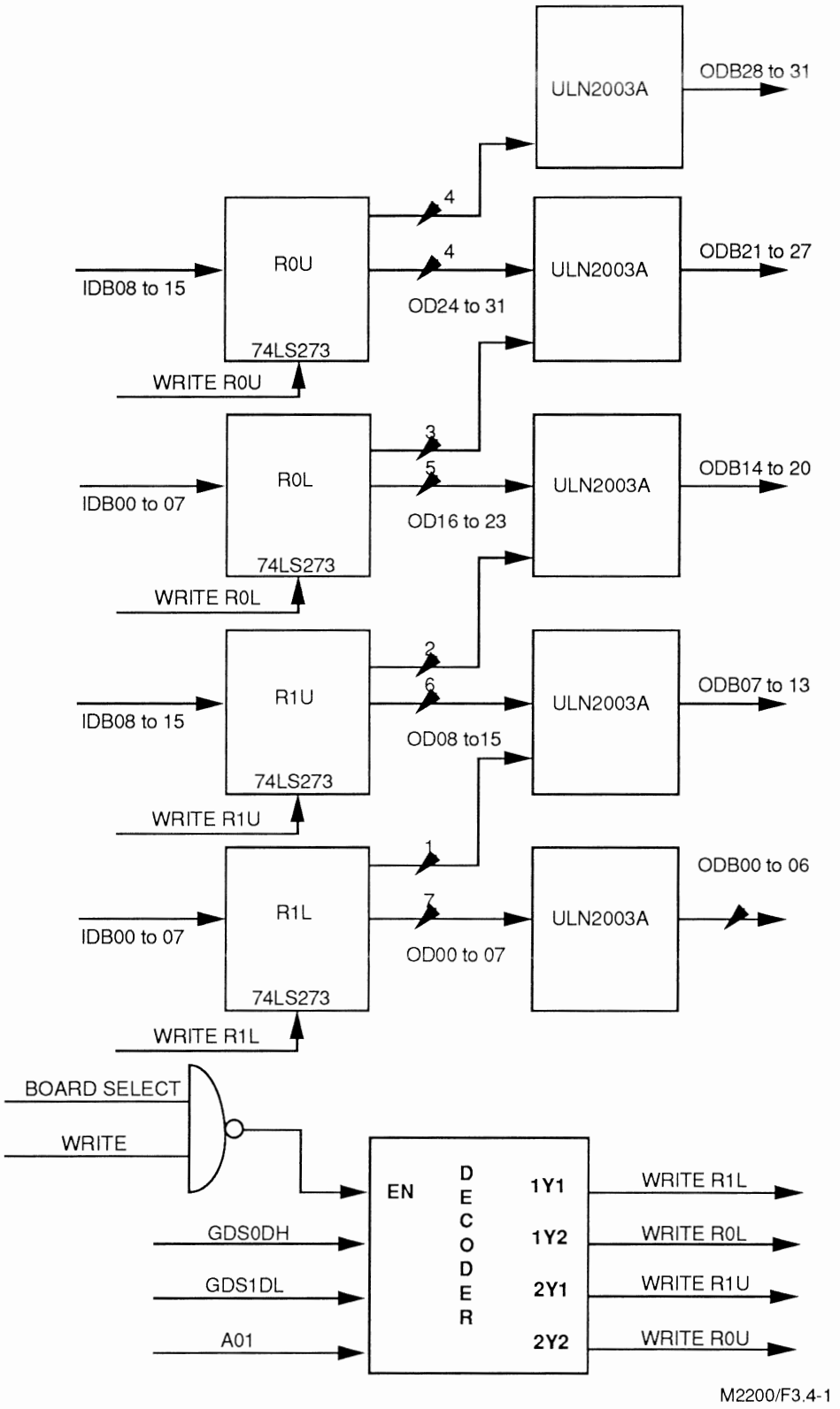


Figure 3.4-1. Output Registers and Driver Functional Block Diagram

3.5 OUTPUT RELAY

A typical output relay is shown in Figure 3.5-1. Optional contact protection electronics is shown. Each of the 32 relays is driven from the output logic shown in Figure 3.4-1. The relay contacts are routed to two front panel DIN connectors whose pin assignments are specified in Section 5. When using contact protection electronics, connect the negative supply to P3/P4 even output connector pins (A2, A4, A6, etc.) and connect the load to P3/P4 odd output connector pins (A1, A3, A5, etc.).

3.6 CONTACT PROTECTION

The use of proper contact protection is essential to achieve the specified contact life expectancy of the relays used on this board, particularly when relays are used to switch inductive loads. Circuits that may normally be considered resistive can contain capacitance and inductance in associated wiring that can cause excessive arcing of relay contacts and reduce contact life.

Inductive loads generate large transient voltages when the energizing circuit is opened. These transients can be 40 or 50 times the value of the applied supply voltage and can cause serious damage to relay contacts.

Figure 3.5-1 shows a contact protection network that is suitable for most applications where these types of relays are used and space is provided on the Relay Output Board to accommodate these components. Formulas are shown below to calculate these R and C values:

$$C = \frac{I^2}{10} \geq 0.001 \text{ MFD}$$

$$R = \frac{E}{10(I)^x} \geq 0.5 \text{ ohms}$$

where: C =microfarads

R = ohms

E = DC volts or 1.4 x rms voltage (immediately prior to contact closure)

$$X = 1 + \frac{50}{E}$$

I = amperes (immediately prior to contact opening)

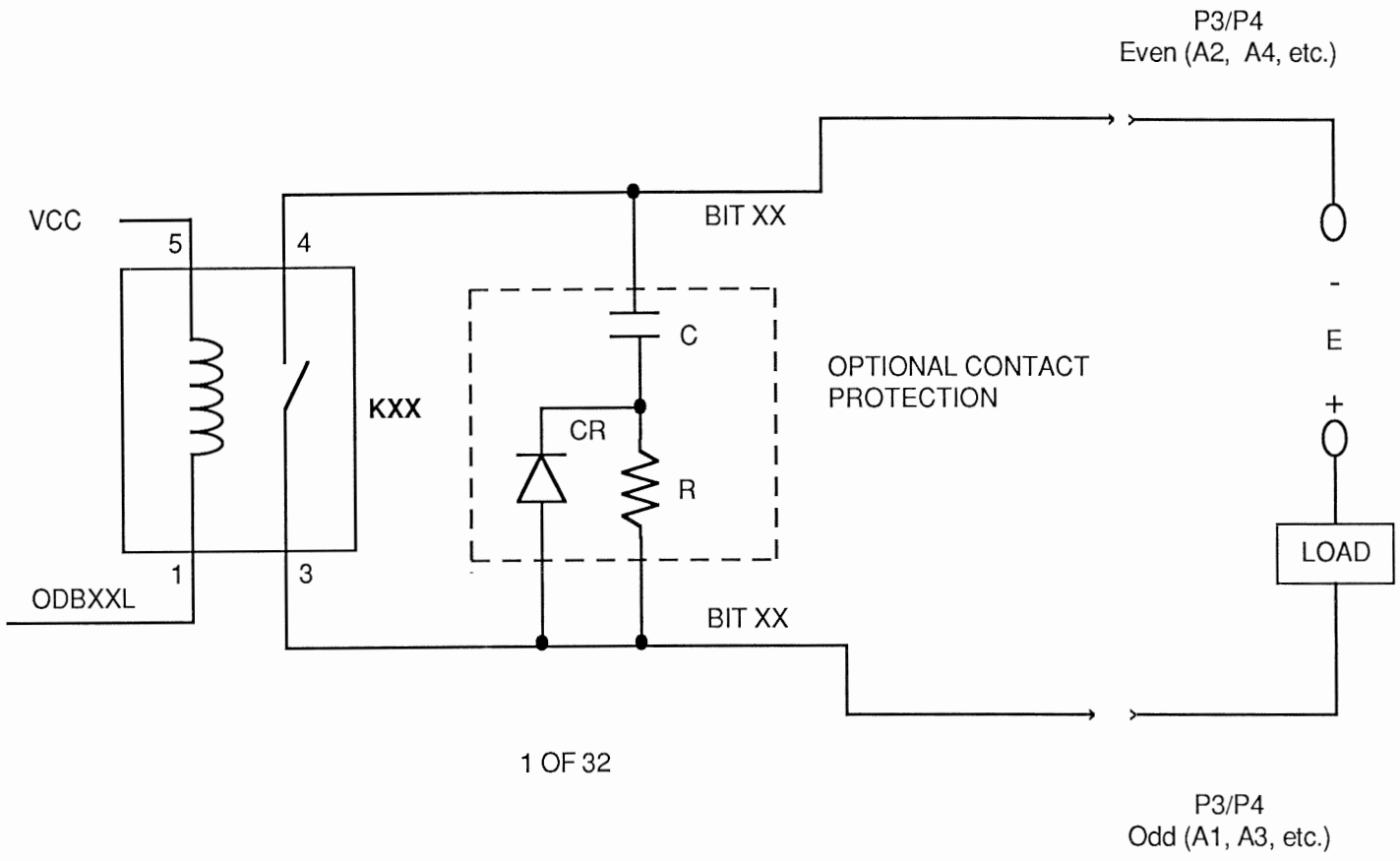


Figure 3.5-1. Typical Output Relay

M2200/F3.5-1

Also, C may be increased up to 10 times the calculated value to aid in the reduction of transients when switching inductive loads.

High current loads (capacitive or resistive) at high voltages should be avoided due to the resultant large RFI emissions that could cause false triggering of adjacent logic circuits. Additional external arc suppression may be required for applications such as switching highly inductive AC loads at voltages above 100 VAC.

The board has some specific values for these components. They were determined by using the previous formulas. Contact the factory for assistance in determining the values for the contact protection circuitry that will best fit your situation.

SECTION 4

PROGRAMMING

4.1 INTRODUCTION

The four 8-bit Output Data Registers are accessed as two 16-bit registers or as four 8-bit registers. These registers latch output data, which provide inputs to the relay drivers as specified in Section 3.

Fourteen address bits, A15 to A02, are used for address decoding to generate a board select signal which activates the board for a data transfer. The base address selection information is provided in Section 5.

Because the Relay Output Board supports only 8- or 16-bit data transfers, attempts to execute longword transfers will result in no response from the board. This will force a bus timeout error from the system.

4.2 REGISTER MAP

Address bits A15 to A02 are selected by Address Select DIP switches. Therefore, XXX in the relative address space represents that portion of the address that is switch selectable. More detailed information concerning base address selection is provided in Section 5.

RELATIVE ADDRESS	MNEMONIC	NAME/FUNCTION
$XXX0	DR0	DATA REGISTER 0
$XXX1	DR1	DATA REGISTER 1
$XXX2	DR2	DATA REGISTER 2
$XXX3	DR3	DATA REGISTER 3

4.3 REGISTER BIT DEFINITIONS

Register Bit Definitions are shown in Table 4.3-1.

Table 4.3-1. Register Bit Definitions

$XXX0 DR0

BIT 15	BIT 14	BIT 13	BIT 12	BIT 11	BIT 10	BIT 9	BIT 8
OUTPUT DATA							
OD31	OD30	OD29	OD28	OD27	OD26	OD25	OD24

$XXX1 DR1

BIT 7	BIT 6	BIT 5	BIT 4	BIT 3	BIT 2	BIT 1	BIT 0
OUTPUT DATA							
OD23	OD22	OD21	OD20	OD19	OD18	OD17	OD16

$XXX2 DR2

BIT 15	BIT 14	BIT 13	BIT 12	BIT 11	BIT 10	BIT 9	BIT 8
OUTPUT DATA							
OD15	OD14	OD13	OD12	OD11	OD10	OD9	OD8

$XXX3 DR3

BIT 7	BIT 6	BIT 5	BIT 4	BIT 3	BIT 2	BIT 1	BIT 0
OUTPUT DATA							
OD7	OD6	OD5	OD4	OD3	OD2	OD1	OD0

M2200/T4.3-1

SECTION 5

CONFIGURATION AND INSTALLATION

5.1 UNPACKING PROCEDURES

```
*****************
*    CAUTION    *
*****************
```

SOME OF THE COMPONENTS ASSEMBLED ON VMIC'S PRODUCTS MAY BE SENSITIVE TO ELECTROSTATIC DISCHARGE AND DAMAGE MAY OCCUR ON BOARDS THAT ARE SUBJECTED TO A HIGH ENERGY ELECTROSTATIC FIELD. UNUSED BOARDS SHOULD BE STORED IN THE SAME PROTECTIVE BOXES AS SHIPPED. WHEN THE BOARD IS TO BE LAID ON A BENCH FOR CONFIGURING, ETC., IT IS SUGGESTED THAT CONDUCTIVE MATERIAL BE INSERTED UNDER THE BOARD TO PROVIDE A CONDUCTIVE SHUNT.

Upon receipt, any precautions found in the shipping container should be observed. All items should be carefully unpacked and thoroughly inspected for damage that might have occured during shipment. The board(s) should be checked for broken components, damaged printed-circuit board(s), heat damage, and other visible contamination. All claims arising from shipping damage should be filed with the carrier and a complete report sent to VMIC together with a request for advice concerning the disposition of the damaged item(s).

5.2 PHYSICAL INSTALLATION

```
*****************
*    CAUTION    *
*****************
```

DO NOT INSTALL OR REMOVE BOARDS WHILE POWER IS APPLIED.

De-energize the equipment and insert the board into an appropriate slot of the chassis. While ensuring that the board is properly aligned and oriented in the supporting card guides, slide the board smoothly forward against the mating connector until firmly seated.

5.3 JUMPER AND SWITCH LOCATIONS

Refer to Figure 5.3-1 for the locations of the jumpers and switches described in this section.

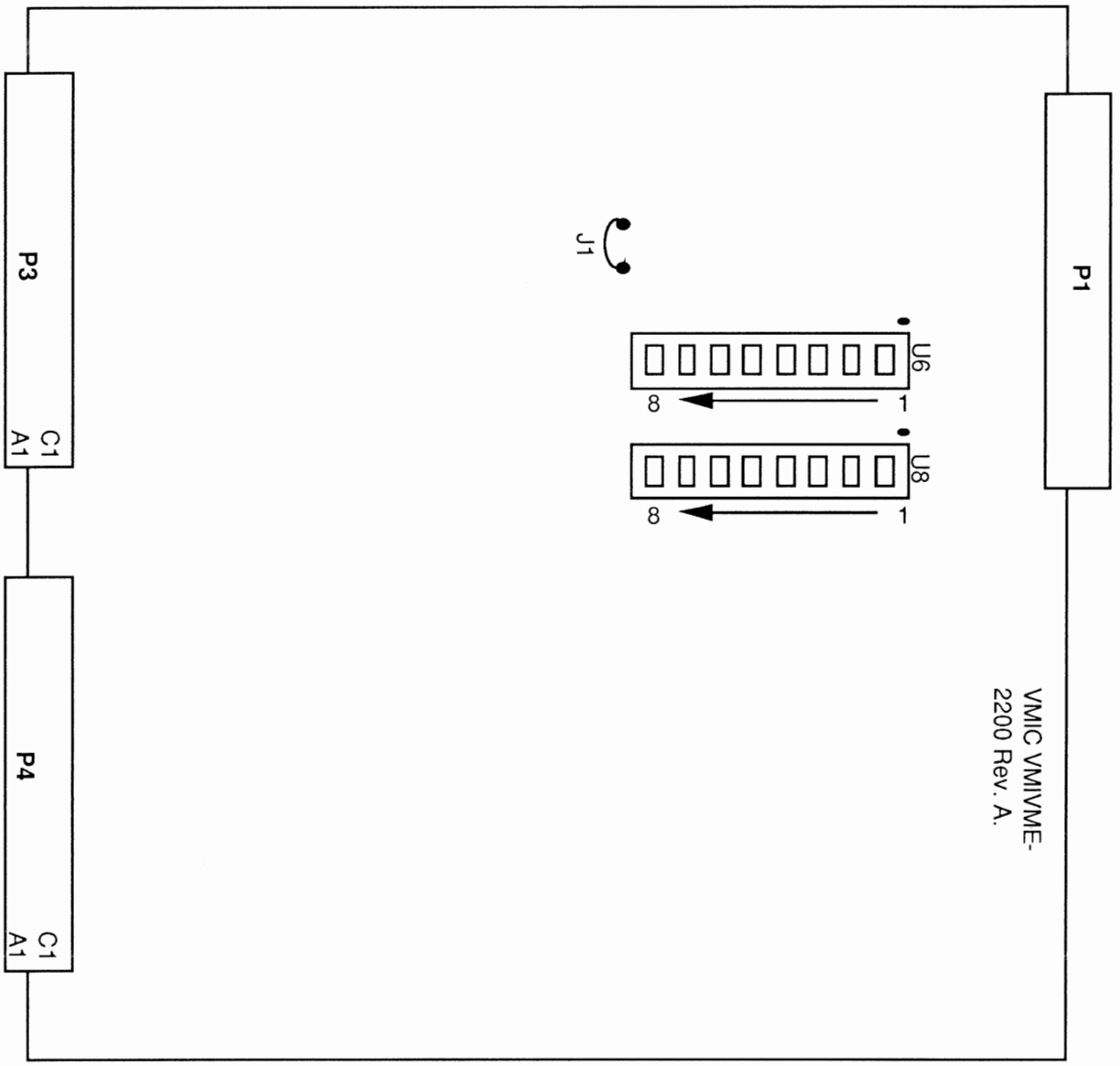


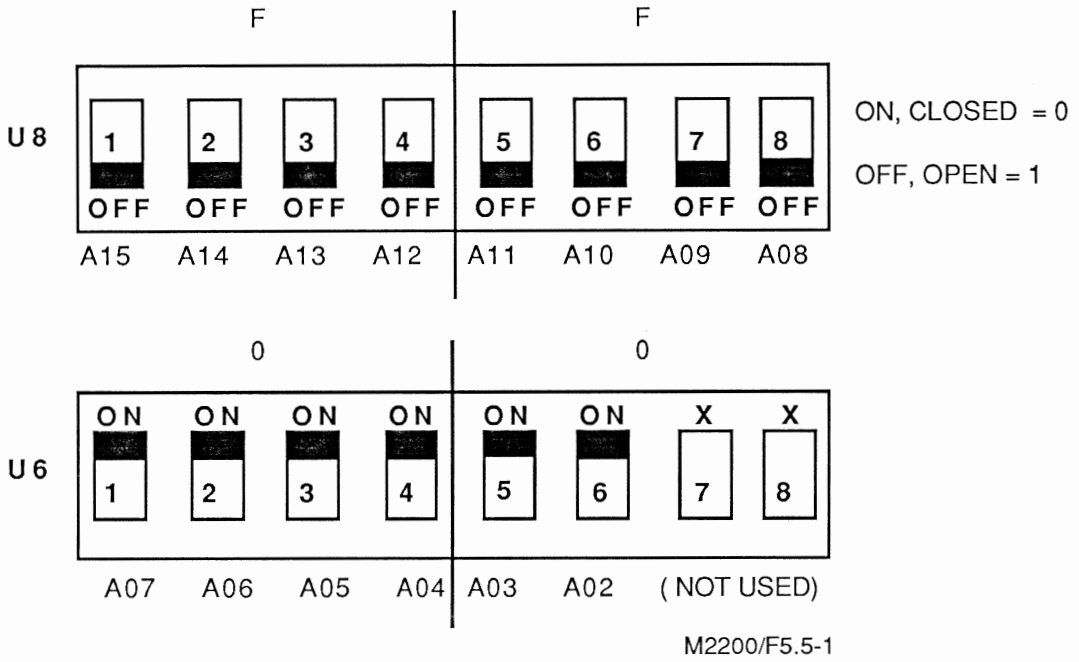
Figure 5.3-1. Jumper and Switch Locations

5.4 ADDRESS MODIFIERS

The Relay Output Board is configured at the factory to respond to short supervisory I/O access. This configuration can be changed by installing the jumper J1, to enable the board to respond to short nonprivileged I/O access.

5.5 ADDRESS SELECTION SWITCHES

Figure 5.5-1 below shows the two addressing DIP switches and their use in the addressing scheme.



Example

For the VMIVME-2200 to respond to a base address of (FF00), the U6 and U8 switches would be set accordingly.

Figure 5.5-1. Address Selection Switches

5.6 I/O CABLE AND FRONT PANEL CONNECTOR CONFIGURATION

The I/O connectors (P3 and P4) on the Relay Output Board are 64-pin DIN standard and were selected by VMIC because of their high quality. Although these connectors are generally used with flat-ribbon cables, a variety of cables and mating connectors are available for most user requirements. The user should refer to VMIC's Connector and I/O Cable Application Guide (VMIC Document No. 825-000000-006) for additional information concerning the variety of possible cabling and connector types available. I/O connector pin configurations for P3 and P4 are shown in Figure 5.6-1. Details concerning I/O connections are shown in Figure 5.6-2. This figure has conductor No. 1 shown at the bottom of the connector as it plugs into the header due to pin No. 1 of P3 and P4 being mounted as shown. Connector pin assignments for the 32 output channels of the VMIVME-2200 are shown in Table 5.6-1. When cabling to a VMIVME-2220 equipped with optional contact protection, connect the load to P3/P4 odd channel and connect the power supply negative polarity to P3/P4 even channel. See Section 3.5 for more details.

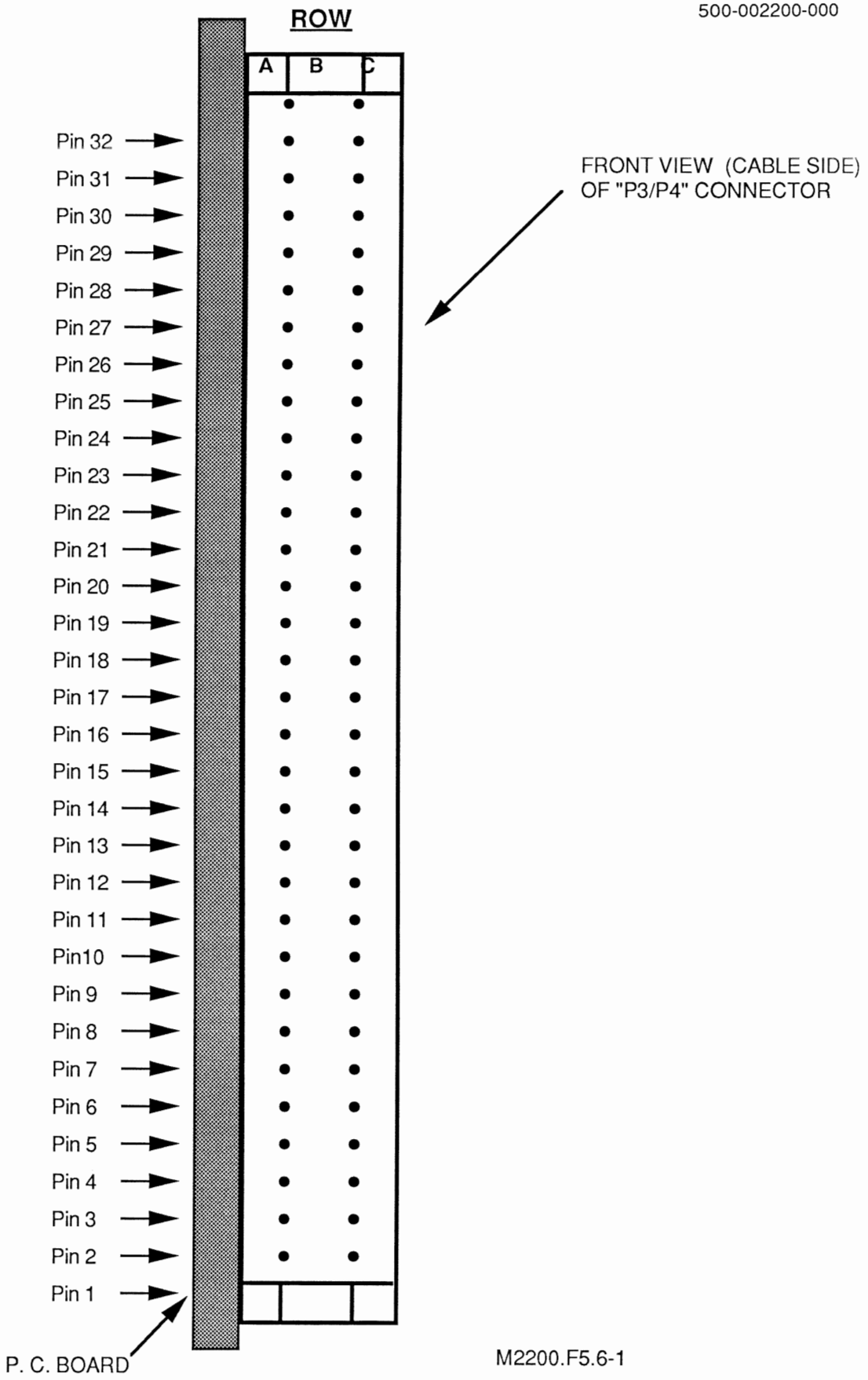


Figure 5.6-1. P3/P4 Connector Pin Assignments

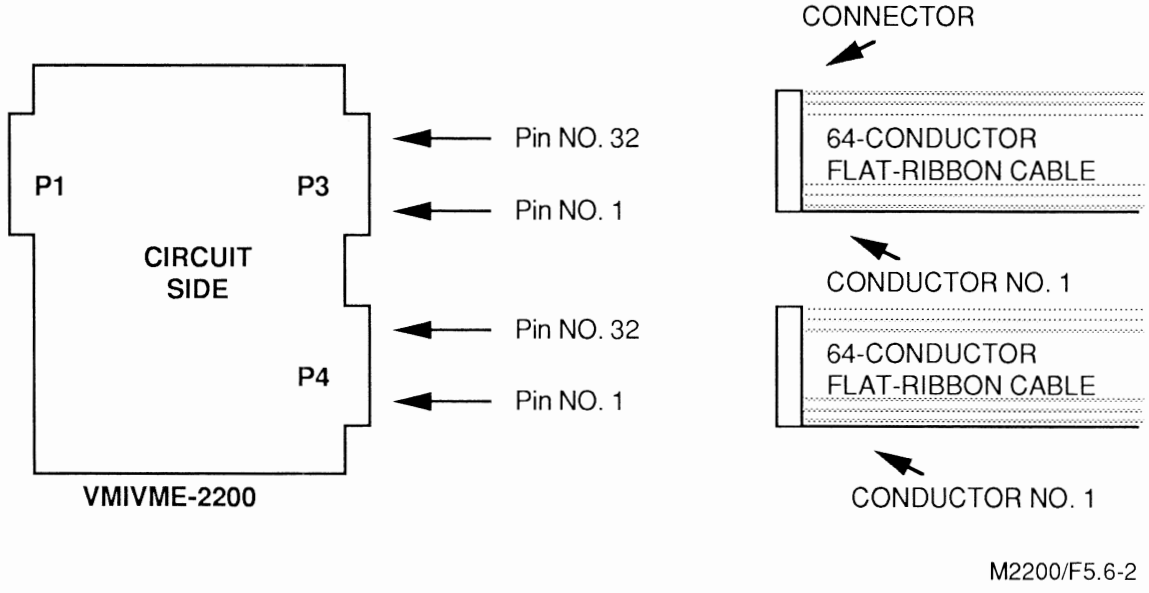


Figure 5.6-2. Cable Connector Configuration

Table 5.6-1. P3, P4 Pin/Channel Assignments

P3		P4	
A32	Bit 31	A32	Bit 15
A31	Bit 31	A31	Bit 15
A30	Bit 30	A30	Bit 14
A29	Bit 30	A29	Bit 14
A28	Bit 29	A28	Bit 13
A27	Bit 29	A27	Bit 13
A26	Bit 28	A26	Bit 12
A25	Bit 28	A25	Bit 12
A24	Bit 27	A24	Bit 11
A23	Bit 27	A23	Bit 11
A22	Bit 26	A22	Bit 10
A21	Bit 26	A21	Bit 10
A20	Bit 25	A20	Bit 09
A19	Bit 25	A19	Bit 09
A18	Bit 24	A18	Bit 08
A17	Bit 24	A17	Bit 08
A16	Bit 23	A16	Bit 07
A15	Bit 23	A15	Bit 07
A14	Bit 22	A14	Bit 06
A13	Bit 22	A13	Bit 06
A12	Bit 21	A12	Bit 05
A11	Bit 21	A11	Bit 05
A10	Bit 20	A10	Bit 04
A09	Bit 20	A09	Bit 04
A08	Bit 19	A08	Bit 03
A07	Bit 19	A07	Bit 03
A06	Bit 18	A06	Bit 02
A05	Bit 18	A05	Bit 02
A04	Bit 17	A04	Bit 01
A03	Bit 17	A03	Bit 01
A02	Bit 16	A02	Bit 00
A01	Bit 16	A01	Bit 00

M2200/T5.6-1

SECTION 6

MAINTENANCE

6.1 MAINTENANCE

This section of the technical manual provides information relative to the care and maintenance of VMIC's products. Should the products malfunction, the user should verify the following:

a. Software
b. System configuration
c. Electrical connections
d. Jumper or configuration options
e. Boards fully inserted into their proper connector location
f. Connector pins are clean and free from contamination
g. No components of adjacent boards are disturbed when inserting or removing the board from the VMEbus card cage
h. Quality of cables and I/O connections

User level repairs are not recommended. Contact VMIC for a Return Material Authorization (RMA) Number. This RMA Number must be obtained prior to any return.

6.2 MAINTENANCE PRINTS

The appendix(ices) to this manual contain(s) drawings and diagrams for reference purposes.

APPENDIX A

ASSEMBLY DRAWING, PARTS LIST, AND SCHEMATIC

DOCUMENTATION EVALUATION FORM

VMIC welcomes your comments and suggestions.

Please return this form to:

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Huntsville, Alabama 35803-3308
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Evaluation: Please rate the following areas on a scale of 1 to 5 (1 = Poor; 5 = Excellent).

DOCUMENT NO.: _____
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READABILITY	_____	ILLUSTRATIONS	_____
ORGANIZATION	_____	PROGRAMMING INFORMATION	_____
ACCURACY	_____	SPECIFICATIONS	_____
COMPLETENESS	_____	MAINTENANCE DIAGRAMS	_____

SPECIFIC PROBLEMS: **PAGE(s)**

( ) CLARIFICATION REQUIRED _____

( ) NOT ENOUGH INFORMATION GIVEN _____

( ) TYPOGRAPHICAL ERRORS _____

( ) TECHNICAL ERRORS (EXPLAIN): _____

_____

DOCUMENT USE: (check all that apply)

( ) HARDWARE	( ) SOFTWARE	( ) PRODUCT EVALUATION
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