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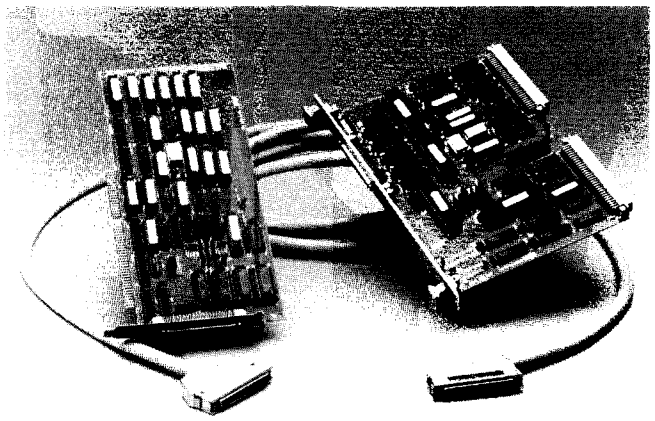
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Find the *Abaco Systems / VMIC VMIVME-5521 / VMIATX-5521* at our website: *Click HERE*

VMIATX-5521L
PC/AT (ISA) or EISA Bus to VMEbus Adapter

- Supports ISA and EISA Personal Computers with "386" or "486" Processors
- Supports A32, A24, A16, D16, D8, Master(s) and Slave(s) in the VME Chassis
- Software Controlled Dynamic Address Mapping
- Generates All VMEbus Signals Required of a VMEbus Master
- Supports 25-foot Cable
- Link Consists of One 6U VMEbus Board, One ISA Interface Board, and Cable Assembly
- Meets ANSI/IEEE STD 1014-1987



PRODUCT OVERVIEW — The VMIATX-5521L is a high performance ISA Bus-to-VMEbus link. This link allows a personal computer host, that supports a 16-bit ISA (AT) Bus or an EISA Bus, to function as a VMEbus master controlling an external VME chassis. The VMIATX-5521L can function as a master controlling a remote VME chassis; or if placed in slot one of the VMEbus, can function as the only master in the chassis.

CONFIGURATION — The VMIATX-5521L consists of two boards and an interconnecting cable. One board is an interrupting ISA Bus single-slot slave and the other board is a 6U form factor VMEbus master. Figure 1 is a block diagram of the VMIATX-5521L.

OPERATION — All single ISA Bus cycles to the memory area mapped by the ISA Interface slave board are repeated to memory mapped cycles in the VME chassis. VMEbus block transfers are not supported. If the cycle addresses a VMEbus slave, the link completes the cycle. If no slave is addressed, cycle is completed and an Error Flag is set. VMEbus interrupts may be individually configured by the user for handling by the PC CPU or by a local VMEbus CPU. 8- and 16-bit data transfers are supported.

DYNAMIC ADDRESS MAPPING — The VMIATX-5521L provides mapping registers for translating ISA Bus address cycles into VMEbus address space cycles.

The VMIATX-5521L occupies 2 Mbytes of contiguous ISA Slot memory space. This space appears as four 512 Kbyte windows (pages) that may be mapped into any VMEbus address space (short I/O, standard, and extended, in either user or supervisory mode). Mapping is accomplished by setting page registers in I/O space. Each page also has independent byte-swapping Control Registers which greatly enhance transfer of floating-point numbers between little-endian and big-endian processors. All seven VME interrupt levels may be monitored, and translated into up to four ISA interrupt requests (IRQ10, IRQ11, IRQ12, IRQ15) as configured by software. Error conditions are reported by a request on any chosen interrupt level.

Several operating system calls are required to initialize the host system for accessing the VMIATX-5521. VMIC offers an MS/DOS Driver, model number VMIATX/SW-5521.

VMEbus BUS MASTER OPERATION — The VMIATX-5521L operates in single cycle mode. Each access to the VMEbus may require the VMIATX-5521L to arbitrate for VMEbus mastership. The VMIATX-5521L is a Release-on-Request (ROR) VMEbus master, thus, arbitration may not be required every cycle. VMEbus arbitration will never be required when the VMIATX-5521L is the only bus master in the VME chassis.

EXPANSION — VMIC offers a variety of VME-to-VME repeaters, VME-to-VME DMA links, and reflective memory products that provide data communications between VMEbus chassis. Once the link to the VMEbus is established via the VMIATX-5521L, additional VME chassis may be linked to the system with these products.

ORDERING INFORMATION

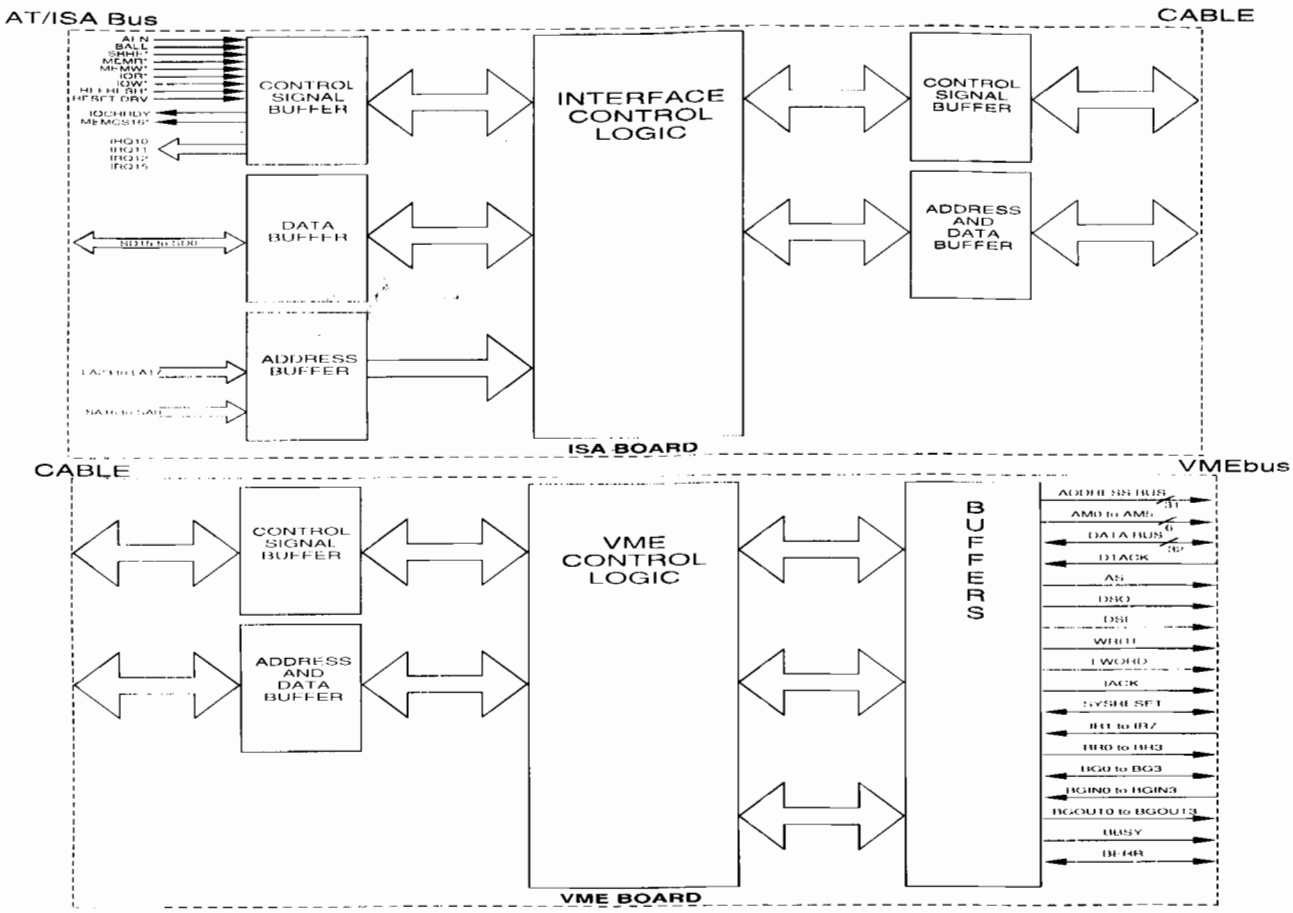
June 24, 1993 800-305521-000 B	–	A	B	C	–	D	E	F
VMIATX-5521L	–	0	0	0	–			

ABC = 000 (Options reserved for future use)

NOTE

VMIATX-5521-000 consists of a VME Board, ISA Interface Board, and a 25-foot cable.

For Ordering Information, Call:
1-800-322-3616 or 1-205-880-0444 • FAX (205) 882-0859
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SPECIFICATIONS

PHYSICAL/ENVIRONMENTAL

Temperature Range:
0 to +55 °C, operating.
-20 to +85 °C, storage.

Relative Humidity Range:
20 to 80 percent, noncondensing.

Cooling:
Convection

Size:
Double Eurocard form factor (Slave Board).
16-bit AT Card form factor (Master Board).

POWER

Requirement:
5 V @ 4.5 A (estimate for VME board).
5 V @ 1.5 A (for ISA Interface board).

TIMING

Interface Overhead:
(25-foot cable)
AS (rd/wr): (910 nsec/950 nsec).
DS (rd/wr): (1,020 µsec/1,060 nsec).
DTACK (rd/wr): (570 nsec/400 nsec).

PERFORMANCE

Measure Transfer Rate:
(25-foot cable, SRAM 5 Memory)

Read	Write
0.947 Mbytes/sec	1.010 Mbytes/sec

CONNECTION

Requirement:
One 80-conductor round cable, 25 feet long.

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