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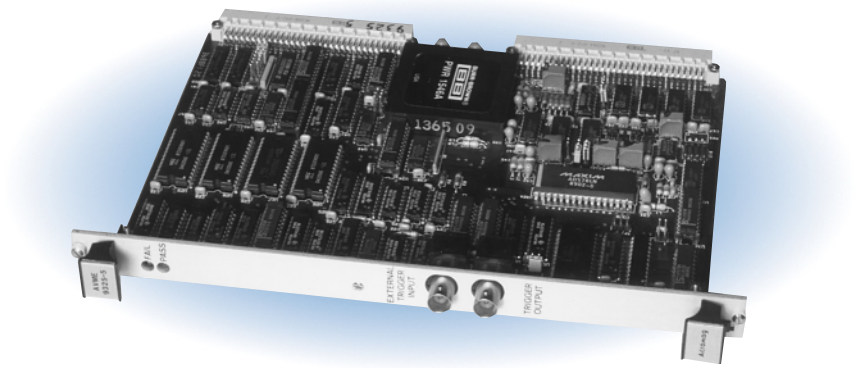
AVME9325-5 High-speed 12-bit A/D Analog Input w/RAM

This board provides a means for interfacing analog input signals to the VMEbus. On-board RAM enables high-speed data acquisition, up to 200,000 samples per second, without monopolizing the bus bandwidth.

Features

- 16 differential or 32 single-ended inputs
- 5µS/channel throughput rates
- 12-bit A/D resolution
- Three jumper-selectable input ranges: ±5, ±10, 0 to 10 volts
- 128K bytes of dual port RAM capable of storing 64K data samples
- Individual/sequential channel scanning
- Three trigger sources (external, software, internal timer)
- Pre-, post-trigger data acquisition
- Trigger output connector
- Three interrupt options:
  - Block of conversions completed
  - Half of buffer filled
  - End of conversion
- Four programmable gains: 1, 2, 4, 8



The AVME9325-5 is equipped with internal RAM for more efficient data processing with minimal burden on the host computer.

Operation

Two functional modes offer operational flexibility. The block mode performs a user-specified number of A/D conversions while continuous mode can perform an unlimited number of conversions.

In the continuous mode, a specified number of conversions can be performed after a stop trigger to allow pre- and post-trigger data acquisition.

Several trigger sources can be used to initiate the A/D conversions. Software provides host computer control over conversion triggering while the programmable internal timer enables precise periodic triggering. A BNC connector on the front panel allows triggering synchronized to external events.

Interrupts may be enabled to signal the host when a block of conversions has been completed, or as each half of dual-port RAM has been filled, or at the end of a single channel conversion.

Specifications

Analog Input

Configuration: 16 differential or 32 single-ended channels.

Input range: ±5, ±10V bipolar or 0 to 10V unipolar.

Programmable gains: x1, x2, x4, and x8.

Maximum throughput rate: 200,000 samples/second (5µS/conversion).

Settling time (20V step): 4µS to 0.01% of full scale range.

A/D conversion time: 3µS maximum.

A/D resolution: 12 bits.

No missing codes over temperature: 12 bits.

A/D nonlinearity: ±1/2 LSB.

System accuracy: 0.04% of full scale range.

Gain temperature coefficient: 50 ppm/°C maximum.

Offset temperature coefficient: 24 ppm/°C maximum.

P3, P4 connectors: BNC jack.

Environmental

Operating temperature: 0 to 70°C (32 to 158°F).

Relative humidity: 5 to 95%, non-condensing.

Isolation: Non-isolated.

RFI resistance @ 27, 151, and 467 MHz: <0.25% of full scale range at 10V/m field intensity.

Power:
+5V +0.25/-0.125 @ 3A typical.
+12V @ no load.

VME Compliance

Meets VME Specifications per revision C.1 dated October 1985 and IEC 821-1987

Data transfer bus: A24:D16/D08 (EO) DTB slave.

Address modifier codes: 39H, 3DH

Interrupts: I(1-7) jumper programmable interrupt level.

Memory Map: addressable on 256K byte boundaries in the standard address space.

Ordering Information

AVME9325-5
5µS A/D (200KHz throughput)

For software, see Page 58. For wiring hardware, see Pages 60-65. For signal conditioning, see Page 66.

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