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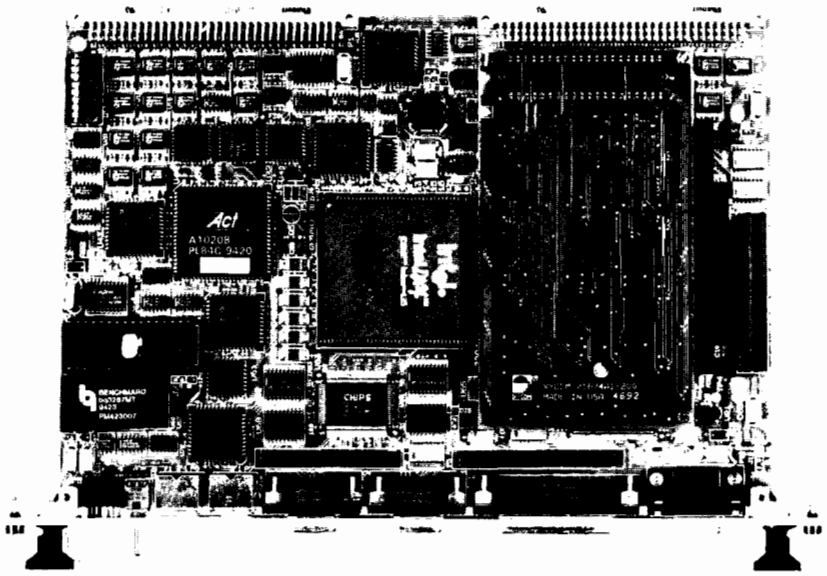
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XVME-675
VMEbus PC/AT Processor Module



Features

- Single-slot PC/AT-compatible computer
- 100 MHz 80486DX4
- 16 Kbyte on-chip cache
- 16 or 32 Mbytes of dual-access DRAM
- Upgrade path to Pentium technology
- Super VGA graphics controller
  -1024 x 768, 256 colors
  -1 Mbyte video DRAM
- PS/2-compatible keyboard/mouse ports
- Two serial ports and one parallel port
- IDE hard disk and floppy disk controllers
- 32-bit VMEbus master/slave interface
- Programmable byte-swapping hardware
- VMEbus interrupter and interrupt handler
- PC/104 expansion site
- BIOS routines handle VMEbus interface

Overview

The powerful XVME-675 uses a 100 MHz Intel DX4 microprocessor, offering a 50% increase in performance over the Intel 486DX2. Other features of the Intel DX4 include a 16 Kbyte on-chip cache, internal floating point unit, paged, virtual memory management, and complete 32-bit internal architecture. In addition, the Intel DX4 offers significant power savings as the result of enhancements with energy efficient SL technology.

With a P24T- and P24CT-compatible CPU socket, the XVME-675 provides an upgrade path to Intel Pentium® technology. It also offers a PC/104 open-architecture expansion site.

There's more to high performance than increasing clock speed. That's why the XVME-675 incorporates the latest PC/AT chipset technology. The DRAM controller uses a four-level double word buffer to access the 16 or 32 Mbytes of system RAM. This provides zero wait state writes to system DRAM.

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Hardware Description

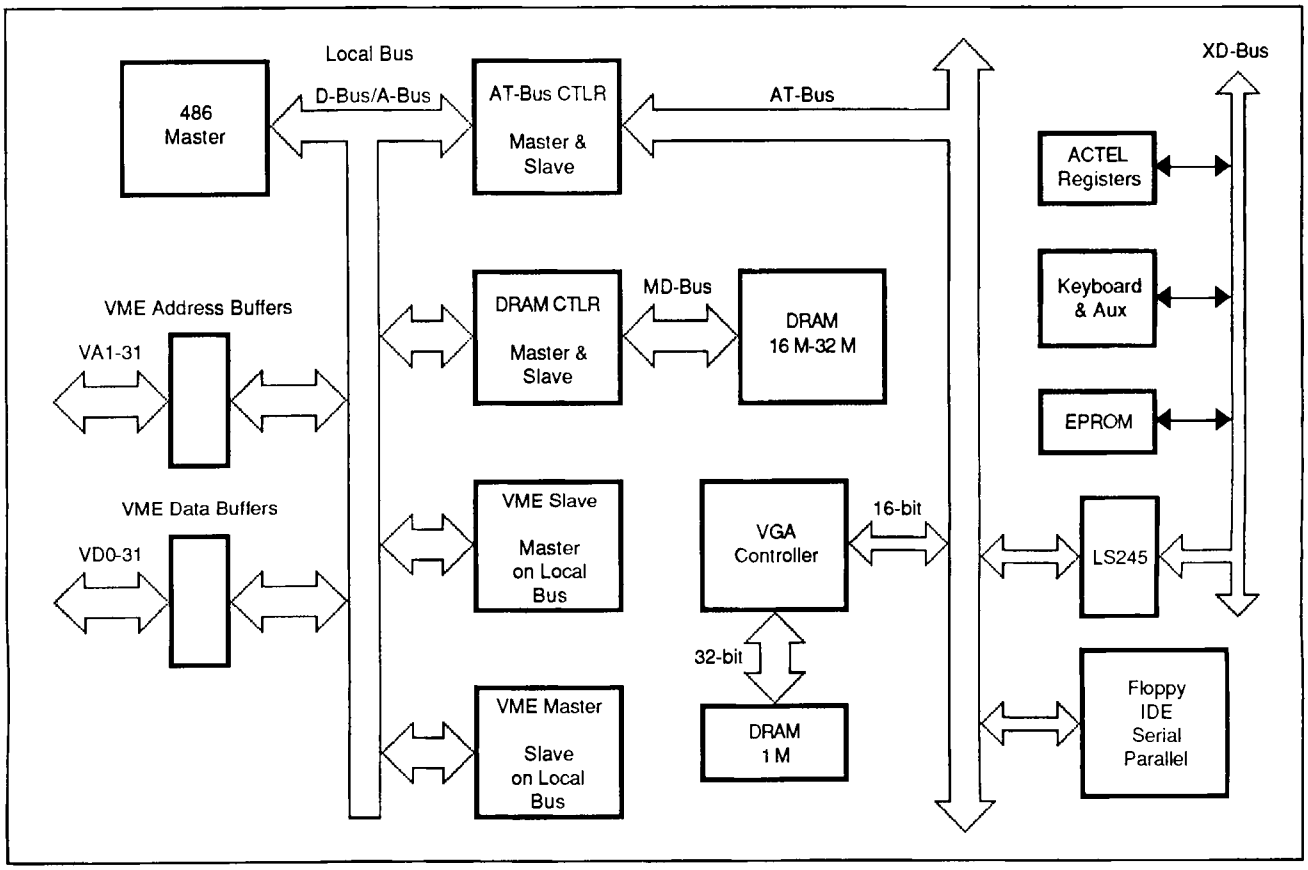
The Super VGA controller incorporates an internal FIFO that provides zero wait-state writes to the XVME-675's 1 Mbyte of video RAM. Using the Super VGA controller's hardware cursor and BitBLT engine features enhances the performance of graphical user interfaces such as Microsoft® Windows™. The XVME-675 supports resolutions of 640 x 480 with 64K colors up to 1024 x 768 with 256 colors.

Since VMEbus performance is critical to high-speed applications, the XVME-675 VMEbus interface is located on the local 80486 bus, which maximizes VMEbus access speeds. Also, the pipeline architecture of the slave interface minimizes the impact of VMEbus accesses to the dual-access DRAM. Programmable byte-swapping is provided for master and slave VMEbus cycles, further enhancing software performance.

The XVME-675 is easy to configure—all the module's system resource options are configured with a single eight-position switch. In addition, the VMEbus master and slave interface options are software configurable and can be accessed through the BIOS setup menus.

When it comes to PC/AT functional expansion, the XVME-675 makes that simple too, by incorporating an open-architecture PC/104 site on board. This features makes it easy to add functions like Ethernet or SCSI interfaces, serial and parallel ports, or solid state disks. PC/104 modules are available from over 100 suppliers, and the list continues to grow.

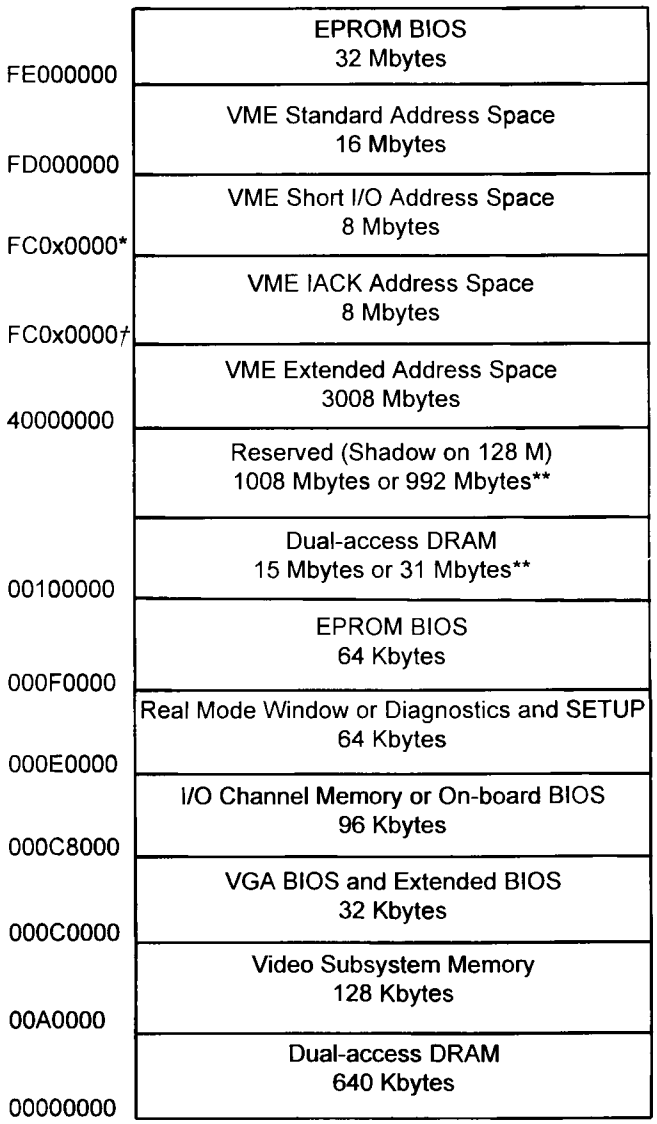
XVME-675 CPU Block Diagram



Designed to be easy to use, the XVME-675 includes BIOS routines to configure the VMEbus interface. Application programs can access the software-configurable features built into the board through BIOS interrupts or subroutine calls. Available functions include real mode window mapping, VMEbus interrupt processing, and VMEbus master and slave interface control.

The XVME-675 also provides board-level diagnostics built into the BIOS. These diagnostics can be used to test system memory, serial and parallel ports, and other module functions.

Xycom also offers software support libraries for MS-DOS®, Microsoft Windows™ (Windows NT, Windows 95, and OS/2, available 2/95), and QNX® 4.x, making it even easier to integrate the XVME-675 into your application.



**For x = 1, 3, 5, 7, 9, B, D, F; † For x = 0, 2, 4, 6, 8, A, C, E*
*** For the 16 Mbyte and 32 Mbyte versions, respectively*

Hardware Specifications

CPU Speed

XVME-675/18, /19	100 MHz 80486 DX4
P24T- and P24CT-compatible CPU socket	

Super VGA Graphics Controller

Maximum Resolution	1024 x 768, 256 colors
Video DRAM	1 Mbyte

Serial Ports (2)

RS-232C compatible

Parallel Interface

Centronics compatible

On-board Memory

Dual-access DRAM	16 or 32 Mbytes
EPROM (ROM BIOS)	256 Kbytes

Real Time Clock

Motorola 146818A compatible

Power Specifications

+12V (all versions)	24 mA, maximum
-12V (all versions)	24 mA, maximum
+5V	
XVME-675/18	4.2 A typ., 5.2 A max.
XVME-675/19	4.9 A typ., 6.1 A max.

Environmental Specifications

Temperature	
Operating	0° to 28°C (32° to 82°F) no air flow 0° to 64°C (32° to 147°F) 600 ft./min. air flow
Non-operating	-40° to 85°C (-40° to 185°F)
Altitude	
Operating	Sea level to 10,000 ft (3048 m)
Non-operating	Sea level to 50,000 ft (15240 m)
Vibration	
Frequency	5 to 2000 Hz
Operating	.015" peak-to-peak displacement 2.5 g (maximum) acceleration
Non-operating	.030" peak-to-peak displacement 5.0 g (maximum) acceleration
Shock	
Operating	30 g peak acceleration, 11 msec duration
Non-operating	50 g peak acceleration, 11 msec duration
Humidity	5% to 95% RH, non-condensing

Ordering Information

XVME-675/18	100 MHz VMEbus PC/AT Processor with 16 Mbytes DRAM
XVME-675/19	100 MHz VMEbus PC/AT Processor with 32 Mbytes DRAM
XVME-955	HD/FD unit for the XVME-675
XVME-957	HD/FD unit for the XVME-675

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