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Find the *Alpha Data ADM-XRC-II* at our website: ***Click HERE***

ADM-XRC-II

31st March 2010 **Datasheet Revision: 1.0**

Summary

The **ADM-XRC-II** is based on the Xilinx™ Virtex-2 FPGAs, provides up to 24MBytes of SSRAM and has an interface to allow specialised IO modules to be connected to the board.The **ADM-XRC-II** a complete processing system, memory, I/O and Host interface resources. It contains a PCI to local bus bridge providing the target design with a simple interface to the Host system.

Features

Applications:

DSP
Reconfigurable Computing

Target Devices:

Xilinx Virtex-2 - 2V3000, 2V4000, 2V6000,2V8000 (FF1152)

Memory:

SRAM - 6 independent ZBT SRAM banks 256K/512K/1024K x 36

DRAM - 256Mbyte via XRM-DDR

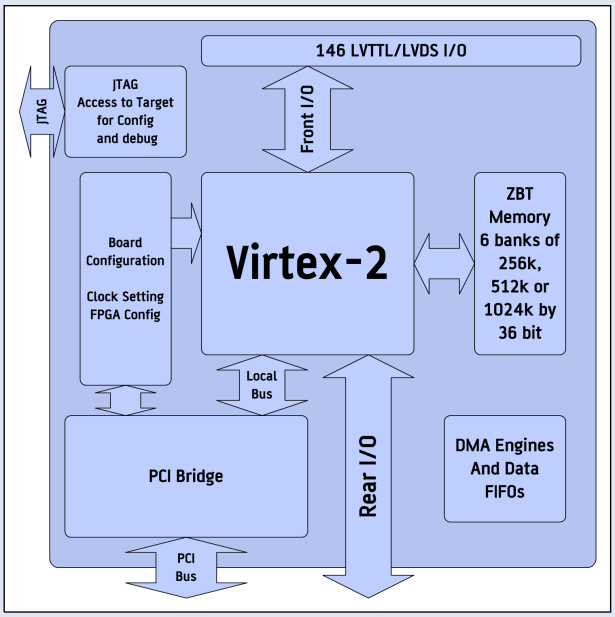
FLASH - 16MByte

Front Connector I/O:

Up to 146 LVTTL/LVDS I/O
via a range of XRM front panel adapters
Programmable signaling levels of 2.5V or 3.3V

Rear Connector I/O:

64 I/O connections via PMC Pn4 connector
programmable signaling levels of 2.5V or 3.3V



Compatible Products					
XRM-ADC-D2-125	Dual 14-bit ADC Interface (125Msps)	XRM-DAC-D3-275	Dual Channel 14-bit DAC	XRM-ETH	10/100Mbit Ethernet
XRM-FPDP	Front Panel Data Port Interface	XRM-HD-SDI	Dual HDTV I/O Interface	XRM-IO146	High Density Digital I/O
XRM-IO34	Digital I/O	XRM-MDR	Digital I/O	XRM-RIO	Parallel RapidIO Adaptor
XRM-VIDEO-IO	Video I/O interface	XRM-ZBT	ZBT Memory and RS232 Interfaces		

ADM-XRC-II

Specification	
Product Name	ADM-XRC-II
Target Devices	Xilinx Virtex-2 - 2V3000, 2V4000, 2V6000,2V8000 (FF1152)
Host I/F	PCI
Interface	PCI, 66MHz 64-bit interface PCI Bandwidth up to 533Mbytes/sec. High performace bus mastering PCI interface wih twin DMA engines.
Memory	**SRAM** - 6 independent ZBT SRAM banks 256K/512K/1024K x 36 **DRAM** - 256Mbyte via XRM-DDR **FLASH** - 16MByte
Front I/O	Up to 146 LVTTL/LVDS I/O via a range of XRM front panel adapters Programmable signaling levels of 2.5V or 3.3V
Rear I/O	64 I/O connections via PMC Pn4 connector programmable signaling levels of 2.5V or 3.3V
Clocks	Programmable clock generators
Device Configuration	PCI Bus direct to SelectMAP port From Flash direct on power up External JTAG connector
Software	Drivers for Microsoft Windows™, Linux and VxWorks API with template designs in VHDL and Verilog
Environmental	**Temperature:** Air cooled option Operating Temperature 0° to +55°C **EMC:** FCC 47CFR Part 2 EN55022 Equipment Class B

Ordering Codes		
ADM-XRC-II/z-y/m		
Virtex-II Device	z	3000,4000,6000,8000
Virtex-II Speed	y	4, 5, 6
ZBT SSRAM	m	6, 12, 24

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