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Product comparison of PXI 3030 Series models

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	3030A	3035	3030C	3035C	3036	3070A
Input Frequency Minimum	330 MHz		250 kHz			250 MHz
Input Frequency Maximum	3 GHz	6 GHz	3 GHz	6 GHz	13 GHz	3 GHz or 6 GHz
Frequency Resolution	1 Hz to 3 GHz, 2Hz to 6 GHz, 4Hz to 9GHz, 8Hz to 12 GHz					0.01 Hz
Frequency Settling Time*	<250 us		<325 us (above 500 MHz)			<150 us
Max Input Level	+22 dBm (8 dB RF atten)	+30 dBm (10dB RF atten)				
RF Input Attenuation	0 to 28 dB (step 4 dB)	0 to 31 dB (step 1 dB)				0 to 51 dB (step 1 dB)
Level Accuracy	<0.6 dB, .03 dB typ	<0.6 dB, .03 dB typ (below 3 GHz) <1.0 dB (above 3 GHz)	<1.0 dB (above 3 GHz) <2.0 dB (above 6 GHz)			<0.8 dB (above 3 GHz) <0.9 dB (above 3 GHz)
Return Loss	16 dB (below 6GHz)					14 dB typ 17 dB
Sensitivity	-152 dBm/Hz typ	-140 dBm/Hz typ	-147 dBm/Hz typ (above 500 MHz) <2.0 dB (above 6 GHz)			-160 dBm/Hz typ
Residual Spurious	-100 dBm typ					
Spurious Response	-75 dBc		-70 dBc			
LO Phase Noise (2GHz, 20kHz offset)	-116 dBc/Hz					-121 dBc/Hz
2 tone Intermodulation (0dBm)	-75 dBc					-73 dBc
Instantaneous Bandwidth (-1dB)	36 MHz		90 MHz (above 1 GHz)			160 MHz (above 900 MHz)
Amplitude Flatness +/- 0.1 dB	5 MHz					10 MHz
Amplitude Flatness +/- 0.25 dB	30 MHz	33MHz (below 2.9 GHz)	67MHz (above 1GHz)			67 MHz
Amplitude Flatness +/- 0.5 dB		33MHz (above 2.9 GHz)				100 MHz
Maximum IQ	85 MSa/s		250 MSa/s			400 MSa/s

Sample Rate	50 MSa/s	250 MSa/s	400 MSa/s
Sample Resolution	16 or 32 bit		
ADC Clock Rate	103.76 MHz	250 MHz	400 MHz
ADC Resolution	14 bit	13 bit	14 bit
Sample Memory	256 Mbyte	512 Mbyte	2 Gbyte
Real Time Data Out	LVDS upto 90 MSa/s		NA
Triggering	TTL, Data I/O, Star Trigger, Trigger Bus, Local Bus, Software		
Slots Occupied	2	3	
Slot Type	PXI 1	PXIe Hybrid Slot Compatible	

* 3010/3070A option 01 fitted

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