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Innovating the HP Way

Cost Effective Signal Distribution for System Applications

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Product Overview

HP E1750A Broadband Distribution Amplifier
HP E1752A Pulse Distribution Amplifier



The HP E1750A and E1752A bring high performance signal distribution to the VXI format.

Designed to meet the demanding needs of today's system applications in:

- site frequency distribution for manufacturing
- automatic test equipment
- distribution of communications synchronization frequencies/pulses
- dissemination of precision frequencies and pulses for calibration and timekeeping applications

Economical Signal Distribution

The HP E1750A and E1752A VXI distribution amplifiers buffer and distribute standard clock and other signals in laboratory, factory, and general ATE environments where key requirements are:

- broadband sine wave or pulse buffering/distribution
- distribution of precise clock frequencies
- distribution of precise 1 pps timing pulses
- high isolation/low cross talk between output channels
- no level adjustments
- preservation of input phase and frequency characteristics with changing environment
- channel fault alarm

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Using VXI mainframe technology, these amplifiers provide an economical way of distributing signals around a single system, or an entire manufacturing plant or site.

Fault Alarm Design Made Easy

Registers within the amplifiers allow you, via the VXIbus controller, to query the individual distribution input and output channels for presence/absence of a signal. Easy-to-read front panel status lights provide you with a quick visual indication of individual channel health. This greatly facilitates fault alarm design, a feature not found on most distribution amplifiers.

Performance for Demanding Applications

While cost-per-channel has been lowered, no compromises have been made in delivering high quality signal distribution. These amplifiers provide high channel-to-channel isolation and low noise for demanding applications. In the HP E1750A, an internal AGC design enhances performance by automatically leveling input signals and ensuring extremely low AM-to-PM conversion.

General Specifications

Input Ranges

	HP E1750A	HP E1752A
Frequency, min	100 KHz	1 pps[1]
Frequency, max	10 MHz	10 Million pulses per second [2]
Level, min	+7 dBm	Logic LO: ≤ 0.8 V
Level, max	+19 dBm	Logic HI: 2.0 V ≤ V_{in}
Damage	+27 dBm	+14 V, -9 V
Impedance	50 Ω	50 Ω

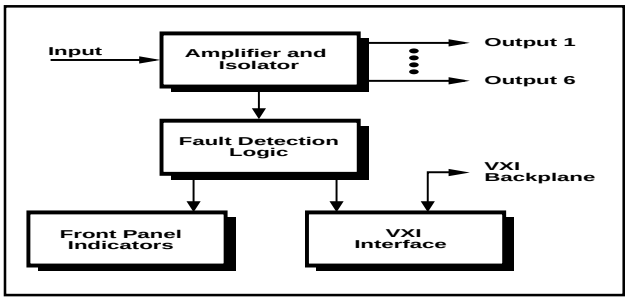
HP E1750A Outputs, 50 Ω load

Level:	+13 dBm ±1 dB
Phase noise:	<-145 dBc [3,4]
Harmonic distortion:	<-45 dBc [5]
Spurious modulation:	<-80 dBc [6]
Port-to-Port Isolation (open/short loads)	
Phase modulation	<-100 dBc
Phase change, peak	< ±0012°

HP E1752A Outputs, 50 Ω load

Rise/Fall Time:	<5 ns
Pulse Amplitude:	>3.5 V
Propagation Delay:	22 ns, typical
Jitter:	<1 ns rms

Notes:
[1] may be used with lower pulse repetition rates, with restrictions on duty cycle and fault alarm operation (see User's Guide)
[2] 50ns minimum pulse width
[3] measured 1 KHz from carrier, 1 Hz BW
[4] <-142 dBc for 5 MHz <$freq_{in}$ ≤10 MHz
[5] <-40 dBc for 5 MHz <$freq_{in}$ ≤10 MHz
[6] discrete sidebands, 10 Hz to 50 KHz



HP E1750A/E1752A Function Block Diagram

Ordering Information

HP E1750A Broadband Distribution Amplifier
Opt AV9 Delete User's Guide

HP E1752A Pulse Distribution Amplifier
Opt AV9 Delete User's Guide

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