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Agilent Technologies E5904B Option 300 Trace Port Analyzer for ARM7-ETM and ARM9-ETM Microprocessors

Data Sheet



Figure 1. Agilent E5904B option 300 trace port analyzer for ARM7 and ARM9.

- **Single box with both trace port and emulation interfaces to the CPU core**
- **Real-time debug of ASICs with ARM7 or ARM9 cores**
- **Support for ARM's Real Time Trace (RTT) and Real Time Monitor (RTM)**
- **Support for a variety of ETM implementations**
- **200 MHz operation**
- **Self powered**
- **Trigger in/out for use with another Agilent logic analyzer or oscilloscope**

Quickly and accurately determine the root cause of your team's most difficult hardware and software integration problems with Agilent Technologies' powerful trace port analysis and JTAG emulation.

Agilent's emulation and analysis solution for ARM ETM combines the powerful tools of run control, code download, debugger connections, and real-time execution trace for a complete debug environment in a small single box device. Combine the industry's leading debuggers to emulation with real-time trace to solve today's most complex ARM design problems.

Complete System Analysis

The E5904B is an integrated JTAG emulator and Trace Port Analyzer (TPA). This tool supports ARM's Real Time Trace (RTT) and Real Time Monitor (RTM) for full speed real-time system debug of ASICs with the ARM7 or ARM9 cores and the ARM Embedded Trace Macrocell (ETM).

The self-powered Agilent trace port analyzer is compatible with low power and battery-powered applications.

Trace Port Analyzer

ARM ETM microprocessors contain a dedicated trace port used for real-time execution tracing. During code execution, the CPU sends branch destination addresses, pipe line status, and optional read/write data from inside the processor core to the trace port Embedded Trace Macrocell (ETM). This information is encoded, serialized, and reduced to a small port width.

Agilent's trace port analyzer links this data to the debugger software, which interprets the trace data and reconstructs the instruction code flow. This tool provides a cost-effective, nonintrusive, real-time view of code execution. It is not necessary to run any special debug code on the target.

Because the information on code execution is broadcast from inside the microprocessor, real-time execution trace is available even when the instruction cache is enabled or when the processor is running code in on-chip memory. Real-time execution trace provides a window into the processor's cache that is unavailable by analyzing the controller's external bus.

Agilent's trace port analyzer is controlled by the debugger software via the LAN.

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ETM/JTAG Support

The E5904B supports a variety of ETM implementations including single and dual ETM connector pinout definitions and half-rate clocking. Four, eight, and sixteen bit wide trace packet implementations are supported. The JTAG emulator can support raw JTAG TCK rates up to 40 MHz and includes support for synchronized RTCK-based target systems.

Debugger Interface

A debugger must be used with the trace port analyzer. Industry-leading debuggers can control the Agilent trace port analyzer. The debugger user interface will let you:

- Download code, start/stop processor execution, single-step through a program, set breakpoints, and display/modify registers and memory.
- Set triggers, trigger sequences, etc., in the Embedded Trace Macrocell using the run control unit of the trace port analyzer.
- Collect trace information.
- Display execution flow and captured trace data.

Debugger interfaces must be ordered directly from the debugger vendor.

Debugger Connections

ARM Inc.
750 University Ave., Suite 150
Los Gatos CA 95032 USA
408 579-2200
http://www.arm.com

GreenHills Software, Inc.
30 West Sola Street
Santa Barbara, CA 93101 USA
805 965-6044
http://www.ghs.com

A System Integration Tool

Coupling an oscilloscope and/or logic analyzer to the trace port analyzer via its trigger input/output ports lets you view system operation as related to CPU status. For example, the oscilloscope's trigger output could be used to trigger the CPU's ETM when a control line is at a specified state.

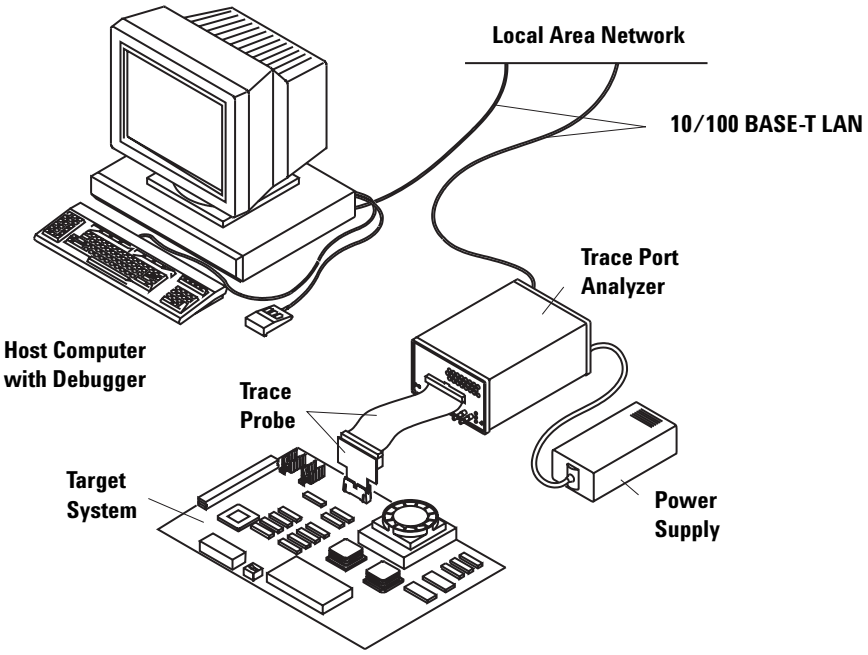


Figure 2. Typical trace port analyzer system.

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Performance Characteristics

JTAG Probe Input Characteristics

Target Connection	38 pin Mictor connector contains both JTAG and trace Status Port signals. Connector pin outs for specific processors can be obtained from http://www.agilent.com
JTAG Configuration	As per IEEE 1149.1 specification
JTAG Clock Frequency	40 MHz maximum, user selectable rate
Download Speed	400 kbytes/sec max

JTAG Input Characteristics

TDO, DBGACK, and RTCK	R_{in} = 4.7 kΩ pull up to V_{ref} C_{in}: TDO = 75 pF, BDGACK = 95 pF, DTCK = 80 pF
TDO Sampling with Respect to TCK	Selectable: falling or rising TCK Minimum required setup and hold window = 7.0 ns t_{su} = 7.5 ns, t_h = -0.5 ns
V_{ref} [1]	R_{in} = 25 kΩ pull down to ground
SRST [2]	R_{in} (inactive) = 4.7 kΩ pull up to V_{ref} R_{in} (active) = 12 Ω pull down to ground C_{out} = 200 pF

JTAG Output Characteristics

TDI, TCK, TMS, TRST, and DBGRQ	Voh/Ioh = 66 ± 15 Ω to V_{ref} Vol/Iol = 66 ± 15 Ω to 0.2 V
Delay	TDI and TMS delay from TCK falling edge to TDI and TMS valid: min 1 ns, max 2.5 ns

Trace Port Input Characteristics

Maximum Clock Frequency	200 MHz (full clock mode), 120 MHz (half clock)
Data Setup/Hold Times	1.5/1.0 ns
Clocking	Single or double edge
Data Port Width	4, 8 or 16 bits
Trace Depth	2 M maximum, 1 M with time tags
Time Tags	50 bit time tag counter with 9.6 ns resolution
Input Impedance	10 kΩ ± 5% data = 4.0 pF, clock = 6.5 pF
Multi Core Support	Yes, with daisy chained TAPS
Hot Plugging	Yes

[1] V_{ref} is used to determine the target power status and the reference for input threshold and output voltage swings. The Agilent Emulation probes do not draw power from the target system.

[2] Open collector output. Pulled up to generated voltage equivalent to the V_{ref} voltage with a 2.61 kΩ pull-up resistor.

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Performance Characteristics (continued)

Communications

LAN	RJ-45 connector IEEE 802.3 auto sensing 10/100 BASE-T Ethernet Maximum download speed [3]: 1 Mbyte/sec
Trigger Out	SMB (m) 2 V into 50 Ω load
Break In	SMB (m) input RC = 2 kΩ and 20 pF Edge triggered, TTL level Maximum input = 5 V above VCC

Power

Power	12 V dc, maximum current = 1 A
Power Supply	External module, 100 - 240 V input auto sensing 50/60 Hz, IEC 320 Connector
Operating Voltage	1.65 V to 3.6 V

Dimensions

Physical Size	105 mm (4.13 in.) wide x 151 mm (5.94 in.) deep x 62 mm (2.50 in.) high

Environmental

Temperature	Operating: 0° C to +40° C (+32° F to +104° F); Non-operating: -40° C to +60° C (-40° F to +140° F)
Altitude	Operating and non-operating: 4,600 meters (15,000 ft.)
Relative Humidity	80% @ 40° C for 24 hours
Regularity Compliance	EMC CISPR 11:1990/EN 55011:1991 Group 1, Class A; IEC 801-2:1991/EN 50082-1:1992 4 kV CD, 8 kV AD; IEC 801-3:1994/EN 50082:1993 3 V/m (1 kHz 80% AM, 27-1 kMz); IEC 801-4:1998/EN 50082-1:1992 0.5 kV Sig lines, 1 kV power lines
Safety Approvals	IEC 1010-1:1990; AMD 1:1992; UL 1244; CSA-C22.2 No. 231

[3] Maximum download speed can be limited in specific processor.

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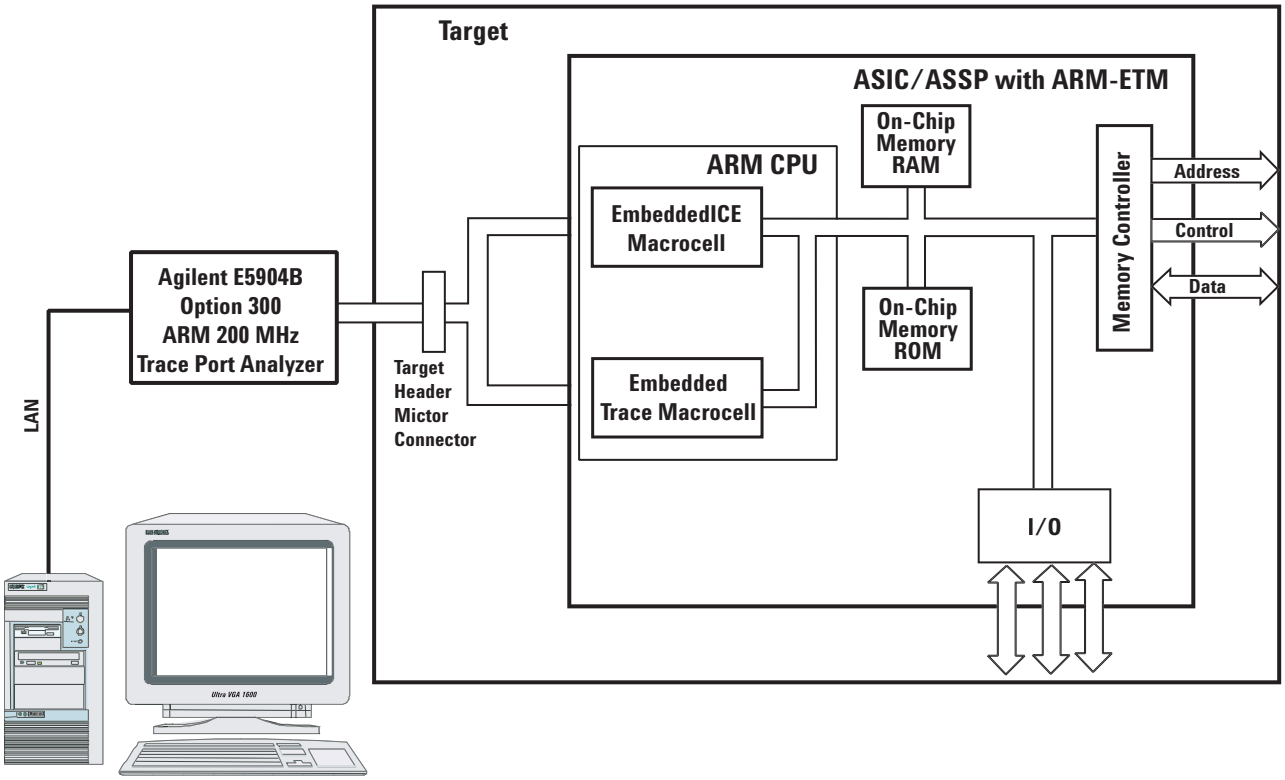


Figure 3. Real-time trace and JTAG emulation with the Agilent trace port analyzer.

Target Requirements and Design Considerations

The Agilent Technologies trace port analyzer connects to the target Mictor header with a probe. If a vertical orientation is used, refer to the user's guide for detail of required height and keep-out distance.

The Mictor connector can be modeled, if needed, as a transmission line with Z = 68 Ω and T pd = 47 pS.

Ordering Information

Part #	Description
E5904B Option 300	200 MHz ARM Trace Port Analyzer. Includes user's guide, power supply, ac line cord, ARM cable, ARM buffer board, dual ETM buffer board, performance verification board, and RG-9 LAN cable.

Recommended Accessories

Part #	Description
E5346-68701	5 vertical Mictor headers (AMP part number 2-767004-2) and support shrouds
E5346-44701	Mictor support shroud
8120-5048	SMB (f) to BNC (m) 50 Ω trigger cable 1.22 m (48 in.)

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Related Literature

Publication Title	Publication Type	Publication Number
Agilent Technologies 16700 Series Logic Analysis System	Product overview	5968-9661E

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