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Installation Guide

Agilent Technologies
N5102A Baseband Studio Digital Signal Interface Module

Due to our continuing efforts to improve our products through firmware and hardware revisions, N5102A module design and operation may vary from descriptions in this guide. We recommend that you use the latest revision of this guide to ensure you have up-to-date product information. Compare the print date of this guide (see bottom of page) with the latest revision, which can be downloaded from the following website:

http://www.agilent.com/find/basebandstudio



Agilent Technologies

Manufacturing Part Number: N5102-90003

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Documentation Change: Users Guide to Installation Guide

The N5102A Baseband Studio digital signal interface module documentation has changed. The N5102A Baseband Studio Digital Signal Interface Module User’s Guide (N5102-90001) has been changed to this manual, the N5102A Baseband Studio Digital Signal Interface Module Installation Guide (N5102-90003).

The following information, that was documented in the N5102A user’s guide, has been moved to the signal generator documentation.

- The Operation chapter information was moved to:
  - The Peripheral Devices chapter in the E8257D/67D PSG Signal Generators User’s Guide
  - The Digital Signal Interface Module chapter in the E4428C/38C ESG Signal Generators User’s Guide
- The Key descriptions were moved to:
  - E8257D/67D PSG Signal Generators Key Reference
  - E4428C/38C ESG Signal Generators Key and Data Field Reference
- The SCPI commands were moved to:
  - The Digital Signal Interface Module Commands chapter in the E8257D/67D PSG Signal Generators SCPI Command Reference
  - The Digital Signal Interface Module Commands chapter in the E4428C/38C ESG Signal Generators SCPI Command Reference

If you are using software that controls the module, you can find the information in the software’s Help system. If you do not have these manuals, a current online version is available on the Agilent Technologies web site (at http://www.agilent.com/find/psg or http://www.agilent.com/find/esg).

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1 Installation

This chapter provides the following:

- “Safety Information” on page 2
- “Getting Started” on page 4
- “Connecting the N5102A Module to the ESG/PSG or N5101A PCI Card” on page 8
- “Operation Verification” on page 11
- “Regulatory Information” on page 13

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Safety Information

Warnings, Cautions, and Notes

The following safety notations are used throughout this manual. Familiarize yourself with each notation and its meaning before operating this product.

WARNING	***Warning* denotes a hazard. It calls attention to a condition or situation that could result in personal injury or loss of life. Do not proceed beyond a warning until the indicated conditions or situations are fully understood.**

CAUTION	*Caution* calls attention to a possible condition or situation that could result in a loss of a user's work, damage, or destruction of the product. Do not proceed beyond a caution until the indicated conditions are fully understood.

NOTE	*Note* calls the user's attention to an important point of special information within the text. It provides operational information or additional instructions of which the user should be aware.

Instrument Markings

The following markings are is used on the N5102A Baseband Studio digital signal interface module. Familiarize yourself with it and its meaning before operating the module.



The CE mark is a registered trademark of the European Community. If this symbol is accompanied by a year, it is the year when the design was proven.



The CSA mark is a registered trademark of the Canadian Standards Association.

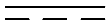


The C-Tick Mark is a trademark registered to the Australian Spectrum Management Agency. This indicates compliance with all Australian EMC regulatory information.

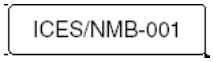
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This symbol indicates that the center conductor (of the power supply) is positive, and the outer conductor is negative.



This symbol indicates that the input power required is DC.



This symbol indicates compliance with the Canadian Interference-Causing Equipment Standard (ICES-001).

General Safety Considerations

WARNING	**Personal injury may result if the module cover is removed. There are no operator serviceable parts inside. To avoid electrical shock, refer servicing to qualified personnel.**

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Getting Started

Checking the Shipment

1. Inspect the shipping container for damage.

   Signs of damage may include a dented or torn shipping container or cushioning material that indicates signs of unusual stress or compacting.

2. Carefully remove the contents from the shipping container and verify that your order is complete.

   The following items are shipped standard with each N5102A Baseband Studio digital signal interface module:

   - installation guide
   - three-prong AC power cord (specific to geographic location)
   - power supply
   - proprietary three-meter digital bus cable
   - five break-out boards (PC boards with connectors that simplify the connections between the N5102A module and the device under test)
   - loop back fixture (for troubleshooting)
   - Device Interface port mating connector
     See "Rear Panel" on page 20 for connector locations.

Instrument Dimensions

Length:	189.9 mm (7.48 in)
Width:	144.8 mm (5.70 in)
Height:	41.6 mm (1.64 in)

Meeting Electrical and Environmental Requirements

The N5102A module is designed for use in the following environmental conditions:

- indoor use
- altitudes < 15,000 feet (4,572 meters)
- 0 to 55°C temperatures, unless otherwise specified

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- 80% relative humidity (maximum for temperatures up to 31°C, decreasing linearly to 50% relative humidity at 40°C).

CAUTION This product is designed for use in INSTALLATION CATEGORY II and POLLUTION DEGREE 2, per IEC 61010-1 and 664, respectively.

Ventilation

Ventilation holes are located on the front and rear panels of the N5102A module. Do not allow these holes to be obstructed, as they allow air flow through the module.

When installing the module in a cabinet, the convection into and out of the module must not be restricted. The ambient temperature outside the cabinet must be less than the maximum operating temperature of the module by 4°C for every 100 watts dissipated within the cabinet.

CAUTION Damage to the module may result when the total power dissipated in the cabinet is greater than 800 watts. When this condition exists, forced convection must be applied.

Line Settings

The *N5102A module* requires a power supply that meets the following conditions:

Voltage: 5V

Frequency: DC

Current: 4.0A

The module's *power supply* requires a power source that meets the following conditions:

Voltage: 100–240V

Frequency: 50-60 Hz

Current: 0.7A

CAUTION Damage may result if a supply voltage is not within its specified range.

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Connecting the AC Power Cord

This is a Safety Class 1 Product provided with a protective earth ground incorporated into the power cord. The AC power cord is the device that disconnects the mains circuits from the mains supply. In addition, an external circuit breaker, readily identifiable and easily reached by the operator, should be available for use as the disconnecting device. Use the following steps to connect the AC power cord:

WARNING	**Personal injury may occur if there is any interruption of the protective conductor inside or outside of the product. Intentional interruption is prohibited.**

CAUTION	Damage to the product may result without adequate earth grounding. Always use the supplied three-prong AC power cord.

1. Ensure that the power cord is not damaged.
2. Install the product so that one of the following items is readily identifiable and easily reached by the operator: AC power cord, alternative switch, or circuit breaker.
3. Insert the mains plug into a socket outlet provided with a protective earth grounding.

AC Power Cord Localization

The AC power cord included with the module is appropriate for the final shipping destination. You can, however, order additional AC power cords for use in different areas: see “Replaceable Parts” on page 50.

Proper Usage and Cleaning

The N5102A module cover protects against physical contact with internal assemblies that contain hazardous voltages, but does not protect against the entrance of water. To avoid damage and personal injury, ensure that liquid substances are positioned away from your N5102A module.

WARNING	**Personal injury may result if the N5102A module is not used as specified. Unspecified use impairs the protection provided by the equipment. The N5102A module must be used with all means for protection intact.**

Cleaning Suggestions

To prevent dust build-up that could potentially obstruct ventilation, clean the N5102A module cover periodically. Use a dry cloth, or one slightly dampened with water, to clean the external case parts.

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WARNING **Electrical shock may result if the N5102A module is not disconnected from the mains supply before cleaning. Do not attempt to clean internally.**

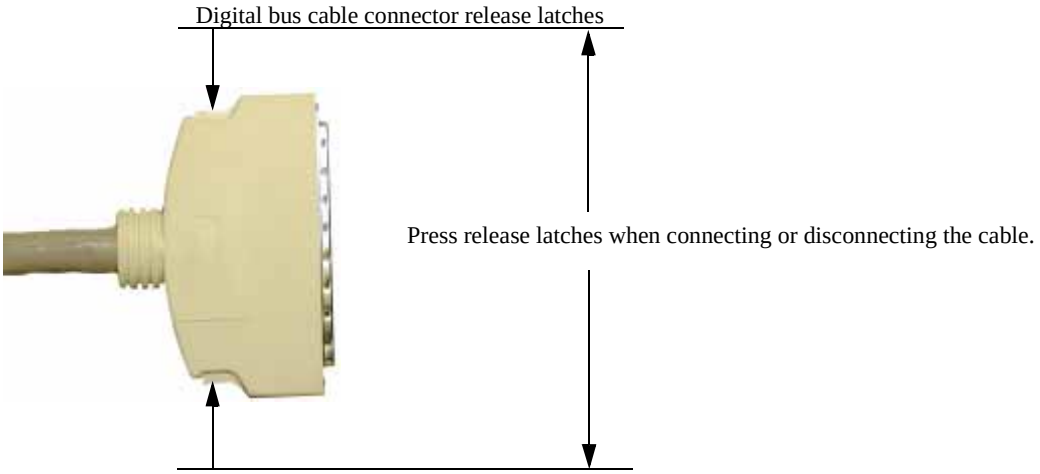
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Connecting the N5102A Module to the ESG/PSG or N5101A PCI Card

The N5102A digital signal interface module is used with an Agilent E4438C ESG[1], E8267C PSG[1], E8267D PSG[1], or a PC with an installed Agilent N5101A PCI card being used with software[2] engineered to control the N5102A module.

This section provides information on connecting the N5102A module to a signal generator or N5101A PCI card. The illustrations in this procedure show the N5102A module being connected to an ESG signal generator, however, the connection process is the same for a PSG signal generator or a PCI card installed in a PC. For more information on the N5101A PCI card, see the *N5101A Installation Guide*.

CAUTION The digital bus cable connector has a release latch on each side (as shown below). To avoid connector damage, simultaneously squeeze both release latches when connecting or disconnecting the cable. A securely connected cable does not come loose when gently pulled.



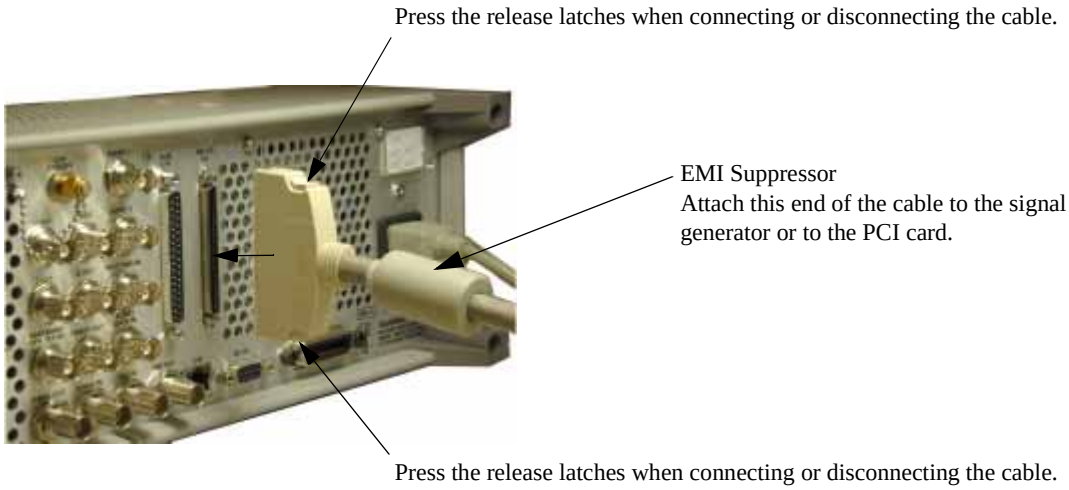
1. Refer to Figure 1-1. Connect the end of the digital bus cable that has the EMI suppressor to the signal generator or PCI card's digital bus connector.

1. Requires Options 003 and/or 004, and either 601(if available) or 602.
2. For example, the N5110B Baseband Studio for waveform capture and playback software is designed to control the N5102A module.

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NOTE The digital bus connector may be labeled as DIGITAL BUS, DIG I/Q I/O, or DIGITAL I-Q I/O.

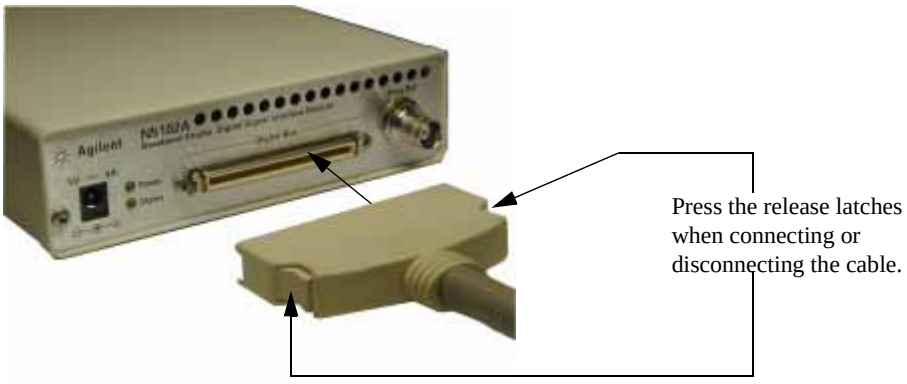
Figure 1-1 Signal Generator Digital Bus Cable Connection



2. Refer to Figure 1-2. Connect the other end of the digital bus cable to the Digital Bus connector on the N5102A module.

   The proprietary three meter cable enables you to place the interface module in a location close to the device under test (DUT).

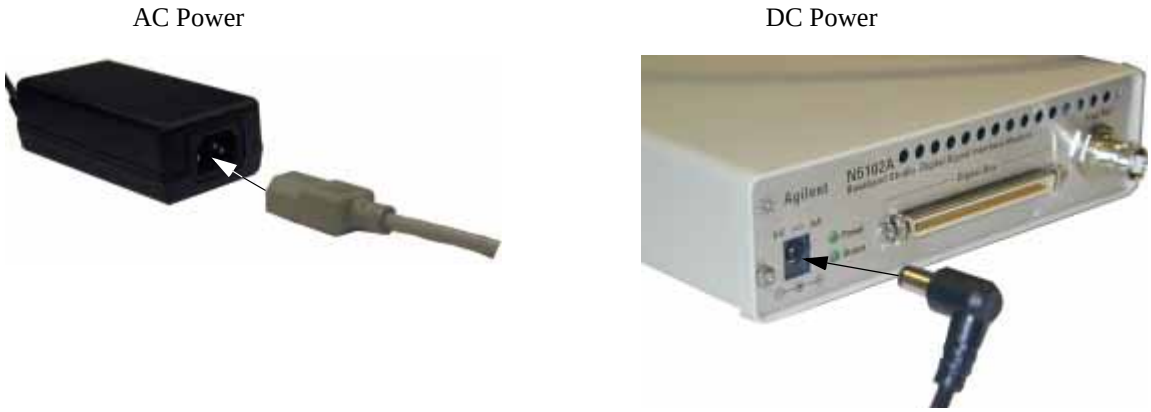
Figure 1-2 N5102A Module Digital Bus Cable Connection



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3. Refer to Figure 1-3. Connect the AC power cord to both the power supply and the AC power source (for details on connecting an AC power cord to an AC power source, see "Connecting the AC Power Cord" on page 6).
4. Connect the power supply to the N5102A module DC power receptacle.

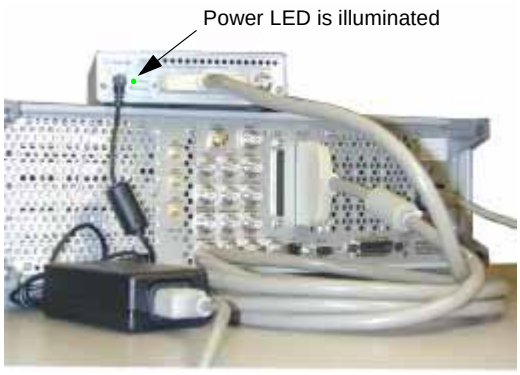
Figure 1-3 N5102A Module Power Supply Connections



The power LED should be illuminated, indicating that the interface module is connected to the power source. If the power LED is not illuminated, check the AC power connection for the power supply and ensure that the DC power supply plug is fully inserted into the N5102A module DC power receptacle. If problems still persist after checking the power cords, refer to Chapter 4, "Troubleshooting," on page 43.

Figure 1-4 shows a completed installation.

Figure 1-4 Completed N5102A Module Installation



Note:
This illustration shows the N5102A module connected to an ESG signal generator. Connections to a PSG signal generator or an N5101A PCI card are similar.

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Operation Verification

This section describes how to verify the operation of the N5102A module when connected to an ESG or PSG signal generator. The operation verification uses the device interface test (Device Intfc), which is one of four interface module diagnostic tests, referred to as loop back tests. This loop back test checks the complete setup, providing a high level of confidence that the system is functioning properly. The three other tests are used if this test fails, and are described in "Running Diagnostic Tests" on page 46.

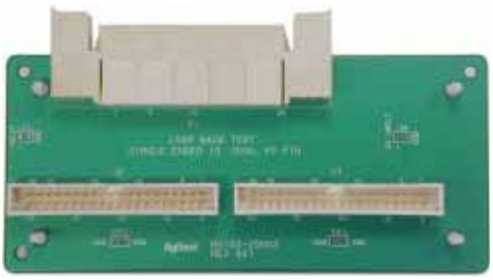
If your N5102A interface module is connected to an N5101A PCI card in a PC, operation verification occurs through the controlling software (for example, N5110B Baseband Studio for waveform capture and playback). For more information, see the software's documentation.

CAUTION The Device Interface connector on the interface module communicates using high speed digital data. Use ESD precautions to eliminate potential damage when making connections.

1. Connect the N5102A module to the signal generator (as described in "Connecting the N5102A Module to the ESG/PSG or N5101A PCI Card" on page 8).
2. Refer to Figure 1-5. Connect the Loop Back Test Single Ended IO Dual 40 Pin board to the Device Interface connector on the rear panel of the N5102A module.

Figure 1-5 Connecting the Loop Back Board to the N5102A Module

Loop Back Test Single Ended IO Dual 40 Pin Board



Connecting the Board to the Device Interface Connector



The Loop Back Test Single Ended IO Dual 40 Pin board is used both for loop back testing and as a break-out board to simplify the connection between the N5102A module and the device under test. When used for loopback testing, there should be no connections to the dual 40-pin connectors.

3. If the signal generator is not already on, turn it on.

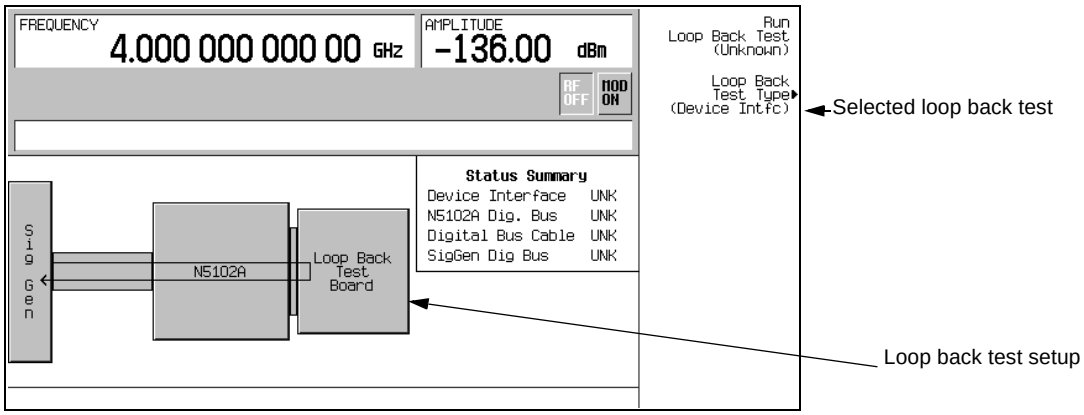
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4. On the signal generator, select the device interface test:

   Press **Aux Fctn** > **N5102A Interface** > **Diagnostics** > **Loop Back Test Type** > **Device Intfc**.

   As shown in Figure 1-6, the currently selected test is displayed in parenthesis below the **Loop Back Test Type** softkey. Note also that the graphic provided displays the current test setup.

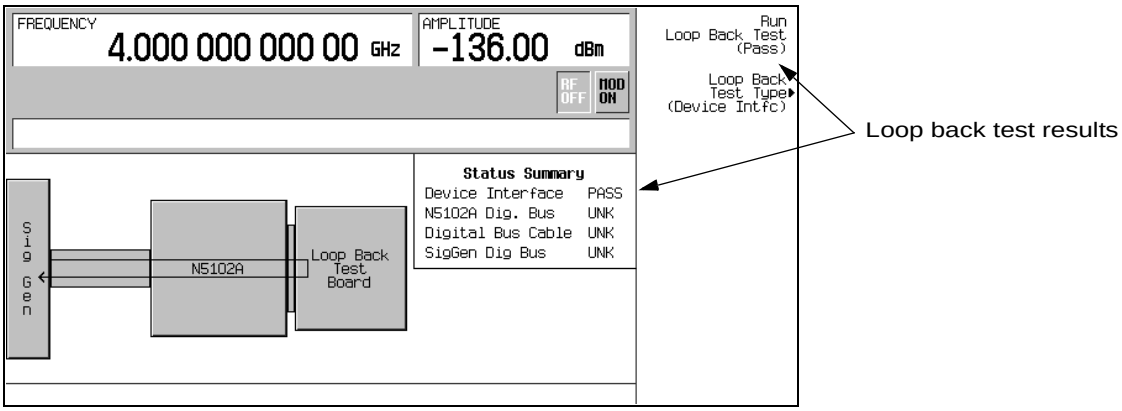
Figure 1-6 ESG / PSG Diagnostic Test Display



5. Run the selected test: press **Run Loop Back Test**.

   When the test completes, the results of the test (pass or fail) replaces Unknown in both the parenthesis with the softkey and in the Status Summary display as shown in Figure 1-7. If this test fails, refer to "Running Diagnostic Tests" on page 46.

Figure 1-7 ESG / PSG Loop Back Test Result



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Regulatory Information

Statement of Compliance

This product has been designed and tested in accordance with IEC Publication 61010, *Safety Requirements for Electronic Measuring Apparatus*, and has been supplied in a safe condition. The documentation contains information and warnings that must be followed by the user to ensure safe operation and to maintain the product in a safe condition.

Assistance

Product maintenance agreements and other customer assistance agreements are available for Agilent Technologies products. For any assistance, contact Agilent Technologies (see page 51).

Certification

Agilent Technologies certifies that this product met its published specifications at the time of shipment from the factory.

This product does not require calibration.

Declaration of Conformity

A declaration of conformity is on file for this product, and a copy is available upon request.

Compliance with German Noise Requirements

This is to declare that this instrument is in conformance with the German Regulation on Noise Declaration for Machines (Laermangabe nach der Maschinenlaermrerordnung –3.GSGV Deutschland).

Table 1-1 German Noise Requirements

Acoustic Noise Emission / Geraeuschemission	
LpA < 70 dB	LpA < 70 dB
Operator position	am Arbeitsplatz
Normal position	normaler Betrieb
per ISO 7779	nach DIN 45635 t.19

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Compliance with Canadian EMC Requirements

This ISM device complies with Canadian ICES-001.
Cet appareil ISM est conforme a la norme NMB du Canada.

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2 Overview

This chapter describes the features of the N5102A Baseband Studio digital signal interface module along with the options required for its operation.

- "Features" on page 16
- "Front Panel" on page 18
- "Rear Panel" on page 20

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Features

The N5102A Baseband Studio digital signal interface module works with the Agilent E4438C ESG[1], E8267C PSG[1], E8267D PSG[1], or N5101A PCI card installed in a PC to provide a flexible digital interface for delivering digital baseband (IQ) or digital intermediate frequency (IF) test signals. The complex modulation formats of the signal generators, including W-CDMA, multitone, 1xEV-DV, WLAN and many more, are available at the bit level for testing digital components, transceivers, and subsystems. The N5102A module delivers the digital IQ or digital IF[2] signals to your device with the data requirements, clock features, and signaling you need. With its selection of logic types and break-out board connectors, the interface module connects into your test system, in most cases eliminating the need for custom fixtures.

The N5102A module provides many features:

- output mode (requires Option 003 for ESG/PSG or Option 195 for N5110B waveform playback)
- input mode (requires Option 004 for ESG/PSG or Option 194 for N5110B waveform capture)
- bit level access to arbitrary waveform generator (ARB) and real-time signal generator baseband data from a wide range of signal creation applications
- simple user interface
- flexible data formats
  - variable 4-bit to 16-bit word size
  - serial, parallel, and parallel interleaved data transmission
  - 2’s complement or offset binary word representation
  - MSB or LSB bit order
  - digital IQ or digital IF[2] signal
- flexible clocking
  - automatic resampling
  - 1 kHz to 100 MHz sample rate
  - multiple clock inputs and outputs
  - adjustable clock phase and skew
  - multiple clocks per sample 1x, 2x, and 4x (Output mode only. Input mode is limited

1. Requires Options 003 and/or 004, and either Option 601 or 602.
2. Digital IF only available for output mode.

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to 1x.)

- flexible signal interface
  - — multiple logic types provide single ended and differential testing capability—low voltage TTL (LVTTL), LVDS, and CMOS 1.5 V, 1.8 V, 2.5 V, and 3.3 V
  - — proprietary three meter digital bus cable connects the N5102A module to the signal generator
  - — interchangeable break-out boards

When connected to an ESG or PSG signal generator the parameters for the N5102A module are set using the user interface (UI) on the ESG/PSG. This provides familiar softkey operation for both the modulation format and the interface module. Option 003 (output mode) and Option 004 (input mode) on the ESG/PSG enable the N5102A module user interface on the signal generators.

When connected to an N5101A PCI card, the parameters for the N5102A module are set using the UI on the controlling software (for example, N5110B Baseband Studio waveform capture and playback). This provides intuitive operation for both the waveform setup and the interface module in a single software application.

With the capability of connecting an N5102A module to a signal generator or an N5101A PCI card, you can perform multiple levels of testing. Since the baseband data that is provided to the interface module is the same data that can be modulated onto the RF carrier, this enables early-stage testing of digital components and subsystems with the N5102A module, and then testing the integrated system using the modulated RF carrier.

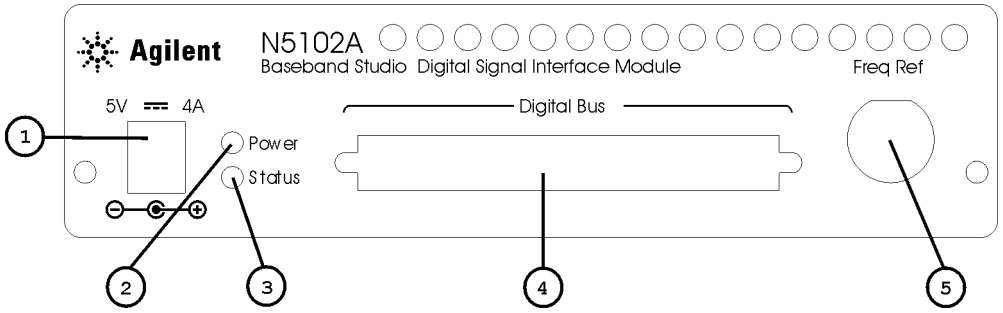
The N5102A digital interface module does not support the Real Time GPS format.

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Front Panel

The baseband data and frequency reference inputs for the N5102A module are located on the front panel along with the receptacle for the DC power. A Power LED indicates when DC power has been applied and a Status LED shows when the data lines are active.

Figure 2-1 Front Panel Features



1. DC Power Receptacle

This receptacle accepts the DC power cord from the power supply. A DC power cord is shipped with the interface module.

2. Power LED

This LED illuminates when DC power is supplied to the N5102A module.

3. Status LED

This LED illuminates when the interface module is first turned on by an ESG, PSG, or controlling software.

- For the ESG/PSG, press the **N5102A Off On** softkey in the signal generator UI or after performing a module diagnostic test. Once lit, the LED stays on until the DC power is removed from the interface module.
- For controlling software, refer to the software's Help system for module control information.

The LED conveys the status of the data lines and has two modes of operation:

Blinks Rapidly	This indicates that the data lines are active and ready to transmit or are transmitting a digital signal.
Solid Illumination	The data lines are inactive.

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4. Digital Bus Connector

The N5102A module uses this connector to communicate with the ESG, PSG, or PCI card. A proprietary three-meter digital bus cable is supplied that connects to the Digital Bus connector.

5. Freq Ref Connector

When Internal is the selected clock source, the clock is referenced to this 50W connector. This connector accepts an external clock at 3 dBm ±6 dB within the frequency range of 1 MHz to 100 MHz.

CAUTION It is important that the interface module, the DUT, and the signal generator (if one is being used) are locked to a common frequency reference. Failure to have a common frequency reference may result in a loss of data. Also refer to the common frequency reference information in:

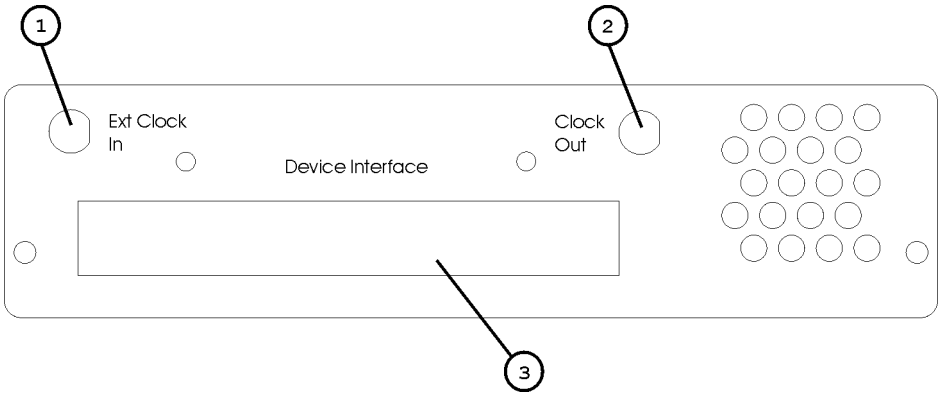
- E4428C/38C ESG Signal Generators User's Guide Digital Signal Interface Module chapter
- E8257D/67D PSG Signal Generators User's Guide Peripheral Devices chapter
- Help system in the software that controls the N5102A module.

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Rear Panel

The rear panel has three connectors that are shown in Figure 2-2 and are described in the following sections.

Figure 2-2 Rear Panel Features



1. Ext Clock In Connector

This AC coupled 50W connector is used for connecting an external clock source to the N5102A module. It accepts a signal with a nominal amplitude of 0 dBm and has a frequency range of 1 MHz to 400 MHz.

CAUTION It is important that the interface module, the DUT, and the signal generator (if one is being used) are locked to a common frequency reference. Failure to have a common frequency reference may result in a loss of data. Also refer to the common frequency reference information in:

- E4428C/38C ESG Signal Generators User's Guide Digital Signal Interface Module chapter
- E8257D/67D PSG Signal Generators User's Guide Peripheral Devices chapter
- Help system in the software that controls the N5102A module.

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2. Clock Out Connector

This 50W connector outputs the clock signal at a nominal 400 mVp-p level with a frequency range of 100 kHz to 400 MHz. For a frequency range of 1 kHz to 100 kHz, a high impedance load of 5 kW produces a nominal 2 Vp-p clock signal.

3. Device Interface Connector

This connector interfaces with the device under test and supplies the digital IQ and digital IF signals in addition to sense lines, ground connections, a DC supply, and input and output clock signals. For more information on this connector, including a pin-out diagram, see "Device Interface Connector" on page 34.

CAUTION The Device Interface connector on the interface module communicates using high speed digital data. Use ESD precautions to eliminate potential damage when making connections.

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3 Device Interface Connections

This chapter provides information for the N5102A module Device Interface connector, the supplied break-out boards, and the device interface mating connector.

- “Break-Out Boards” on page 24
- “Device Interface Connector” on page 34
- “Device Interface Mating Connector” on page 39

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Break-Out Boards

This section describes the different break-out boards and provides pin-out diagrams for each one.

To maximize signal integrity, make the device connection as close as possible to the N5102A module Device Interface connector. The break-out boards are supplied to aid in minimizing this distance. An alternate solution is to incorporate the device interface mating connector onto the device under test (DUT). This eliminates the need for the break-out board and connecting cables.

Five interchangeable break-out boards are supplied with the N5102A module, each with a different type of connector, providing a wide range of connection possibilities. The break-out boards connect to the Device Interface connector on the rear panel of the module. The different boards are easily identified by their connector. If the situation arises where none of the break-out boards mate with the device being tested, make a customized connection solution using the device interface mating connector. See "Device Interface Connector" on page 34 and "Device Interface Mating Connector" on page 39 for information. Table 3-1 lists the five break-out boards and the test type for which each is intended.

Table 3-1 Break-Out Board List

Break-Out Board	Test Type	Comment
Single Ended I/O Dual 20 Pin	Single-ended	0.1 inch spaced header This connector is commonly used for Agilent logic analyzer probe connections.
Differential I/O Dual 38 Pin	Differential	This connector is commonly used for Agilent logic analyzer probe connections.
Loop Back Test Single Ended IO Dual 40 Pin	Single-ended	0.1 inch spaced header This board serves a dual purpose: • as a break-out board for DUT connectivity • used for N5102A module diagnostic testing
Single Ended I/O 68 Pin	Single-ended	Single SCSI-style connector
Differential I/O Dual 100 Pin	Differential	This connector is commonly used for Agilent logic analyzer probe connections.

The mating connectors for the break-out boards are readily available from suppliers external to Agilent Technologies and are listed in Table 3-2 along with the connectors already mounted on the boards.

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Table 3-2 Connector Part Numbers and Manufacturers

Connector Type	Break-out Board Output Connector Manufacturer Part Number	Mating Connector Manufacturer Part Number	Manufacturer
20-Pin	2520-6002UB	3421-6700 (wire connector)	3M
38-Pin Mictor	2-767004-2	767006-1 (board connector)	Tyco Electronics
40-Pin	2540-6002UB	3417-6700 (wire connector)	3M
68-Pin D-Subminiature	787170-7	749621-7 (wire connector)	Tyco Electronics
100-Pin Samtec	ASP-65067-01	ASP-65267-02 (wire connector)	Samtec

Table 3-3 Clock, Marker, and Trigger Zero-ohm Resistor Installation

Board Type	Data Clock	Marker 1 and Trigger	Marker 2
63003 Dual 40 Pin	No zero-ohm resistors are required		
63004 Dual 20 Pin	J2–2 <–> C2	J1–2 <–> C12	J2–1 <–> D2
63005 Mictor	J2–1 <–> C2 (pos. clock) J2–2 <–> C1 (neg. clock)	J1–1 <–> C12 J1–2 <–> C11	N/A
63006 Samtec	No zero-ohm resistors are required		
63007 SCSI	J1–66 <–> C2	Not available	

Dual 20-Pin Break-Out Board

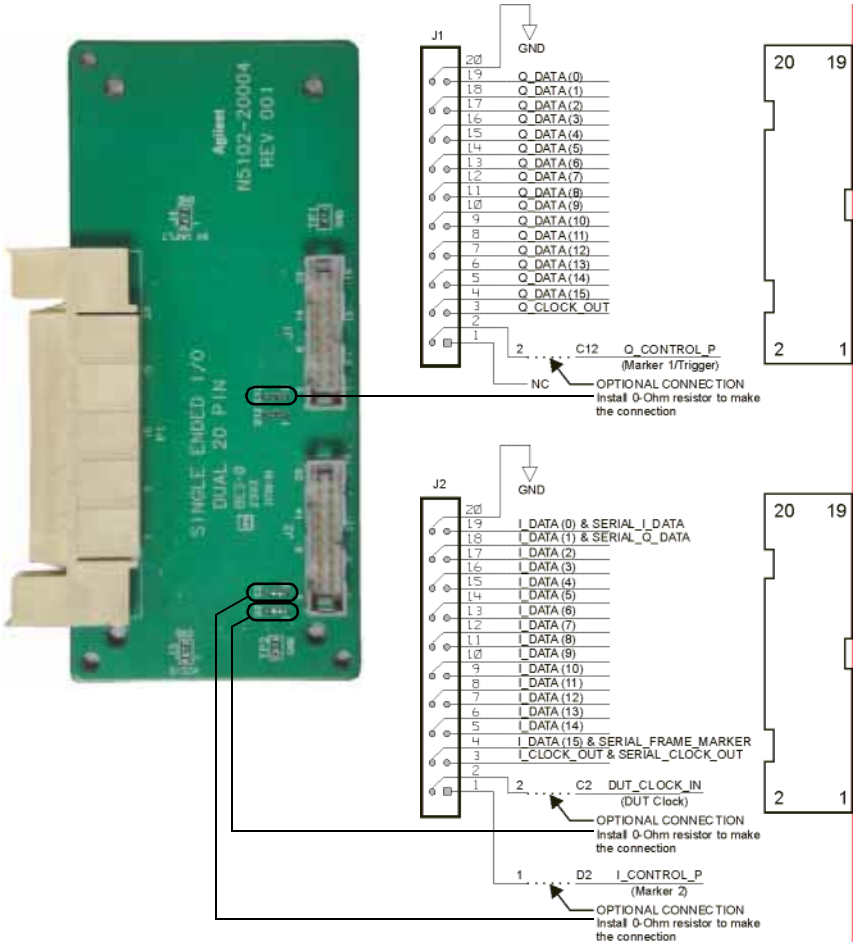
Use this break-out board when single-ended testing is required and there are minimal connection points. It is most suitable at lower sample rates using easily constructed ribbon cables. Figure 3-1 shows this board along with the pin-out diagram for the connectors. The 20-pin connectors are a common 0.1 inch spaced header. Notice that the parallel I and Q signals are separated by connectors; J1 provides the Q signals and J2 provides the I signals. The serial signals are also provided on the J2 connector.

To enable the use of clock, trigger, marker 1, or marker 2 signals with this break-out board, zero-ohm resistors must be installed between the contacts for the desired signals, as shown in Figure 3-1. Refer to Table 3-3 on page 25 for a list of required connections.

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The VCCIO (selected high logic level voltage) is obtained at J3, while the + 5 volt unfiltered DC supply is acquired at J4.

Figure 3-1 Dual 20-Pin 0.1 Spaced Connector



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Dual 38-Pin Break-Out Board

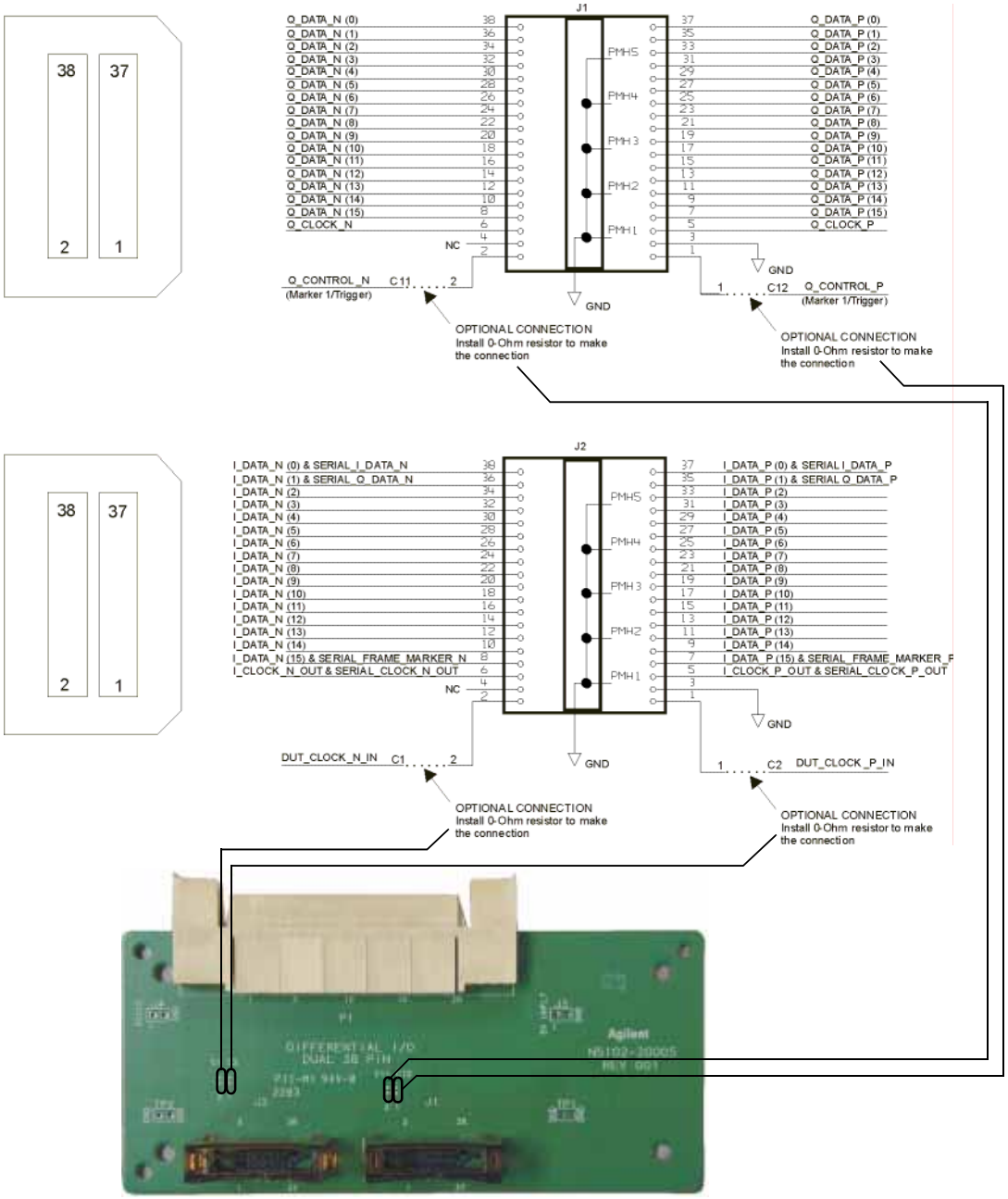
This board is intended for differential testing, however it can also be used for single-ended signals. For single-ended signals, the data is transmitted on the positive lines. Figure 3-2 shows this break-out board along with the pin-out diagram for the connectors. Notice that the parallel I and Q signals are separated by connectors; J1 provides the Q signals and J2 provides the I signals. The serial signals are also provided on the J2 connector.

To enable the use of clock (positive or negative), trigger, marker 1, or marker 2 signals with this break-out board, zero-ohm resistors must be installed between the contacts for the desired signals, as shown in Figure 3-2. Refer to Table 3-3 on page 25 for a list of required connections.

The VCCIO (selected high logic level voltage) is obtained at J4, while the + 5 volt unfiltered DC supply is acquired at J3.

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Figure 3-2 **Dual 38-Pin Mictor Connector**



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Dual 40 Pin Break-Out Board

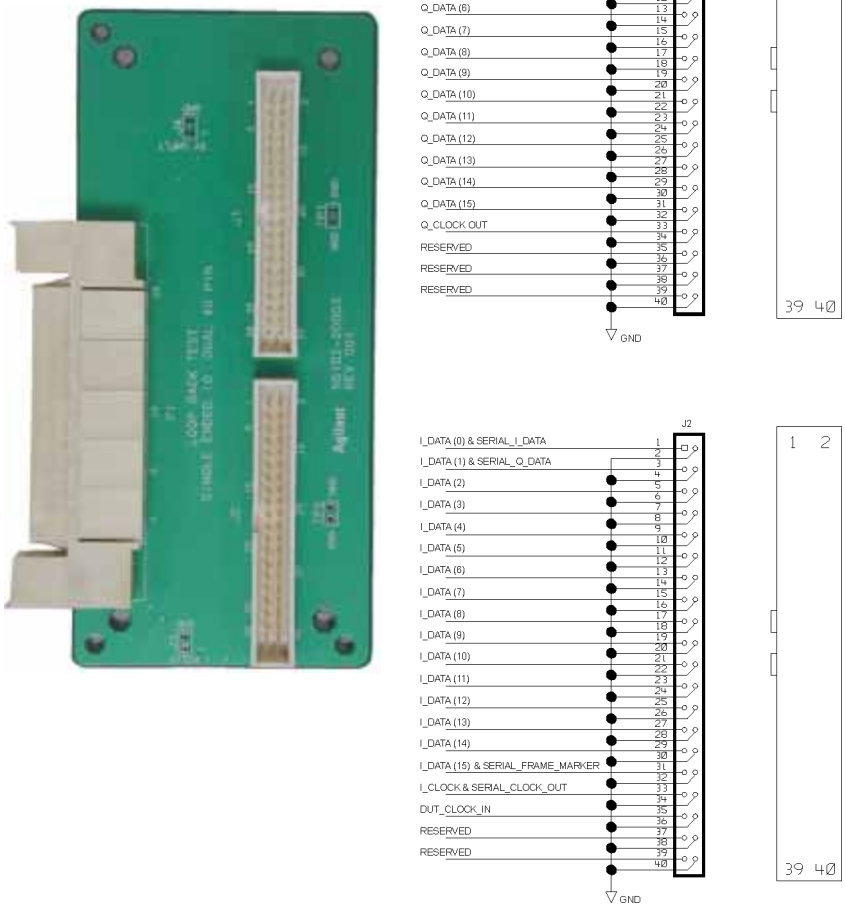
This break-out board is useful for higher rate single-ended signals that benefit from a ground associated with each signal line. The 40-pin connectors are a common 0.1 inch spaced header. Figure 3-3 shows this break-out board along with the pin-out diagram for the connectors. Notice that the parallel I and Q signals are separated by connectors; J1 provides the Q signals and J2 provides the I signals. The serial signals are also provided on the J2 connector.

This board serves a dual function, one as a break-out board simplifying the connectivity of the device under test and the other as a loop back test board when performing N5102A module diagnostic tests.

The VCCIO (selected high logic level voltage) is obtained at J3, while the + 5 volt unfiltered DC supply is acquired at J4.

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Figure 3-3 Dual 40 Pin 0.1 Spaced Header Connectors



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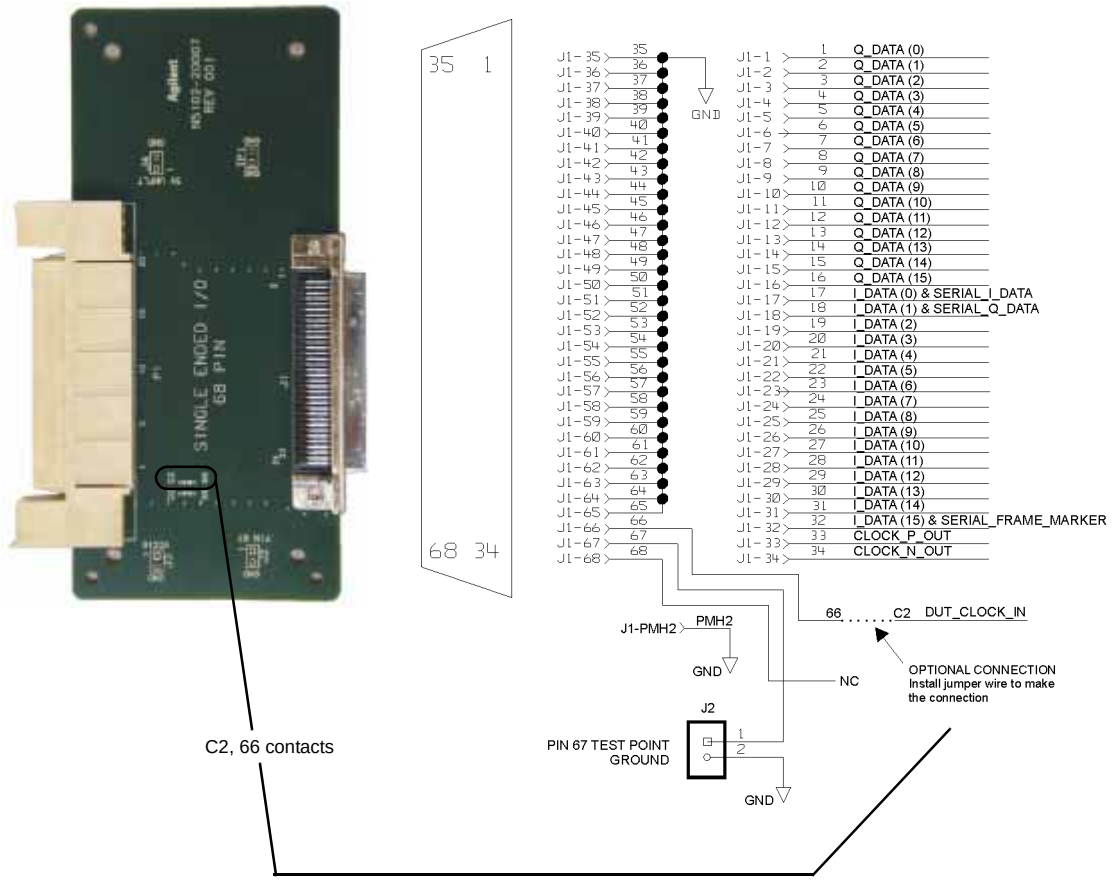
Single 68-Pin SCSI Style Break-Out Board

This break-out board is intended for single-ended testing. The connector is a SCSI style interface that is compatible with some existing products that provide a digital data output. The serial signals are transmitted on the I data lines. Figure 3-4 shows this break-out board along with the pin-out diagram for the output connector.

For the N5102A module to receive a clock through the Device Interface connector by way of this break-out board, a zero-ohm resistor must be installed between the 66 to C2 contacts. This is shown in Figure 3-4.

The VCCIO (selected high logic level voltage) is obtained at J3, while the + 5 volt unfiltered DC supply is acquired at J4.

Figure 3-4 Single 68-Pin D-Subminiature SCSI Style Connector



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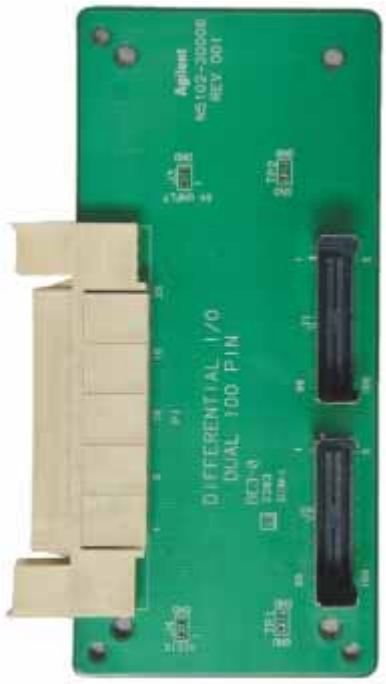
Dual 100-Pin Break-Out Board

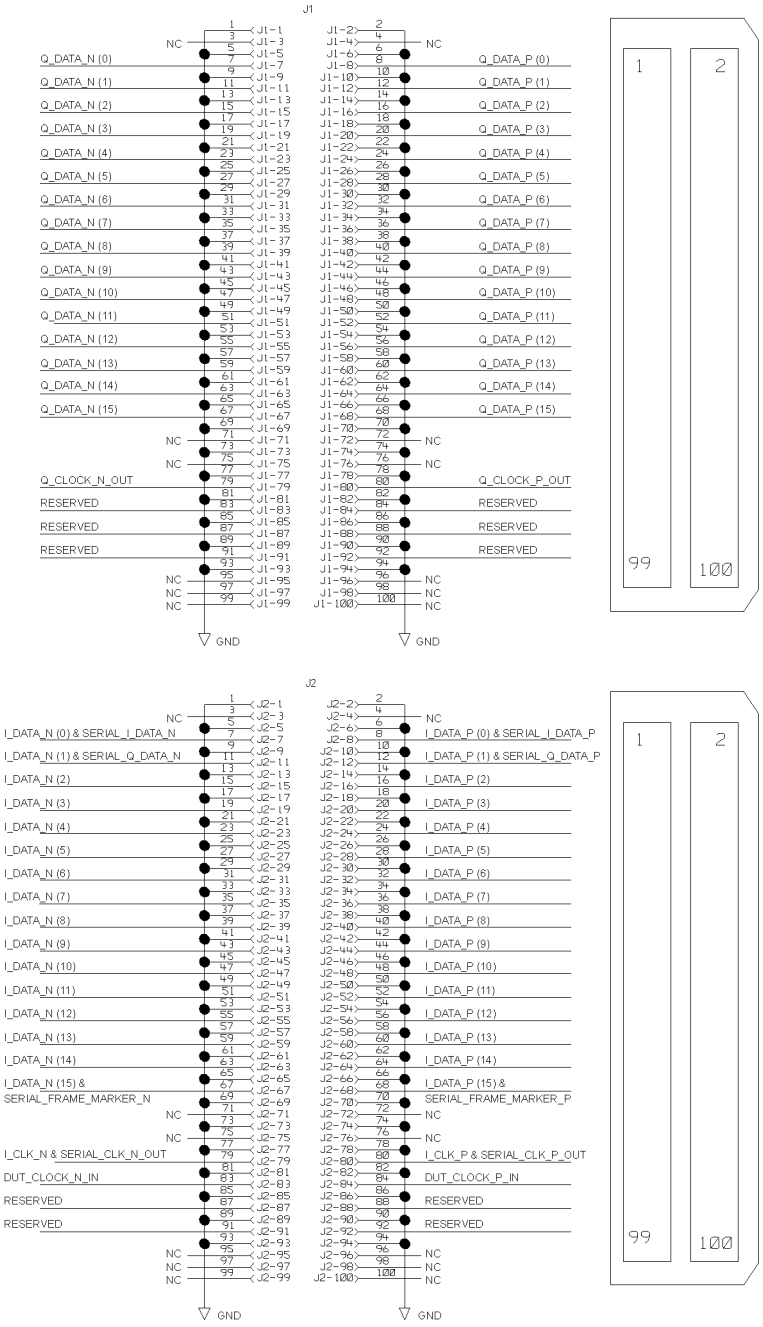
This break-out board is intended for differential testing, however it can also be used for single-ended signals. For single-ended signals, the data is transmitted on the positive lines. Figure 3-5 shows this break-out board along with the pin-out diagram for the connectors. Notice that the parallel I and Q signals are separated by connectors; J1 provides the Q signals and J2 provides the I signals. Serial signals are also provided on the J2 connector.

The VCCIO (selected high logic level voltage) is obtained at J4, while the + 5 volt unfiltered DC supply is acquired at J3.

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Figure 3-5 Dual 100-Pin Samtec Connector



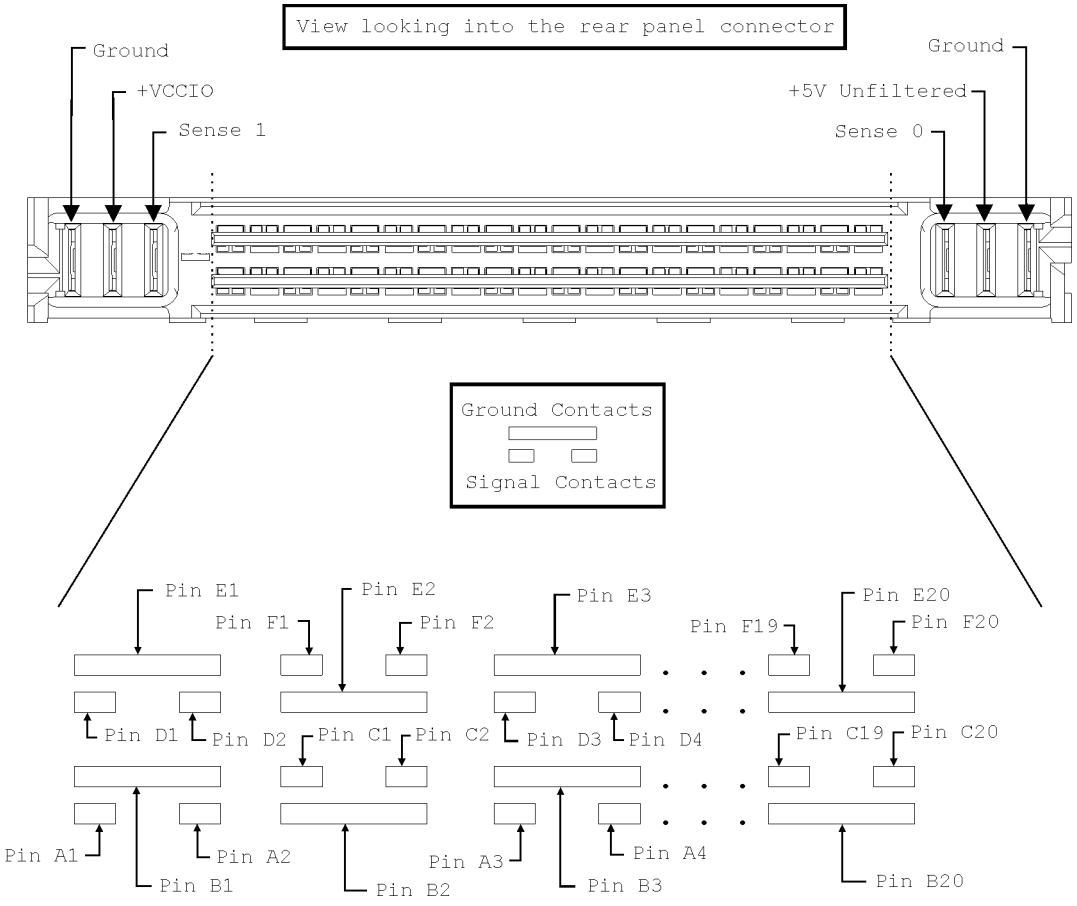


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Device Interface Connector

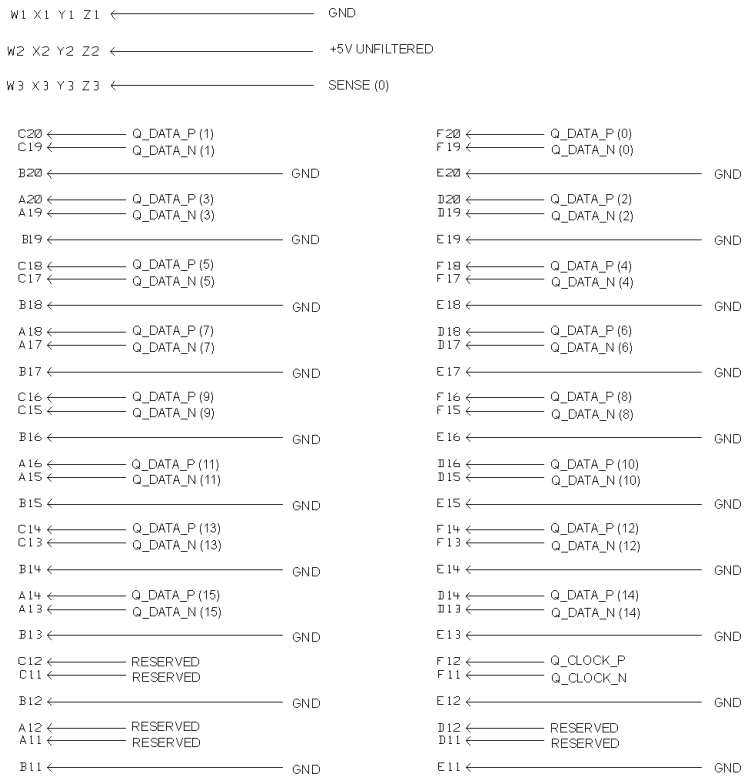
The figures and information shown in this section will assist when customizing a connection solution for the device under test using the device interface mating connector (see "Device Interface Mating Connector" on page 39). The signal contact layout for the Device Interface connector is shown in Figure 3-6 and the connector pin-out is shown in Figure 3-7 and Figure 3-8.

Figure 3-6 Device Interface Connector Layout



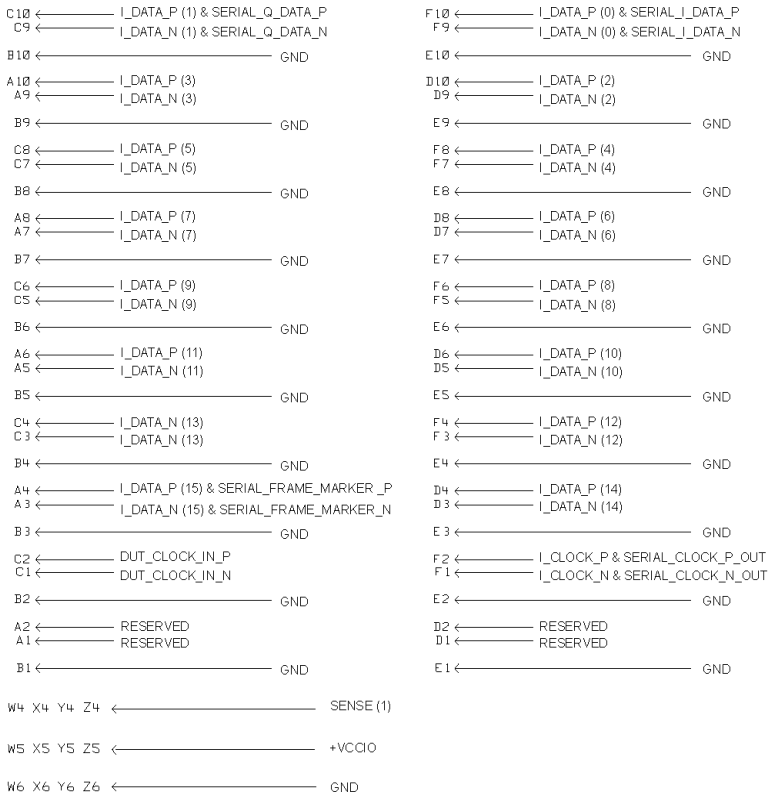
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Figure 3-7 Device Interface Connector Pin-Out



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Figure 3-8 Device Interface Connector Pin-Out (continued)



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Input and Output Clock Signals

There are multiple output clock lines and two input clock lines to handle differential clocking. The N5102A module can be configured to accept the device under test clock through the Device Interface connector for data clocking. Using the input clock signal from the Device Interface connector is an alternative to using a clock signal applied to the Clock In connector. Table 3-4 lists the Device Interface connector pins for the different clock signals and the serial frame marker.

Table 3-4 Clock Signal and Serial Frame Marker Lines

Clock Signal Type	Pin	Clock Signal Type	Pin
Output Q-Clock Neg	F11	Output Q-Clock Pos	F12
Output I-Clock Neg	F1	Output I-Clock Pos	F2
Output Serial Clock Neg	F1	Output Serial Clock Pos	F2
Output Serial Frame Marker Pos	A3	Output Serial Frame Marker Neg	A4
Input Clock Signal (DUT Clock) Neg	C1	Input Clock Signal (DUT Clock) Pos	C2

Data Lines

There are 64 data lines on the Device Interface connector that allow for either differential or single-ended signals. These 64 data lines consist of 32-I lines (16 positive and 16 negative), and 32-Q lines (16 positive and 16 negative). Single-ended signals are routed on the positive data lines. Table 3-5 shows which data lines are used for a given signal.

Table 3-5 Data Lines

Signal	Serial Data		Parallel Data[1]	
	I	Q	I	Q
Differential	Positive and negative lines: F9 & F10	Positive and negative lines: C9 & C10	Positive and negative lines 0–16 (A3–A10, C3–C10 D3–D10, F3–F10)	Positive and negative lines 0–16 (A13–A20, C13–C20, D13–D20, F13–F20)
Single-Ended	F10	C10	Positive lines 0–16 (A4, A6, A8, A10, C4, C6, C8, C10, D4, D6, D8, D10, F4, F6, F8, F10)	Positive lines 0–16 (A14, A16, A18, A20, C14, C16, C18, C20, D14, D16, D18, D20, F14, F16, F18, F20)

1. Parallel interleaving (IQ and QI) occurs on the I data lines.

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DC Supply

Referring to Figure 3-6, notice that the interface module provides an unfiltered +5 volts DC supply through the Device Interface connector. This DC supply provides up to 100 mA and has a self-resettable fuse. Use this DC current to bias components on the device under test where the noise will not compromise test results.

VCCIO

The Device Interface connector also provides a connection for the VCCIO that can be measured at a test point on each break-out board. The VCCIO amplitude is equal to the high voltage level of the selected logic type.

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Device Interface Mating Connector

A mating connector for the Device Interface port is supplied to make the device under test connection easier when none of the break-out boards offer a connection solution for the device.

There are two ways to use the mating connector. One is to attach wires directly to the pins providing a quick connection solution. The other is to make a PC board with a footprint that matches the connector mounting pins. Figure 3-9 shows the layout of the signal contacts while looking directly into the connector and the pin footprint while viewing the connector from the bottom. Figure 3-10 shows the connector footprint for a PC board.

The signal pin-out for the connector can be obtained from Figure 3-7 on page 35 and Figure 3-8 on page 36. These figures display the pin-out diagrams for the N5102A module Device Interface connector.

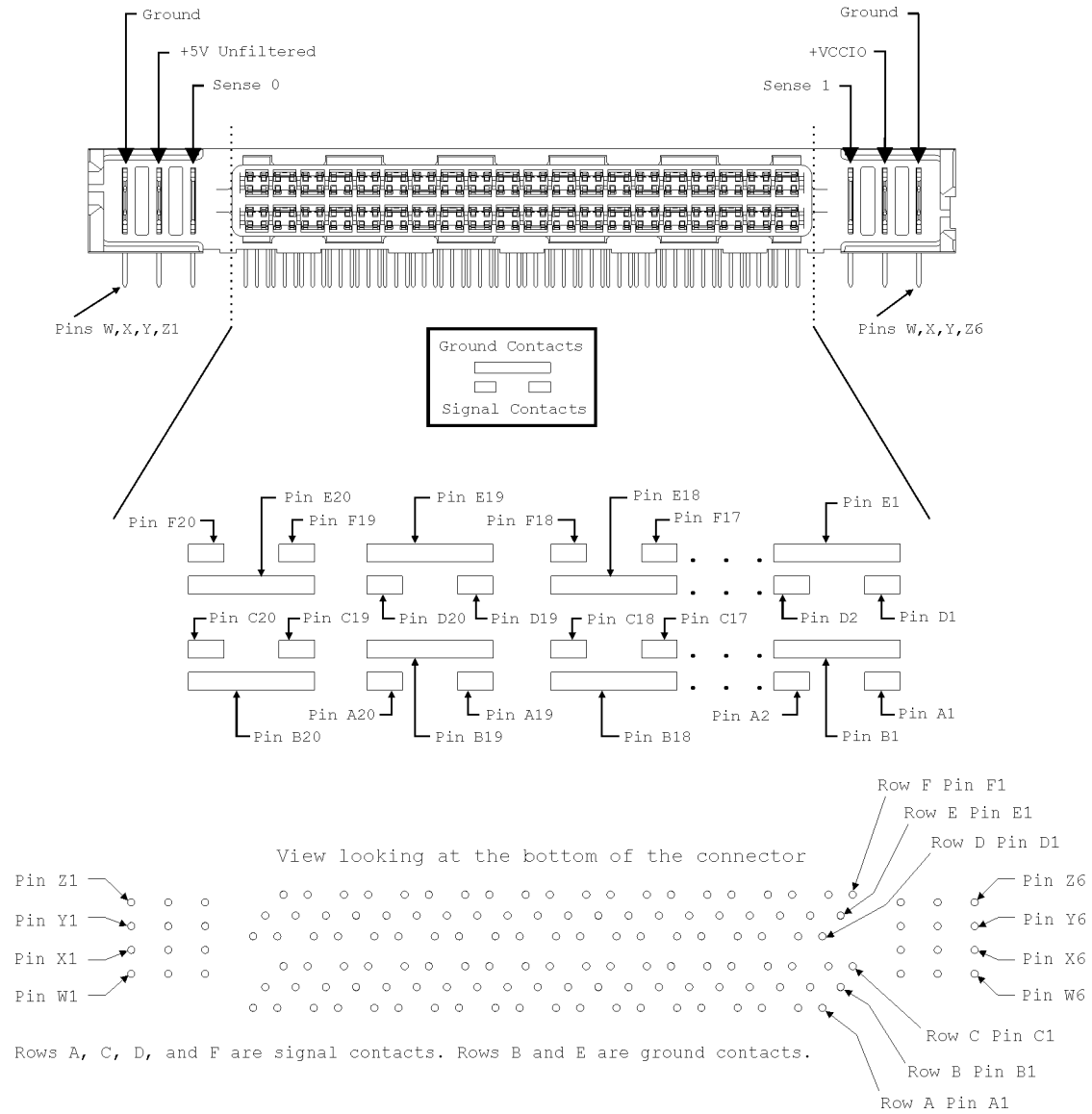
Table 3-6 provides the manufacturer and the part numbers for the Device Interface connector and its mate. Both connectors are available from suppliers external to Agilent Technologies.

Table 3-6 Device Interface Connector Manufacturer and Part Numbers

Connector Type	Connector Manufacturer Part Number	Mating Connector Manufacturer Part Number	Manufacturer
144-Pin Z-Dok+	1367550-5	1367555-2 (board connector)	Tyco Electronics

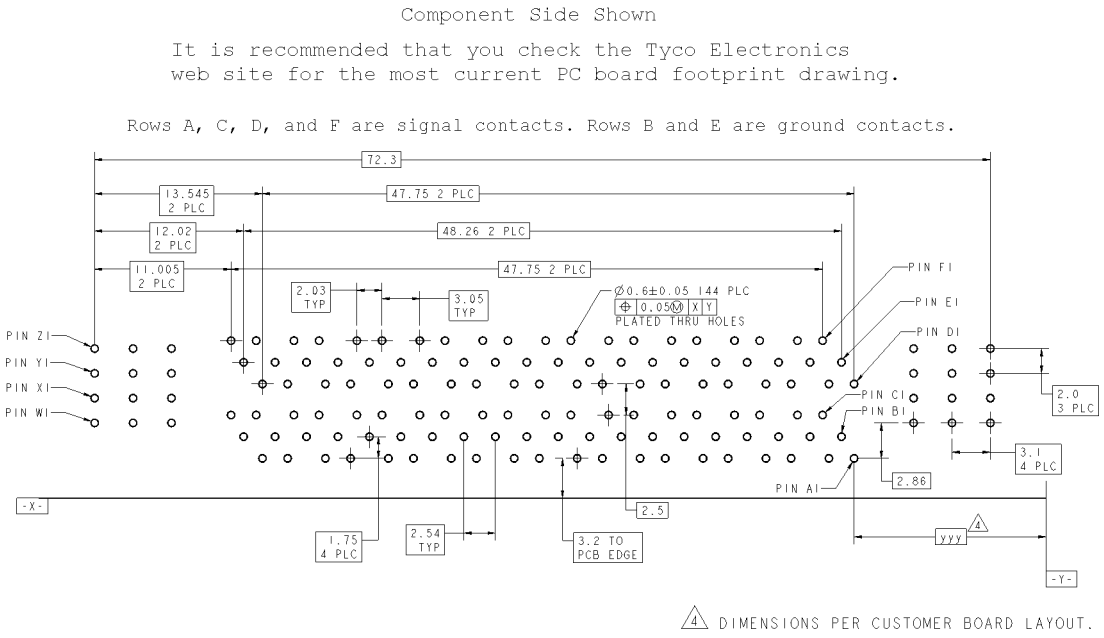
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Figure 3-9 Z-Dok+ Device Interface Mating Connector Layout and Footprint



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Figure 3-10 Z-Dok+ Device Interface Mating Connector PC Board Foot Print



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4 Troubleshooting

This chapter provides the following information to assist you in troubleshooting the N5102A Baseband Studio digital signal interface module:

- "If You Encounter a Problem" on page 44
- "Replaceable Parts" on page 50
- "Returning an N5102A Module to Agilent Technologies" on page 51

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If You Encounter a Problem

CAUTION Immediately unplug the N5102A module from the AC power line if the unit shows any of the following symptoms:

- Smoke, arcing, or unusual noise from inside the unit.
- A circuit breaker or fuse on the main AC power line opens.

These potentially serious faults must be corrected before proceeding.

When connected to the ESG/PSG and the signal generator displays an error, read the error message text by pressing **Utility** > **Error Info**. Resolve any problems specific to the signal generator (refer to the signal generator's documentation).

When connected to a PCI card, read the controlling software's error message and resolve any problems identified in the message. Refer to the software's online help for more information.

If the N5102A module is not operating properly, refer to the following table to begin troubleshooting.

Symptom	Action
Power LED is off	Go to "Checking Power Problems" on page 45
Fails "Operation Verification" on page 11	Go to "Running Diagnostic Tests" on page 46
ESG/PSG or controlling software has a persistent N5102A module error. A module error persists even after you fix the problem described in the ESG/PSG error message and clear the error queue: **Utility** > **Error Info** > **Clear Error Queue(s)**	Reset the module (disconnect the power supply from the digital module and then reconnect it).

NOTE If new firmware is downloaded to the ESG/PSG while the digital module is connected, you must power cycle the digital module to restore normal operation. Disconnect the power supply from the digital module and then reconnect it. This will preset the digital module.

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Checking Power Problems

When you connect the power supply to the module, the green Power LED should light.

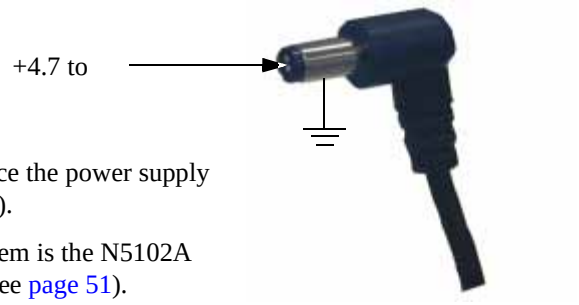


If the Power LED does not light:

1. Check the power cord; ensure that it is:
   - in good condition
   - properly plugged in to a live outlet (line power connection is described on page 6)
   - properly connected to the power supply (power supply connection is described on page 10) and the DC power supply plug is fully inserted into the N5102A module DC power receptacle

   If this does not solve the problem, go to step 2 to check the power supply.

2. Using a DVM, check the power supply output.



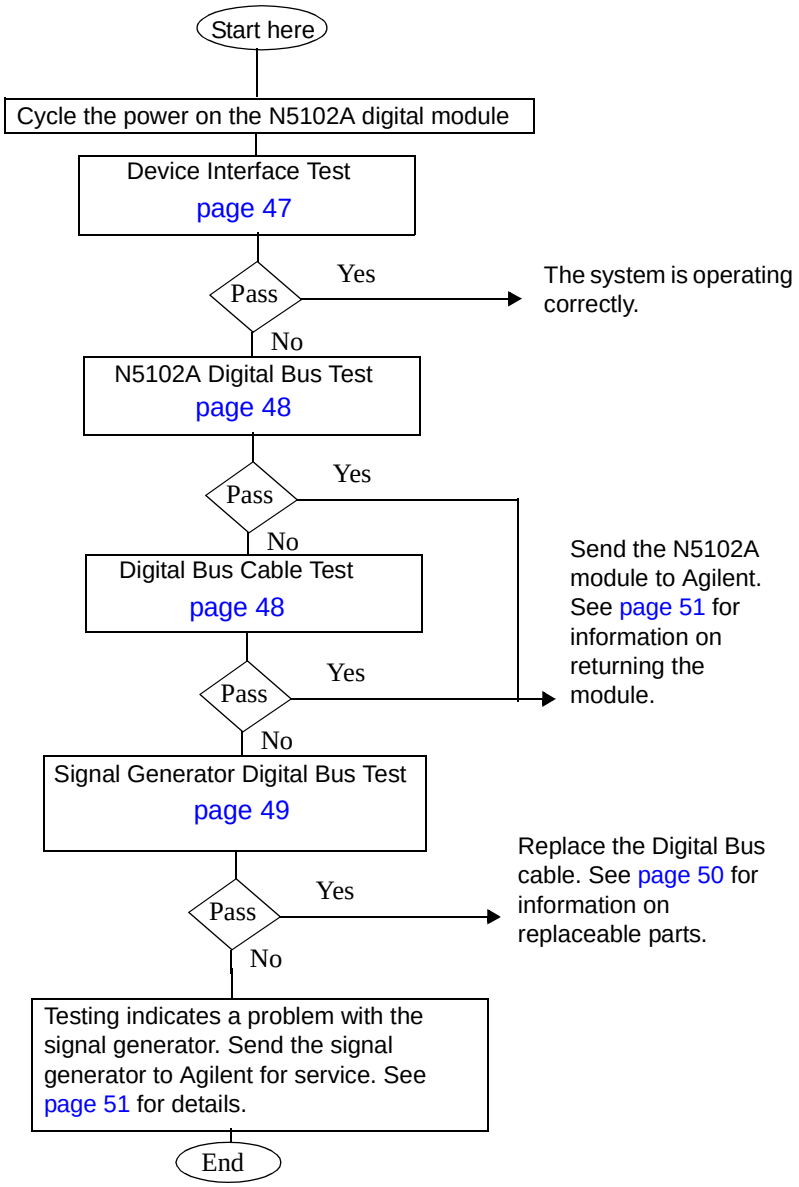
   If the output voltage is not correct, replace the power supply (refer to "Replaceable Parts" on page 50).

   If the output voltage is correct, the problem is the N5102A module. Return the module to Agilent (see page 51).

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Running Diagnostic Tests

Diagnostic tests, referred to as loop back tests, are provided to help isolate problems when connected to the ESG/PSG. Perform the tests, in the order listed, in the following flow chart. When connected to a PCI card, use the controlling software's self tests wizard to diagnose the problem.



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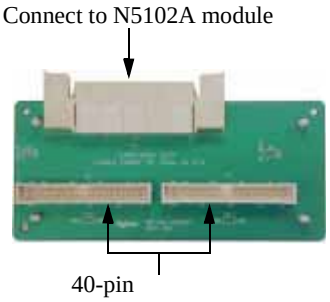
Device Interface (System) Test

This is a comprehensive test that checks the paths from the signal generator Digital Bus connector to the Device Interface connector on the N5102A module. This is the same test used in the section, "Operation Verification" on page 11, which describes the test in more detail.

1. Connect the N5102A module to the signal generator according to the steps in "Connecting the N5102A Module to the ESG/PSG or N5101A PCI Card" on page 8.

2. Connect the Loop Back Test Single Ended IO Dual 40 Pin board, shown at right, to the Device Interface connector on the rear of the N5102A module.

   Ensure that there are no connections to the two 40-pin connectors.



3. Select and run the Device Interface test:

   a. On the signal generator, press
   Aux Fctn > **N5102A Interface** > **Diagnostics** > **Loop Back Test Type** > **Device Intfc**.

   Note that the test selection in parentheses below the **Loop Back Test Type** softkey updates to reflect the current test.

   b. Press **Run Loop Back Test**.

   Because all signal generator modulation formats and the N5102A module interface must be off before a loop back test can run, if they are active when you press the **Run Loop Back Test** softkey, they turn off automatically.

Results:

Pass	The system is operating correctly.
Fail	Examine all connectors (they should be clean and undamaged) and connections (they must be secure). If the test still fails, perform the N5102A Digital Bus test to help isolate the problem.

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N5102A Digital Bus Test

This test checks the communication path from the Digital Bus connector on the signal generator to the input of the N5102A module. It does not require the use of a loop back test board.

1. If not already done, disconnect the Loop Back Test Single Ended IO Dual 40 Pin board from the N5102A module.

   Leave all other connections.

2. Select and run the N5102A Digital Bus test:

   On the signal generator, press **Loop Back Test Type** > **N5102A Dig Bus** > **Run Loop Back Test**.

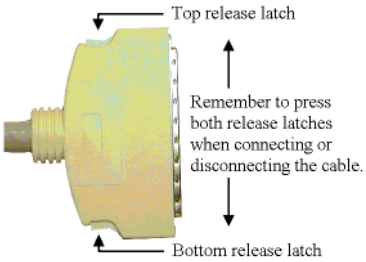
Results:

Pass	The N5102A module has a problem, send the module to Agilent for service (see page 51).
Fail	Perform the Digital Bus Cable test to further isolate the problem.

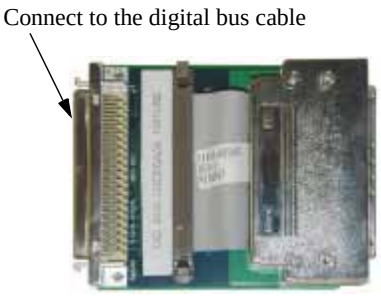
Digital Bus Cable Test

This test checks the communication path from the Digital Bus connector on the signal generator to the end of the digital bus cable. It requires the use of the digital bus loop back fixture.

1. Disconnect the digital bus cable from the N5102A module.



2. Check the connectors on the digital bus loop back fixture, shown at right, to ensure that they are clean and undamaged, then connect it securely to the digital bus cable in place of the module.



3. Select and run the Digital Bus Cable test:

   On the signal generator, press **Loop Back Test Type** > **Dig Bus Cable** > **Run Loop Back Test**.

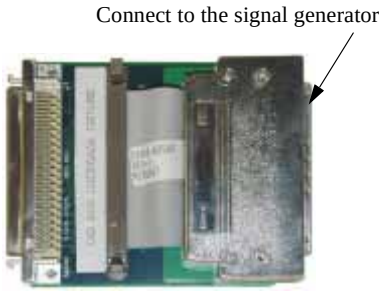
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Results:

Pass	The N5102A module has a problem, send the module to Agilent for service (see page 51).
Fail	Perform the Signal Generator Digital Bus test to further isolate the problem.

Signal Generator Digital Bus Test

This test checks the digital output capability of the signal generator and requires the use of the loop back fixture, shown at right.



1. Disconnect the digital bus loop back fixture from the digital bus cable.
2. Disconnect the digital bus cable from the signal generator, and connect the digital bus loop back fixture securely in its place.
3. Select and run the Signal Generator Digital Bus test

   On the signal generator, press **Loop Back Test Type** > **SigGen Dig Bus** > **Run Loop Back Test**.

Results:

Pass	The problem is with the digital bus cable; replace the cable (see "Replaceable Parts" on page 50).
Fail	The problem is with the signal generator; send the signal generator to Agilent for service (see page 51).

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Replaceable Parts

Contact Agilent (see Table 4-1) for price and availability of the following parts.

Table 4-1 Replaceable Parts

Description	Part Number	Description	Part Number
Digital Bus Cable	N5101-60003	Power Cord	
Digital Bus Loop Back Fixture	E4400-63583	United Kingdom	8120-8709
Loop Back Test Single Ended IO Dual 40 Pin Board	N5102-63003	Australia and New Zealand	8120-0696
Single Ended I/O Dual 20 Pin Board	N5102-63004	Continental Europe	8120-1692
Differential I/O 38 Pin Board	N5102-63005	United States and Canada, 120V	8120-1521
Differential I/O Dual 100 Pin Board	N5102-63006	Switzerland	8120-2296
Single Ended I/O 68 Pin Board	N5102-63007	Denmark	8120-2957
N5102A Installation Guide	N5102-90003	South Africa and India	8120-4600
Power Supply, AC-DC 5V 4A	0950-4540	Japan	8120-4754
		Israel	8120-5181
		Argentina	8120-6868
		Chile	8120-6979
		China	8120-8377
		Brazil and Thailand	8120-8671

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Returning an N5102A Module to Agilent Technologies

To return your N5102A digital signal interface module to Agilent Technologies for servicing, follow these steps:

1. Gather as much information as possible regarding the module's problem.
2. Call the phone number listed on the Internet (*http://www.agilent.com/find/assist*) that is specific to your geographic location. If you do not have access to the Internet, contact your Agilent field engineer.

   After sharing information regarding the module and its condition, you will receive information regarding where to ship your module for repair.
3. Ship the module in the original factory packaging materials, if available, or use similar packaging to properly protect the module.

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