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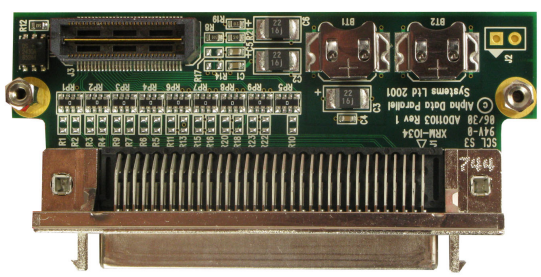
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Find the *Alpha Data ADM-XRC-II* at our website: ***Click HERE***



Summary

The **XRM-IO34** is an I/O Module which provides up to 34 LVDS/LVTTL I/O via the SCSI-2 68pin connector.The XRM also provides battery support to provide a back-up supply for encryption key storage on the connected FPGA board.

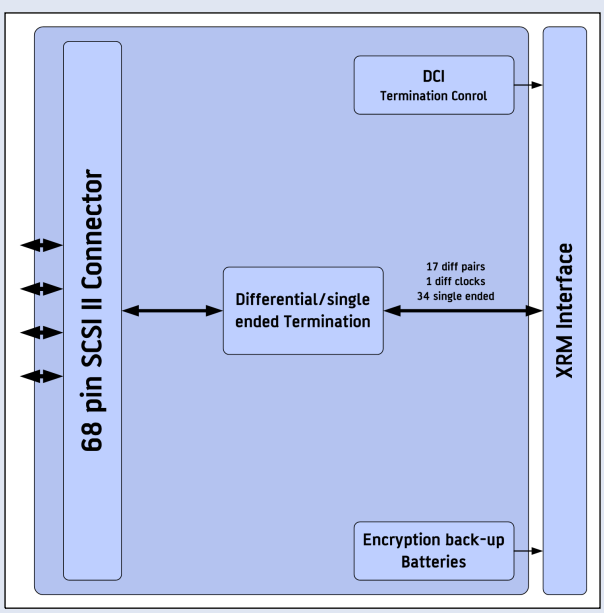
Features

Applications:

General purpose I/O Interface

Front Connector I/O:

34 discrete digital I/Os LVDS/LVTTL



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Specification	
Product Name	XRM-IO34
Front I/O	**Digital I/O:** - 34 discrete digital I/Os LVDS/LVTTL levels=LVDS or LVTTL connector=SCSI-2 68-pin
Software	Example UCF, HDL files and Application software are provided with the board.
Battery	The XRM provides holders for two back-up batteries. Uses SR60 or equivalent batteries.
Environmental	**Temperature:** Operating Temperature 0° to +55°C **EMC:** FCC 47CFR Part 2 EN55022 Equipment Class B

Ordering Codes		
XRM(xver)-IO34		
XRM Version	xver	blank=Original XRM (FPGA products up to Virtex-5), 2=XRM Version 2 (FPGA products Virtex-6 and later)

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