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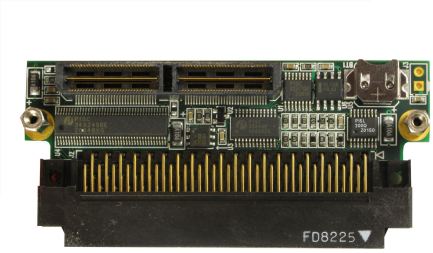
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XRM2-FPDP

Datasheet Revision: 2.0
1st November 2020

AD01107



Prime Application

Parallel FPDP Interface

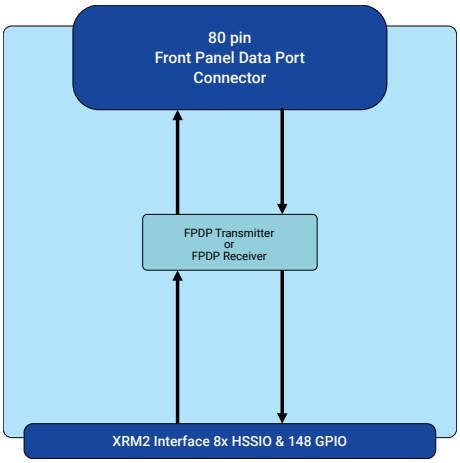
Board Features

- FPDP 80-pin Connector

Summary

The **XRM2-FPDP** is an XRM2 I/O Module which provides a Front Panel Data Port Interface.

Enables the FPGA board to communicate with external devices which support the FPDP protocol.



Deliverables

XRM2-FPDP Board
One Year Warranty
One Year Technical Support

Board Format

Alpha Data XRM2 I/O Module

Input/Output Interfaces

Parallel FPDP Interface
Parallel FPDP Interface - 32-bit parallel synchronous bus

Environmental Specification

Cooling Option	Operating Temperatures		Storage Temperatures	
	Min	Max	Min	Max
AC0	0°C	+55°C	-40°C	+85°C

Operating Humidity

Up to 95% (non-condensing)

EMC Standards

FCC 47CFR Part 2
EN55022:2010 Equipment ClassB

Ordering Information

Order Code: XRM2-FPDP

www.alpha-data.com

UK Office
Address: Suite L4A, 160 Dundee Street, Edinburgh, EH11 1DQ, UK
Telephone: +44 131 558 2600
Fax: +44 131 558 2700
email: sales@alpha-data.com

USA Office
10822 West Toller Drive, Suite 250
Littleton, CO 80127
(303) 954 8768
(866) 820 9956 - toll free
sales@alpha-data.com

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