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Find the *Alpha Data XRM-HSSDC2B* at our website: *Click HERE*

XRM-HSSDC2

2nd April 2009 **Datasheet Revision: 1.0**

Summary

The **XRM-HSSDC2** is an I/O Module which provides 4 HSSDC2 high speed serial connectors.Allows the Hig Speed Serial IO on the FPGA board to drive signals through HSSDC2 connectors to external equipment.

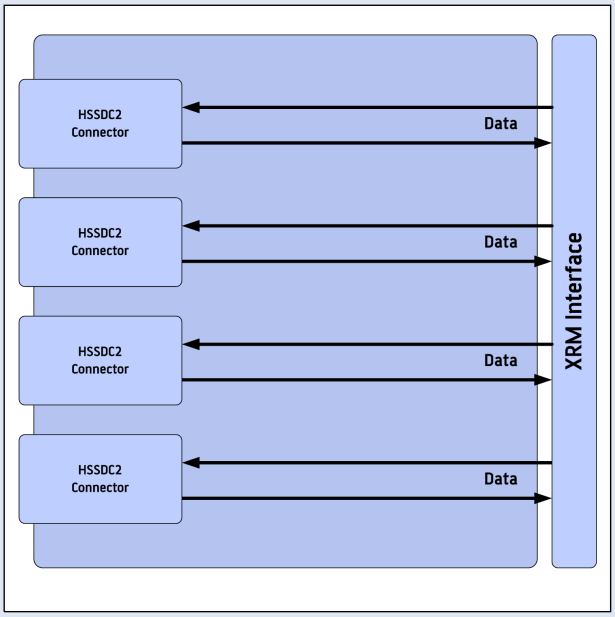
Features

Applications:

High Speed Serial Communications

Front Connector I/O:

Quad High Speed serial links (up to 2.5Gbps)



XRM-HSSDC2

Specification	
Product Name	XRM-HSSDC2
Front I/O	**High Speed Serial Link:** - Quad High Speed serial links (up to 2.5Gbps) connector=HSSDC2 fmax=2.5Gbps
Software	No Software Required
Battery	The XRM provides holders for two back-up batteries. Uses SR60 or equivalent batteries.
Environmental	**Temperature:** Operating Temperature 0 to +50 degrees C Non-Operating(Storage) Temperature -40 to +65 degrees C Operating Humidity 5% to 90% at 40 degrees C non-condensing Non-Operating(Storage) Humidity 5% to 90% at 40 degrees C non-condensing **EMC:** FCC 47CFR Part 2 EN55022 Equipment Class B

Ordering Codes
XRM-HSSDC2

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