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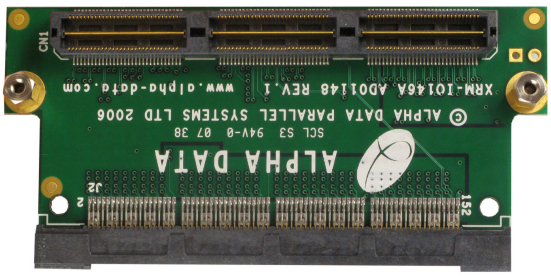
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Summary

The **XRM-IO146** is an I/O Module which provides up to 146 LVDS/LVTTL I/O via the Mictor 152pin connector.The XRM also provides battery support to provide a back-up supply for encryption key storage on the connected FPGA board.

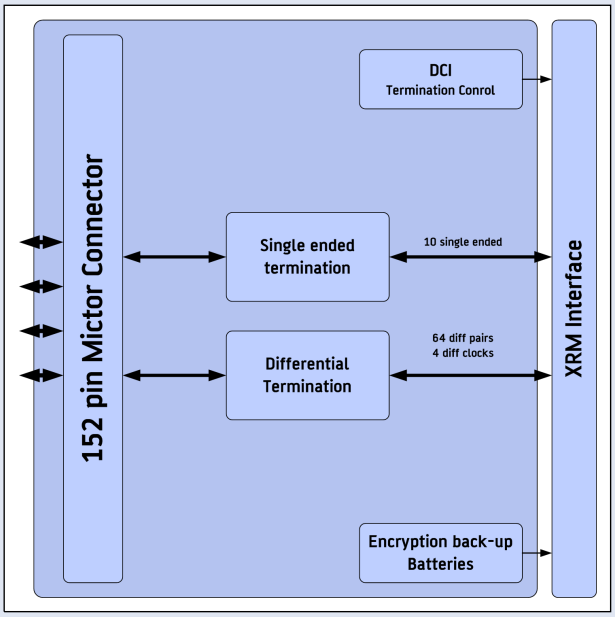
Features

Applications:

General purpose I/O Interface

Front Connector I/O:

146 discrete digital I/Os LVDS/LVTTL



XRM-IO146

Specification	
Product Name	XRM-IO146
Front I/O	**Digital I/O:** 146 discrete digital I/Os LVDS/LVTTL levels = LVDS or LVTTL I/O connector = Mictor 152 pin 64 differential pairs, 10 single ended signals and 4 differential clocks
XRM2	The XRM-IO146 is also available for XRM2 based FPGA products.
Software	Example UCF, HDL files and Application software are provided with the board.
Battery	The XRM provides holders for two back-up batteries. Uses SR60 or equivalent batteries.
Environmental	**Temperature:** Operating Temperature 0° to +55°C **EMC:** FCC 47CFR Part 2 EN55022 Equipment Class B

Ordering Codes		
XRM(xver)-IO146(B)(D)(LOCK)(EXTRA)		
XRM Version	xver	blank=Original XRM (FPGA products up to Virtex-5), 2=XRM Version 2 (FPGA products Virtex-6 and later)
For Virtex-4 products	B	blank=Virtex-5 and later products, A=For Virtex-4 products
DCI Enabled for Virtex-4 and earlier products	D	blank=Not enabled, -G=DCI enabled for Virtex-4 and earlier products
Custom Modes	EXTRA	blank=No other options, -[code]=Other build options for custom products
Cable Lock	LOCK	blank=No locking mechanism, -LOCK=Cable locking assembly fitted
NOTE	#	When using with a Virtex-4 LX or SX based board order the XRM-IO146A. Check with ??mailto:sales@alpha-data.com~Sales?? for further information.
NOTE	#	When using with a Virtex-6 based board order the XRM2-IO146.
NOTE	#	If the XRM-IO146 is to be used to supply 5V indicate this in your order so the appropriate components can be fitted.

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