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Find the *Applied Microsystems CodeICE 960 Hx* at our website: ***Click HERE***

CodeICE* 960 for i960® RP Processor

- Real-Time, Transparent Emulation at Full Processor Speed
- Cross-trigger PCI Bus and Logic Analysis With CodeICE Features
- 26 Hardware Access, 18 Hardware Execution, and Thousands of Software Breakpoints
- Performance-Plus Tools For Enhancing and Optimizing Product Performance
- CPU Browser*: Graphical Internal Register Configuration and Control Tool
- Integrates External Signals From Target Hardware into CodeICE Systems
- Seamless Networking Support For Workstations and PCs
- Supports Intel, MRI and GNU Compilers



The CodeICE* 960 RP emulator uses the latest in innovative emulation technology to deliver a rich feature set and keep costs down at the same time. Applied's CodeICE emulators are also designed to broaden the traditional range of emulator activity to span the development cycle from concept through maintenance and support.

The CodeICE 960 RP includes support for the i960® processor-specific features such as Big-Endian byte ordering, caching, different access modes and bus widths. The emulator operates from a Sun SPARC or HP workstation, and from a PC-hosted network. It can also be accessed from a PC through a high-speed parallel interface. Drawing upon technology developed in support of the 960 J-series processors, update packages will make it easy to configure the CodeICE as an RP or J-series tool.

By focusing on processor architecture, CodeICE 960 RP delivers unmatched productivity enhancing capabilities such as cross-triggering capabilities for PCI bus and logic analysis; CPU Browser*, a graphical interface for configuration, display and modification of the states of internal processor registers; IDP/Dynamic-Mode, for servicing target interrupts while emulation is paused and dynamic access of emulator features without stopping emulation; RTOS-Link*, for integrating support of industry-leading real-time operating systems with CodeICE; and Performance Plus tools, offering convenient profiling and code coverage support for use with Intel's optimizing C compilers, integrated performance analysis and programmable time-stamping. Applied's unique source-level tracing capability displays which source lines were executed and correlated values while executing out of cache.

Included with CodeICE 960 RP is the MWX-ICE C/C++ debugger environment. MWX-ICE lets you graphically organize your approach to development so you can work more easily and efficiently. You can display source code, synchronized bus-level and disassembled trace, processor registers and other information in a multi-windowed environment customized to suit you. MWX-ICE and CodeICE can run standalone or integrated with the MasterWorks environment from Microtec Research.

HOST SYSTEMS SUPPORTED:
PC, Sun SPARC, HP 9000/700

PROCESSORS SUPPORTED:
i960 RP Processor

AVAILABILITY:
Q1 '96

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For International contacts see Appendix B.

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