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Multi-ICE® Interface Unit

Summary of Features

Multi-ICE® Interface Unit is a JTAG emulator for ARM-based system on chip devices.

ARM's popular JTAG-based debug system, Multi-ICE® provides unparalleled depth and breadth of support for all ARM® core-based devices which contain the Embedded-ICE™ logic. ARM develops Multi-ICE support for all new ARM cores during the design and validation phase, ensuring that Multi-ICE supports all the features of the new cores. JTAG emulation allows the core to be started and stopped under control of the connected debugger software. The user can then examine and modify registers and memory, and set breakpoints and watchpoints. Multi-ICE also provides support for code download, trace and [RealMonitor™](#) for real-time debug.




The Multi-ICE unit strengthens the features and usability of the ARM tool chain with:

Fast download 130KB/s on a 400 MHz PII	Debug of very low voltage cores (down to 1V)
Stepping speeds greater than 50/second	Debug of slow (sleep mode) or variable frequency designs
Debug of multiple ARM and mixed architecture devices	Code download to Flash memory
Support for all current ARM cores, including the ARM7™, ARM9™, ARM9E™, ARM10™ families and Intel® XScale™ microarchitecture.	Host console I/O services (such as printf) without stopping the processor
	Attach to an already running target without halting

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