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ARM

THE ARCHITECTURE FOR THE DIGITAL WORLD™

MultiTrace

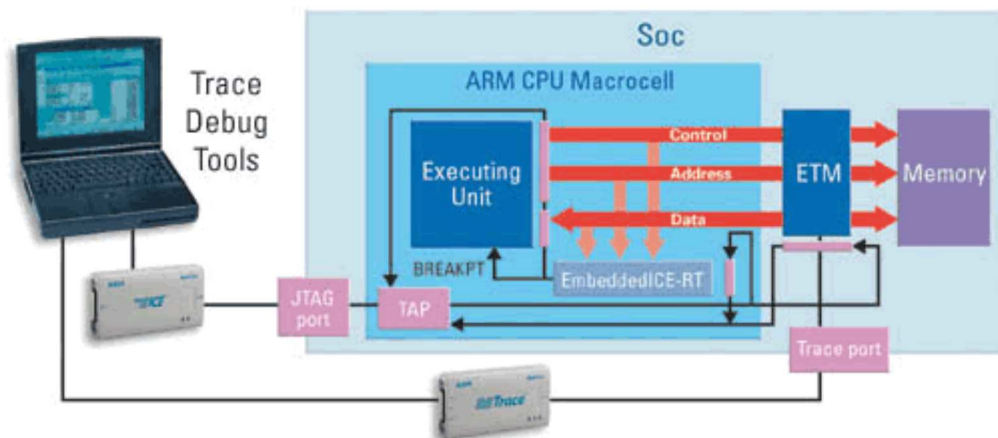
MultiTrace is ARM's Trace Port Analyzer. It passively collects information from ARM core-based system-on-chips (SoC) containing an Embedded Trace Macrocell (ETM). The ETM monitors the ARM instruction and data buses at full core speeds, using the MultiTrace analyzer to buffer the collected information before transmission to the Trace Debug Tools.

MultiTrace is part of ARM's Real-Time Debug solution that provides comprehensive real-time tracing and debugging facilities in highly integrated SoC designs, allowing developers to quickly locate difficult bugs thus reducing time to market and development costs.



Features

- Acts as the external collection unit for the ARM Real-Time Debug solution
- Passively collects trace information at clock speeds up to 200MHz
- High speed upload to [Trace Debug Tools](#) via Ethernet 10/100 Base T
- Works in conjunction with [Multi-ICE](#) to provide real-time trace functionality
- Compatible with the [Embedded Trace Macrocells](#) (ETM) for ARM7, ARM9 and ARM10 cores
- Supports SoCs from 1.0V to 3.3V
- Provides double- or single-edged clocking capability
- Supports data port widths of 4-, 8- and 16-bit (9, 13 and 21 pins)
- Time stamps trace information: 48-bit with 10 ns resolution
- Requires [ADS v1.1](#), [TDT v1.1](#) and [Multi-ICE v2.0](#)



Hot Plugging

The MultiTrace unit is power resilient and will not crash or damage the target application when hot plugged. This is a particularly useful feature for debugging embedded control system ICs in electro-mechanical applications such as automotive, where powering down and restarting is time-consuming.



Trace Buffer

The table shows the maximum depth of the trace buffer when configured for different trace port widths, for any processor clock frequency up to 200MHz.

Trace buffer size and frequency table

Trace depth (maximum)	Trace port width
1M records	4 bits
512k records	8 bits
512k records	16 bits

Double-Edged Clocking

MultiTrace can be configured to clock trace data on both edges of the clock signal. This halves the frequency requirements for the chip I/O design, where a half rate clock can be provided as the trace clock and still trace the processor activity at full speed.

Trace Buffer

Real-Time Trace

ARM's Real-Time Trace solution puts real-time components of in-circuit emulation (ICE) into the SoC device.

This technology suite includes:

- Multi-ICE
- Embedded Trace Macrocell (ETM)
- MultiTrace
- Trace Debug Tools



Real-time systems need to be debugged at full speed with the facility to view a history of microprocessor activity around an event and change memory variables on the fly. A debug solution for real-time systems should provide data for analysis and profiling tools - important in safety-critical applications and for optimization of code performance. These elements provide the capability to non-intrusively trace instructions and data accesses at core speeds - helping to track down bugs that only occur when there is an unforeseen and unpredictable interaction between the application and the hardware.

Key Features:

- Runs at full microprocessor speed
- 100% non-intrusive tracing
- Standard tools for all SoCs
- Includes emulation functionality on-chip



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