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FC7501, FC7502 | PCI SERCOS II

sercos the automation bus

The SERCOS II PCI Fieldbus Cards from Beckhoff allow direct access to the SERCON816-ASIC. The driver for these passive cards is incorporated into the TwinCAT software and allows optimum access to the SERCOS interface. There are no artificial limitations with regard to the number of bus devices and I/O data per device. The power of TwinCAT comes into its own with this interface generation:

- up to 254 devices (axes, I/O modules, e.g. Bus Terminals with Bus Coupler BK7500)
- any assignment and length of the I/O data
- exact synchronisation between TwinCAT and SERCOS
- cycle times from 62.5 µs are possible
- synchronous communication of process data
- Master mode can be parameterised by software.
- It is possible to select two parallel fieldbus channels on one card (2 x SERCON816).
- Synchronisation of both channels and other cards along with PC is possible.
- Upload of network configuration is supported.

TwinCAT I/O provides configuration tools and drivers for different Windows versions for programs in any desired high level language (DLLs) and for Visual Basic applications (ActiveX). Applications with OPC interfaces can access the cards via an OPC server.

Technical data	FC7501	FC7502
Fieldbus	SERCOS II	
Number of fieldbus channels	1	2
Data transfer rates	2, 4, 8, 16 Mbaud	
Interface to the PC	plug-and-play PCI interface 32 bit, direct access to DPRAM and SERCON816 register	
Bus interface	2 x connector FSMA according to IEC 874-2	4 x connector FSMA according to IEC 874-2
Synchronisation	synchronisation of several cards via ribbon cable	
Bus device	≤ 254	
Cycle time	all cycle times supported by SERCOS interface (down to 62.5 µs)	
Hardware diagnosis	1 LED per channel	
Dimensions	approx. 95 mm x 120 mm	
Operating temperature	0…+55 °C	

Ordering information	FC7501-0000	FC7502-0000
FC750x-0000	standard configuration	

Accessories	
TX1100	I/O driver
Cordsets	cordsets and connectors

System	
SERCOS	For further SERCOS products please see the system overview.

We reserve the right to make technical changes.

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