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BiRa Systems

MODEL 2601

24-BIT INPUT AND OUTPUT REGISTER

FEATURES

- 24-bit Input Register/Output with handshaking
- LAM Status Bits for Interrupt Driven Systems
- Internal Loopback for testing
- TTL Inputs or relay closure
- Optional outputs TTL, Open-collector

APPLICATIONS

- General purpose input/output Register
- Device Interface
- Power Supply Interface
- Input and Output Data

GENERAL DESCRIPTION

This unit is a single-width module which contains one 24-bit input register and one 24-bit output register. The unit may be strapped for high true or low true inputs and outputs. Outputs are optional open-collector or totem pole. The unit has handshake signals for both input and output data and LAM's are set to indicate acceptance and ready for additional data. The IN and OUT connectors are either type 2DB52P or 50 pin D.

INPUT REGISTER

The input register is loaded by an external device and read by CAMAC using dataway read lines R1-R24. An externally supplied LOAD DATA strobe latches the input data into the input register and LAM (IN) is set approximately lμs after the leading edge of the strobe. Strapping is provided to allow data to be latched by issuing an F0 A0 command. In this mode, it is assumed that input data is present and the desired data is presented on the read lines during the following F0 A0. A 0.5μs DATA ACCEPTED (IN) signal is returned in response to the LOAD DATA Stobe.

OUTPUT REGISTER

The output register is loaded by CAMAC using dataway write lines W1-W24. Once the register is loaded using an F16 A1 comand a READY flag is raised to indicate data availability at the front panel. An external device connected to the OUT connector must recognize the flag and return a DATA ACCEPTED (OUT) signal. DATA ACCEPTED (OUT) removes READY and sets LAM (OUT) signifying that a new word may be output. Each LAM has a separate LAM ENABLE. LAM's are logically "ORED" to drive the front panel "L" indicator and the dataway LAM line.

Self diagnostics may be performed by enabling internal loopback. In this mode, the contents of the output register are read back via the input register.

In IN and OUT connectors are type 2DB52P or 50 pin "D". Input register signals each have a 1K pull up register in order to accomodate TTL, open collector or relay closure drivers; negative excursions are diode limited. The outputs are optional open-collector or totem pole outputs. The unit may be strapped for high-true or low-true inputs and outputs. If the unit is strapped for program mode then the unit may be placed in either mode under program control. In program mode Z or C sets the unit for low-true input and output.

FUNCTION CODES:

COMMAND	Q	ACTION
F0 A0 RD1	0	Read (IN) register, clear (IN) LAM.
F8 A0 TLM	1	Test (IN) LAM request.
F8 A1 TLM	1	Test (OUT) LAM request.
F8 A15 TLM	1	Test if either LAM request.
F9 A0 CL1	0	Clears (IN) register.
F9 A1 CL1	0	clears (OUT) register.
F10 A0 CLM	0	Clears (IN) LAM status.
F10 A1 CLM	0	Clears (OUT) LAM status.
F16 A1 WT1	0	Writes (OUT) register, clears (OUT) LAM.
F24 A0 DIS	0	Disables (IN) LAM request.
F24 A1 DIS	0	Disables (OUT) LAM request.
F24 A2 DIS	0	Low True mode for (IN) Data, Load Data and Data Accepted.
F24 A3 DIS	0	Low True mode for (OUT) Data, Ready and Data accepted.
F24 A15 DIS	0	Disable Internal Loopback test feature.
F26 A0 ENB	0	Enable (IN) LAM request.
F26 A1 ENB	0	Enable (OUT) LAM request.
F26 A2 ENB	0	High True mode for (IN) Data, Load Data & Data Accepted.
F26 A3 ENB	0	High True mode for (OUT) Data, Ready & Data Accepted.
F26 A15 ENB	0	Enable Internal Loopback for test feature.
F27 A0 TST	1	Test (IN) LAM status.
F27 A1 TST	1	Test (OUT) LAM status.
Z or C CZ	0	Clears both registers, LAM's status and request, disable internal loopback, and set Low True mode for both input and output signals.

POWER REQUIREMENTS:

+6 Volts	110 mA

WEIGHT:

.60Kg. (1 lb. 5oz.)

ORDERING INFORMATION

Please specify the following options when ordering:

Order Model 2601-W-X-Y-Z 24-Bit Input and Output Register.

W	Type of Front Panel Connectors	
	0	2DB52P
	1	DD50P
X	Output Register Driver Type	
	A	Totem Pole Outputs
	B	Open Collector Outputs
Y	Control Signal Driver Type	
	A	Totem Pole Outputs
	B	Open Collector Outputs
Z	Control Signal Polarity	
	A	Program Control
	B	Active High
	C	Active Low

ACCESSORIES

Mating connector for W=0 is 2DB52S connector

Mating connector for W=1 is DD50S connector

Place a Request For Quotation or Purchase Order with Bi Ra Systems

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