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- Application-Specific
  - Board-Level
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- I/O Solutions
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Blacktip-ISA

Single Processor ADSP-2106x ISA Board

PDF Datasheet | Press Release | Block Diagram | Technical Specifications | Ordering Info



Features

Two SHARC processors on the main board

One SHARC digital signal processor

Optional external SRAM up to 512K × 48

EZ-ICEŽ & ICEPAC™ in-circuit emulator compatible

SHARC-optimized high-performance BITSI I/O mezzanine site

Two SHARCNET compatible external link ports (up to 40 MB/s each)

Half-length ISA card

Complete development tools available

Description

High-Performance, Low-Cost ISA
The Blacktip board brings the high performance floating-point power of the Analog Devices ADSP-2106x SHARC DSP processor to the lowest-cost package available for the ISA-bus platform. The half-length PC-format board is outfitted with a SHARC processor, up to 512K words × 48 bits of zero-wait-state SRAM, and a BITSI I/O mezzanine site.

SHARC Processor Bus
The SHARC processor is connected to an external processor bus, giving it direct access to the optional bank of SRAM and memory-mapped I/O devices on a BITSI mezzanine.

I/O Interface
The Blacktip's BITSI I/O mezzanine interface allows the SHARC processor to exchange data with a BITSI I/O mezzanine using link ports, serial ports, and a 32-bit data bus. BittWare's large selection of BITSI mezzanines allows the SHARC to access real-world signals from a variety of I/O devices.

Link Ports and Serial Ports
Two external external link port connectors allow the SHARC processor to communicate directly with other SHARC processors within the PC or in other systems. Four additional link ports connect to the BITSI mezzanine interface for high-performance data streaming. The Blacktip SHARC processor can also access serial devices on the BITSI using its two SPORTS.

Available Software

- Analog Devices VisualDSP
- BittWare VisualDSP Target
- BittWare DSP21k-32 Toolkit
- BittWare Porting Kit

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