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TECHNOLOGY GROUP

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Artisan Technology Group
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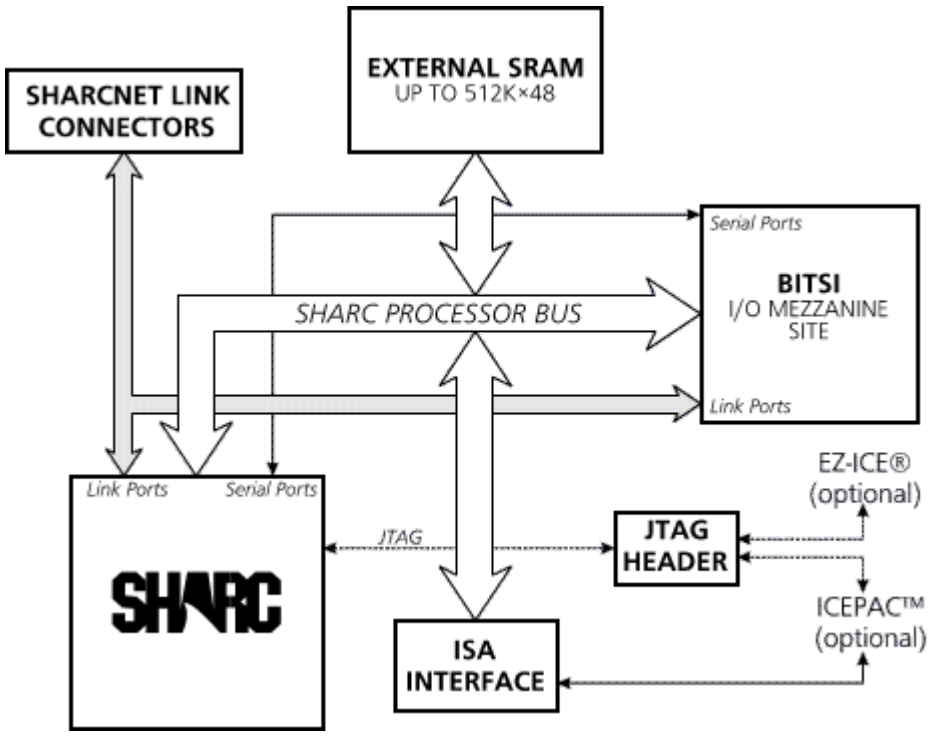
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Block Diagram



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Specifications

Processor

- One ADSP-2106x SHARC DSP running at up to 40 MHz
- External SHARC processor bus provides fast memory access and direct data transfer with BITSI

Serial Ports

- Two synchronous serial connections to the BITSI I/O interface

Link Ports

- Four links provide high-performance data paths (up to 40

ISA Interface

- 16-bit access to the SHARC's IOP registers
- Supports hardware interrupts in both directions
- Supports host-based booting of the SHARC
- Provides access to processor FLAG signals
- Provides access to the JTAG connector for integrated ICEPAC emulation

External Memory

- Optional external SRAM with zero-wait-state access

megabytes/s) to the BITSI I/O interface
- Two external link ports using SHARCNET connectors

- Available in 128K or 512K depths, 32 or 48 bits wide

Dimensions

- 7" × 4.2" (4.5" with fingers) half length, ISA-bus format card

Previous | Description | Feature Photo | Block Diagram | Ordering Info

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BittWare Research Systems
The SHARC tamers.

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Phone: **800-848-0436**
FAX: 603-226-6667

Send mail to webmaster@bittware.com with questions or comments about this web site.
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