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Board-Level
Integrated Circuits
I/O Mezzanines
Software Dev. Tools
Systems

Snaggletooth-PCI

Dual ADSP-2106x SHARC PCI Board

PDF Datasheet | Press Release | Block Diagram | Technical Specifications | Ordering Info



Description

SHARC Power on Industry-Standard PCI Bus

The Snaggletooth-PCI combines power, speed, potential, and flexibility. Its two Analog Devices ADSP-2106x SHARC processors give it the power of the world's fastest digital signal processors, while the industry-standard PCI bus adds speed and performance. A BITSI mezzanine site for off-the-shelf I/O interfaces and a **SHARCPAC** interface for adding processor and memory expansion mezzanines complete the package with amazing potential and flexibility.

Data Streaming

The two SHARC processors share a common processor bus, allowing them to access an optional bank of up to 512K×48 bits of external SRAM, I/O devices on a BITSI mezzanine, the SHARCPAC interface, and the internal SRAM of the other SHARC. Eight external link ports and four external serial ports allow external devices to communicate with the processors.

Features

Two ADSP-2106x SHARC™ processors running at 40 MHz

Up to 512K × 48 zero wait state external SRAM

SHARC-optimized high-performance BITSI I/O mezzanine site

Eight external link ports (up to 40 MB/s each)

Four external serial ports (up to 40MB/s each)

PCI interface provides direct, high-speed master/slave access to both SHARC processors and external memory

Compatible with EZ-ICEŽ in-circuit emulator

Complete development tools available

PCI Interface
The bus-mastering-capable PCI bus interface gives host computers direct access to the SHARC processors' IOP registers and their internal dual-ported SRAM, allowing the host to reset and boot the SHARCs, load program images, and examine memory. The PCI interface also has direct access to the shared bank of external SRAM and the SHARCPAC interface.

I/O Interface
Using the link ports, serial ports, and a 32-bit-wide data bus, the BITSI I/O mezzanine interface can perform optimized I/O transfers with the SHARC processors. With BittWare's large selection of BITSI I/O mezzanines, you have a variety of options to help you solve your I/O problems.

Processor and Memory Expansion
The SHARCPAC expansion site allows you to add processor or memory expansion mezzanines to the Snaggletooth-PCI. You can easily reconfigure the Snaggletooth-PCI to match your multiprocessing and memory requirements. The SHARCPAC interface's link and serial ports allow it to communicate with the Snaggletooth-PCI SHARC processors, devices on the BITSI mezzanine, and external devices.

Available Software

- Analog Devices VisualDSP
- BittWare VisualDSP Target
- BittWare DSP21k Toolkit
- BittWare Porting Kit
- BittWare SpeedDSP optimized libraries
- Real-time operating systems from: Analog Devices, OSE Systems, and Wind River Systems

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