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Quad TigerSHARC PCI board

15 June 2005, ***Computer/Embedded Technology***

BittWare's Tiger-PCI (TSPC) board features the processing power of four 250 MHz *Analog Devices* ADSP-TS101S TigerSHARC DSPs. The PCI board incorporates a 64-bit 66 MHz PCI interface and shared memory, including up to 256 MB SDRAM and 8 MB Flash. The board also features a variety of I/O interfaces, which include a PMC site and external link ports.

The four TigerSHARC DSPs share a common 83,3 MHz local bus, which also provides access to the SDRAM and SharcFIN ASIC. The TigerSHARC provides 12 GBps of internal memory bandwidth and features 6 Mbits of on-chip SRAM. It supports both fixed and floating point operation, providing 1500 MFLOPS of processing power for 32-bit floating point operations and 6 GOPS for 16-bit fixed point operations.

The SharcFIN ASIC features a comprehensive set of bus interfaces and peripherals for the ADSP-TS101S DSPs and provides a clean board-level implementation. It integrates a full 64-bit 66 MHz master PCI interface, an extensive flag and interrupt multiplexer, and a peripheral bus that interfaces the DSPs to peripherals such as the Flash memory.

I/O interfaces on the TSPC include four external link ports and a PMC site with BittWare's PMC+ extensions.

Credit(s)	
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