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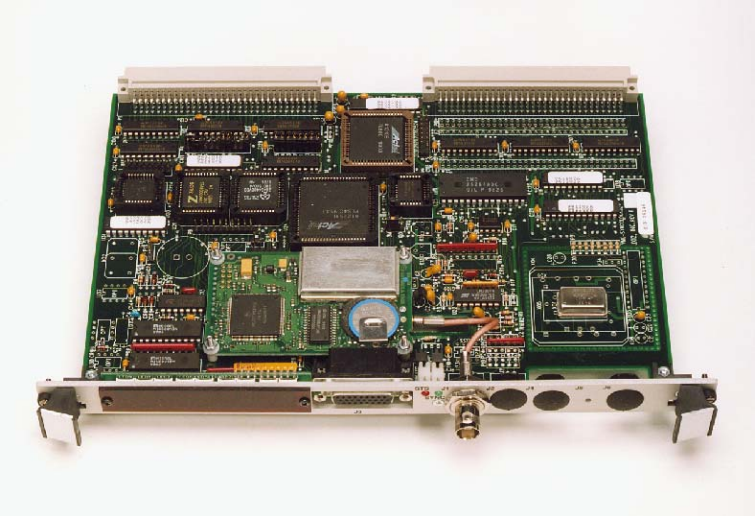
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Find the *Brandywine Communications / JXI2 VME-SyncClock32* at our website: *Click HERE*

brandywine communications

VME-SyncClock32

- Single-slot, 6U, 32 bit VME module
- IRIG A, B, NASA 36, 1 PPS sync inputs
- GPS sync option (maintains single slot)
- Have Quick sync input option
- Propagation delay correction
- Zero latency time reads
- Match Time output
- IRIG-B time code output option
- External Event time tag input
- Three user programmable pulse rates



The VME-SyncClock32 from Brandywine Communications provides precision time with zero latency to the host computer over the VME bus. An on-board microprocessor automatically synchronizes the clock to reference signal inputs. The reference signal inputs can be 1 PPS, IRIG A or B and NASA 36 time codes. GPS and Have Quick inputs are optionally available. The clock can free run and be set by commands from the host over the VME bus.

The on-board clock accepts an IRIG A, IRIG B, or NASA 36 synchronization input and user input signal delay compensation information. An optional IRIG B time code generator is also available.

The advanced microprocessor on the VME-SyncClock32 module constantly measures the time error between the on-board clock and the reference input code and adjusts the error measurement for propagation delay. In units with disciplined TCXO or OCXO oscillators the residual error is used in an adaptive gain loop to adjust the frequency of the 10 MHz oscillator for minimum error. If the incoming time code is missing or corrupted by noise the on-board clock is updated using the disciplined 10 MHz oscillator. When the input code is again useable the correction loop is smoothly closed.

58 bits of BCD time data are available to the host computer using two zero latency time reads. The time message contains units of microseconds through units of years. A status word is available using an additional read.

The time of occurrence of random external events may be captured (time-tagged) by using the Event Time input. When the event input is sensed the current time is saved in a buffer for later interrogation by the host. The resolution of the time tag is 100 nanoseconds.

Internal or external processes may be automatically initiated or terminated by using the Match Time feature. This feature asserts an output when the user input start time matches the time in the internal clock. The output is terminated under user control or when the pre-programmed stop time is encountered. The resolution of the Match Time comparison is one microsecond.

Three user programmable pulse rates are provided. Two pulse rates Clock Low and Clock High, are output at the multi-pin connector. The third programmable pulse rate generator provides heartbeat timing to the host. The divider for each of the three rate generators is programmable by the host over the range 2–65,535. The inputs to the rate generators are 3 MHz or 100 Hz for the heartbeat, 3 MHz for Clock High and 100 Hz for Clock Low.

The GPS synchronization option adds worldwide time transfer capability that can be traced to the U.S. Government standard UTC-USNO. Very precise synchronization, automatic leap year and leap second correction, and accurate position information are additional benefits provided by the GPS option.

A complete software package to support VxWorks is available. C language sample programs are supplied with the VME-SyncClock32.

In addition to the comprehensive set of standard capabilities that is offered by the Brandywine Communications VME-SyncClock32, a wide range of optional features may be specified. These options allow the user to customize the VME-SyncClock32 to fit almost any application. Most options preserve the one-slot configuration.

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VME-SyncClock32 Specifications

General Input Specifications

Input Codes	IRIG A and B, NASA 36
Input Amplitude	.25 Vpp to 10 Vpp
Input Impedance	>10k ohms
Ratio	2:1 to 4:1
Frequency Error	100 PPM maximum
Code Sync Accuracy	One microsecond
1PPS input	TTL, positive edge
1PPS Sync Accuracy	One microsecond
External Event	TTL, positive or negative edge
Resolution	100 nanoseconds–units of year
Min. event spacing	None

General Output Specifications

Propagation Delay	10 nanoseconds-999.99999 milliseconds
Match Pulse	TTL level at Start–Stop time
Resolution	Microseconds–eight milliseconds
Clock Low	TTL, negative going
Clock Divisor	2–65,535
Clock Input	100 PPS
Default Output	1 PPS
Clock High	TTL, negative going
Clock Divisor	2–65,535
Clock Input	3 MPPS
Default Output	76.923k PPS
Heartbeat Rate	Interrupt and flag TTL, negative going
Clock Divisor	2–65,535
Clock Input	100 PPS or 3 MPPS
Default Output	1k PPS
BCD Time	Microseconds–unit year on demand, zero latency, 58 bits in two 32 bit words
Status Word	Eight bits
Status LED	Flashes coded patterns
Interrupts	External Event, RAM FIFO, Heartbeart, Match Time
Flags	Dual Port RAM data ready, FIFO Data Ready, In sync, Heartbeat, Match Time External Event
Connectors	BNC, high density DB-26
MTBF	325,000 Hours Per MIL 217 F Notice 2 at 25˚ C

Mechanical & Environmental

Size	160 mm X 233 mm
Type	Single-slot 32 bit VME
Power	
+5 Vdc	±5%, 400 mA maximum
+12 Vdc	±5%, 100 mA maximum
-12 Vdc	±5%, 50 mA maximum
Operating Temperature	0˚C to +55°C
Storage Temperature	-40°C to +85°C
Humidity	To 95% without condensation

Options

IRIG B DC Shift output	TTL
GPS Sync Input	C/A code
Sync Accuracy	100 nanoseconds
Position Accuracy	25 meters SEP
Tracking	Eight parallel channels
Antenna	L1, magnetic mount, 25' cable
Antenna Options	
Hi-gain	L1, mast mount, 100' cable
Fiber Optic Kit	Fiber optic transmitter/receiver pair for long antenna cable runs
Differential GPS Inputs	Per RTCM–104
IRIG B Modulated Output	2.5 Vpp into 600 Ohms
Input Code Isolation	Transformer coupling
Input Codes	IRIG G, XR3, 2137, IRIG E, 109-60
Output codes	IRIG A, NASA 36, IRIG G
Eight External Event Inputs	TTL, positive or negative edge
Extended Temperature Range	-40° to +85°C, Contact Factory
Have Quick Output	Per ICD-GPS-060
Have Quick Input	Per ICD-GPS-060
Oscillator Upgrades	Disciplined TCXO, 1 PPM Disciplined OCXO, .01 PPM
1 PPS 10 Vdc input	Sync input, +10 Vdc, 50 Ohms
Eight External Event Inputs	Same as standard External Event
Sixteen External Event Inputs	Same as standard External Event
10 Ext. Event Inputs (FIFO)	TTL inputs, hardware FIFO buffer
16 Ext. Event Inputs (FIFO)	TTL inputs, hardware FIFO buffer
Video Time Inserter	
Software Support	VxWorks

Other Brandywine Products

- P(Y) code VME-SyncClock32
- Video Character Inserters
- Time-Message Displays
- PCI, PMC, PC/104, CPCI and ISA Computer Clock Synchronization Boards
- Network Time Servers
- Frequency Generation and Distribution Instruments
- Dual & Triple Redundant Systems

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